

# VIRTUALIZED DYNAMIC RESOURCE ALLOCATION ALGORITHM FOR THE INTERNET DIFFSERV DOMAINS

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# Abstract

The Differentiated Services (DiffServ) architecture has been proposed for providing different levels of service to the Internet Protocol (IP) traffic. Current discussions in the DiffServ networks are focused on managing resources dynamically according to the traffic conditions of the DiffServ router (Per Hop Behaviour). Software Defined Networks (SDN) and Network Function Virtualisation (NFV) technologies have recently emerged in the research agenda to support researchers in managing network domains and to achieve better use of domain resources. This thesis introduces a new scheduling algorithm called “Dynamic Resource Allocation Management - Network Function Virtualization (DRAM-NFV)” to allocate the service classes resources in the proportional delay DiffServ domains. DRAM-NFV algorithm manages the resources among service classes within the edge routers of the DiffServ domains dynamically according to their traffic conditions and manages these resources between the DiffServ domains in the event of congestion based on their traffic conditions at the egress routers of the upstream domain and ingress routers of the downstream domain. The NFV executes the DRAM-NFV algorithm on a virtualized - Network as a Service (NaaS) - cloud infrastructure to manage the SDN controllers for the edge routers of the DiffServ domains through monitoring the traffic conditions in the service classes at the edge routers and reallocating the out-link resources of the edge routers among service classes.

A number of test scenarios were conducted in this research in order to test the performance of the DRAM-NFV algorithm. The performance of DRAM-NFV algorithm is compared with the performance of the DWFQ algorithm by comparing the average End to End Delay for service classes traffic and links utilization. The DWFQ algorithm cannot manage resources between DiffServ domains but can manage the resources locally and dynamically for each DiffServ domain separately.

The network simulator NS3 has been used to implement these test scenarios and to test the performance of the DRAM-NFV algorithm. The results show that with the DRAM-NFV algorithm, better balance for DiffServ domains resources can be achieved through monitoring the bandwidth hungry service class at the downstream domain and managing its resources at the upstream domains. As a consequence of this, the utilizations of some service classes traffic are improved and the average End to End Delay for overall traffic are also reduced. An example of the improvement that was achieved by managing resources between (upstream and



downstream) DiffServ domains dynamically, in test scenario 3- Case Study 2, the average utilization for the highest priority class ( $SC_1$ ) for whole period of simulation at the destination end is increased by 0.175% and the average End to End Delay for overall traffic is also reduced by 800 msec. As a result of reducing the average End to End Delay for overall traffic and improving the utilizations of service classes traffic, the QoS of applications traffic can be improved during the congestion periods in DiffServ domains.

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On January 19, 2009, the Office of the Prime Minister of the Republic of Iraq - the Higher Committee for the Education Development in Iraq (HCED-IRAQ) launched an educational initiative to enhance the educational system of Iraq. Many scholarships were given for thousands of qualified graduate Iraqi students to study in the United Kingdom, the United State, and Australia in order to obtain masters and doctorate degrees in various scientific and literary disciplines.



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# Abbreviations

A-CPI	Application-Controller Plane Interface
AF	Assured Forwarding
APIs	Application Program Interfaces
BE	Best Effort
BPR	Backlog Proportional Rate
BW	Bandwidth
C-DPI	Controller-Data Plane Interfaces
CAPEX	Capital Expenditure
CBQ	Class Based Queuing
CPU	Control Processing Unit
DBWS	Dynamic Benefit Weight Scheduling
DDP	Delay Differentiated Parameters
DiffServ	Differentiated Services
DRAM-NFV	Dynamic Resource Allocation Management -Network Function Virtualization
DSCP	Differentiated Service Code Point
DWFQ	Dynamic Weighted Fair Queuing
EF	Expedited Forwarding
ETSI	European Telecommunications Standards Institute.
FIFO	First Input First Out
FTP	File Transfer Protocol
GCD	Greatest Common Divisor
GPS	Generalized Process Sharing
H-PFQ	Hierarchical Packet Fair Queuing
HaaS	Hardware as a Service
HPD	Hybrid Proportional Delay
I-CPI	Intermediate-Controller Plane Interface
IaaS	Infrastructure as a Service
IETF	Internet Engineering Task Force
IP	Internet Protocol
MPLS	Multi-Protocol Label Switch
NaaS	Network as a Service
NFs	Network Functions
NFV	Network Function Virtualisation
NOS	Network Operating System
NS2	Network Simulator - version 2
NS3	Network Simulator – version 3
OPEX	Operating Expenses
PaaS	Platform as a Service
PAD	Proportional Average Delay
PDD	Proportional Delay Differentiation
PHB	Per-Hop Behaviour
PQCM	Proportional Queue Control Mechanism
QoS	Quality of Service
RAM	Random Access Memory

RED	Random Early Detection
RIO	Random Early Detection with In/Out bit
SaaS	Software as a Service
SC	Service Class
SDN	Software Defined Network
TCP	Transmission Control Protocol
UDP	Unit Datagram Protocol
VLAN	Virtual Local Area Network
VMs	Virtual Machines
VoIP	Voice over Internet Protocol
WFQ	Weighted Fair Queuing
WRED	Weighted Random Early Discarding
WTP	Waiting Time Priority

# Chapter One

## Introduction

### 1.1 Introduction:

Quality of Service (QoS) is the ability of a network to provide different types of services to various identified network traffics over different underlying technologies. Bandwidth, delay, delay variation (jitter) and packet loss rate are the parameters used to measure and specify QoS (Shaikh et al. 2002). The maximum bit rate that can be sustained between two end points of a network is defined as the bandwidth of the network link. Delay is the elapsed time incurred when a packet is passed from a sender through a network domain to its destination (end-to-end delay). Jitter is the variation in end-to-end, transient delays. Finally, the packet loss rate is defined as the ratio of dropped packets to the total number of packets (Kurose & Ross 2013). The importance of Quality of Service (QoS) technologies has increased rapidly with the rapid development of multimedia applications over the Internet – such as distance learning, Video traffic, voice over Internet protocols, and so on. DiffServ is a QoS model, (Giordano et al. 2003). This model has been proposed and standardized to address the QoS requirements of real time applications (Blake et al. 1998). DiffServ provides differentiation services for aggregates of network traffic at routing decision points (Kusmierek et al. 2002).

Packet scheduling is one of the functions performed by a router and it is an important process carried out by DiffServ networks in order to allocate resources to service classes (Tanenbaum & Wetherall 2011). NFV and SDN are cutting-edge technologies in networking which co-operate in order to deploy network functions with less hardware dependence and as a software deployed on a virtualised infrastructure (Cloud) (Chiosi et al. 2012). The SDN and NFV technologies bring specific benefits with regards to efficient resource usage and faster operations management.

### 1.2 Differentiated Services:

Different types of QoS models, Differentiated Services (DiffServ) is one of them. It aims to achieves differentiation among service classes in the network through sequence of operations (Classification, Queuing or Dropping and Scheduling) based on the QoS requirements of the Internet applications and it is Per Hop Behaviour (PHB) such that each domain or router manages its resources locally depending on its traffic condition and in a different way from other domain.

Differentiated Services are beneficial in packet networks (Dovrolis 2000) because not all applications have the same QoS requirements. For example, viable Video traffic sessions necessitate lower delays and fewer losses than file transfers via FTP. With differentiation services, more demanding applications (or users) are provided with better performance than others.

The Internet Engineering Task Force (IETF) had the DiffServ architecture developed in the late nineties (Blake et al. 1998) (Nichols et al. 1998). The concept of DiffServ is that individual flows (microflows) with similar QoS requirements can be aggregated into larger traffic sets called (macroflows) or service classes. DiffServ uses a manageable, scalable and easily deployable architecture for differentiation services across IP networks. The network bandwidths in DiffServ are allocated to macroflows rather than to individual flows. All the packets in a macroflow experience the same forwarding behaviour in the routers. Each macroflow uses a specific service class queue or per-hop behaviour (PHB).

DiffServ can be provided by a set of routers forming an administrative domain called a DiffServ domain. The administrator defines a set of service classes each with corresponding forwarding rules. The classes may differ in terms of their requirements for delay, jitter, and the probability of packets being discarded in the event of congestion (Tanenbaum & Wetherall 2011). Different administrative DiffServ domains are interconnected to constitute the Internet (Reichmeyer & Networks 1998). Each DiffServ domain has core routers which are connected together in order to forward packets. The administrative DiffServ domains and end users are interconnected to each other through edge routers which lie at the boundary of the DiffServ domains (Kurose & Ross 2013). Figure 1-1 shows a typical DiffServ architecture as used in the Internet.

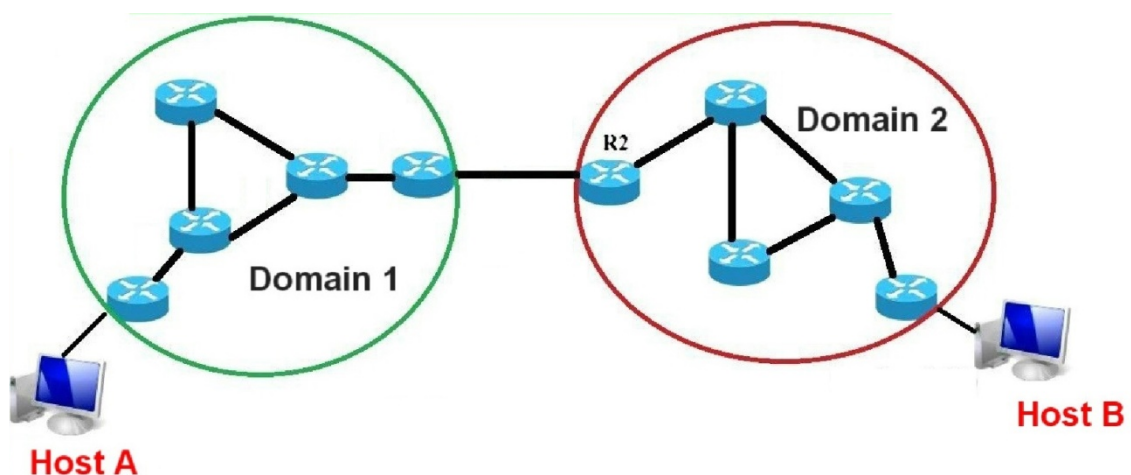


Figure 1-1, A typical DiffServ domains architecture.

When packets of a flow enter a DiffServ domain, they are assigned a code which specifies their forwarding priority (service class); this is achieved by means of a marker which is set by the edge router (Blake et al. 1998). This code is termed the packet's Differentiated Service Code Point (DSCP). The aggregation of flows is achieved at the edges of a DiffServ network. A flow classification system (Suter et al. 1999) (Srinivasan et al. 1999) is required at the network edges in order to aggregate microflows into macroflows.

The aggregation of microflows into macroflows makes the DiffServ network model scalable and manageable, and with an easily deployable architecture because a state is required only for a few service classes not for each individual flow. In addition, there is a queue for each class instead of a queue for each flow. Consequently, the per queue operations of classification, scheduling, buffer management and policing become noticeably simpler and faster.

The DiffServ architecture does not require a signalling protocol because there are no resource reservations for individual flows. The DiffServ architecture allows both absolute (quantitative) and relative (qualitative) service differentiation.

The absolute service differentiation aims to provide a macroflow with a quantitative performance level (minimum forwarding rate, or maximum loss rate) at certain links or network paths. Minimum Forwarding rate means the minimum number of packets that can be sent at certain link or network in one second while the maximum loss rate means the maximum percentage of packets lost with respect to packets sent to certain links or network. Such an absolute differentiation requires some form of admission control to prevent users from sending traffic at a higher rate than their network contract allows.

On the other hand, the relative differentiation service provides a number of classes with preferential performance. The higher classes will provide better QoS to flows than the lower classes will (Dovrolis & Ramanathan 1999) – in terms of both queuing delays and packet losses. However, the exact QoS for each class is not specified and depends on the traffic conditions and the DiffServ mechanisms that the network deploys. The relative differentiation model does not require resources reservation, signalling, fixed routing, or admission control and so it is considered simpler to manage and to deploy. The relative differentiation model is used as the differentiation model in this research [as will be illustrated in chapter 2] because the absolute DiffServ domains aim to provide quantitative performance for their service classes thus this model does not allow us to modify the configuration parameters of absolute DiffServ domains that are setup by the domain administrator. While in relative DiffServ domains; it provides qualitative performance for their service classes and allow us to amend their configuration parameters. In addition, the resources are allocated depending on traffic conditions in service

class queues and the configuration parameters of a DiffServ domain thus this will help us on managing resources between relative DiffServ domains through monitoring the traffic condition in one domain and modifying the configuration parameters of another DiffServ domain.

### **1.3 Research Problem:**

A packet in the Internet travels from source to destination by passing through one or more DiffServ domains. The problem that we have in the current approach of a DiffServ domain is that it is a static, it means that each router or domain allocates its resources locally or separately based on its traffic condition and the domain configuration parameters. Once the domain administrator sets the configuration parameters of a domain, these parameters will not change. The traffic conditions within a DiffServ domain can be changed continuously and randomly therefore there will be a situation that the DiffServ domain favoring certain amount of traffic while other network traffic will be suffered although the priority levels that we have made for service classes in a DiffServ domain so it needs to adjust the parameters at one domain to rebalance the resources at another domain.

In order to explain the research problem in more details, Figure 1-2 shows a packet which needs to be sent from host A in Domain 1 to host B in Domain 2; so the Domain 1 is considered the Upstream domain and Domain 2 is considered the Downstream domain based on traffic direction from Domain1 to Domain 2. Let us assume that this packet was marked with a high forwarding priority class when it entered Domain 1 (upstream) – it has been marked according to router strategy (traffic condition and configuration parameters) for Domain 1 or - per-hop behaviour. When the packet crosses over the boundary between two DiffServ domains, the resources at Domain 2 (downstream) are also allocated depending on the traffic condition at each edge DiffServ router in Domain 2 and configuration parameters for Domain 2; thus the forwarding probability of the packet may be altered if there aren't enough available resources for the service class to which the packet belongs at the next domain (Domain 2). That is, in DiffServ, all the service class queues of a router share one out link to forward their packets to the next hop (router or domain) and the traffic conditions within a DiffServ domain can be changed continuously and randomly so when the traffic of a certain service class or classes increases at Domain 1 (upstream), the traffic congestion could occur at the link that connects the upstream and downstream DiffServ domains and at the out links of DiffServ routers of Domain 2 for this class or these classes. This congestion causes the out links resources for Domain 2 and resources of the link that connects DiffServ domains to be mostly occupied by

the increasing or congested traffic and makes the out links' resources (queues buffer resources), allocated for other classes at the DiffServ routers in Domain 2 (downstream), not well utilized during the congestion period. So the research problem is to “How to manage resources between differentiated Services domains dynamically instead of managing resources statically for each domain in order to achieve better QoS for application traffic”. Appendix A-1 presents an experiment to illustrate the research problem.

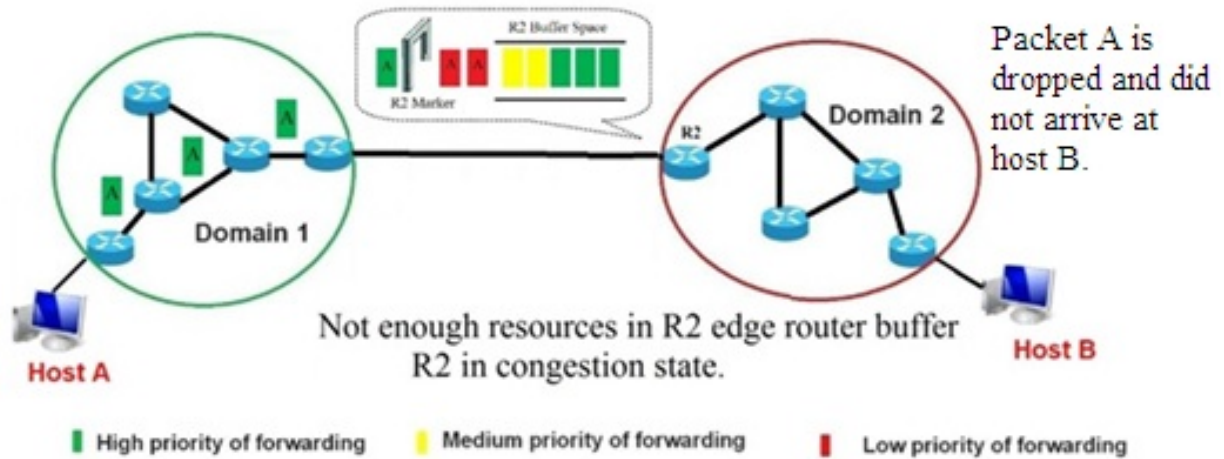


Figure 1-2, Research problem.

## 1.4 SDN and NFV Technologies:

Software Defined Networks (SDN) and Network Function Virtualization (NFV) are considered cutting-edge technologies. SDN is considered a valuable, emerging networking model that ensures that the control layer of a network device is separated from its data layer (Kate Greene 2009). In addition, SDN controllers can connect with one another and with the data layer of SDN forwarding device via Application program Interfaces (APIs) (Kreutz et al. 2015).

NFV has recently emerged and offers the implementation of network services as software executed on a virtualisation infrastructure (the Cloud). Consequently, it reduces hardware dependency (Chiosi et al. 2012). This method of implementation leads to efficient resource usage and faster operations management with lower costs because the NFV provides the infrastructure in which the application programs of the SDN controller are executed. Moreover, it (NFV) participates in the deployment of the controller's applications across multiple networks.

Although the DiffServ is Per Hop Behaviour (PHB) and it is a static approach in allocating its resources separately per each domain such that the domain administrator sets the parameters for service classes in a fixed manner and did not change it. It will be better if the DiffServ domains are managing their resources dynamically rather than statically because the network



operation (traffic condition) within a DiffServ domain can be changed continuously and randomly and therefore there will be a situation that the network favouring certain amount of network traffic. As a consequence, other network traffic will be suffered.

Instead of having a static DiffServ, we can monitor traffic condition in another domain and make changes on managing resources of other domain so we have now calibrating between DiffServ domains in managing resources. This calibration can be done by:

1. Using SDN switches, OpenFlow, for example that we can programmed them automatically and remotely. As a result of using SDN networks, we can monitor and manage the queues and congestion levels in DiffServ domains.
2. Using the concept of NFV in order to extract and implement virtually a certain function in DiffServ which is resource management function. As a result of using NFV, the management process will be deployed and do not become locally. It is still locally if the traffic condition within a DiffServ domain is normal but it becomes across DiffServ when there is unbalanced in traffic condition or congestion within a DiffServ domain.

## **1.5 Research Motivation:**

This SDN and NFV techniques have not been used, as yet, for the implementation of the management of resources across DiffServ networks. Making use of the opportunities presented by NFV and SDN, the management process could take place outside the DiffServ domain itself. It could take place in a virtualized infrastructure while NFV monitors the traffic situation of the downstream domain (the domain that the packets will travel to) and provides the allocating resource scheme to the upstream domain.

The motivation for this current work is to make effective use of the benefits offered by NFV and SDN and to study how these techniques may be used to manage resources within the edge routers of DiffServ domains and among different DiffServ domains. Within the edge routers, the resources are allocated depending on the traffic state in each service class queue and according to the differentiation patterns within the DiffServ domain. Also, the management of resources among DiffServ domains includes the monitoring of the congestion level for each service class queue at the downstream domain. When the service class congestion level exceeds the congestion threshold parameters of a service class queue, the scheduling rate for the equivalent or mapped service class queue at the egress router of the upstream domain is reduced by an optimum value. In this present study, a new scheduling algorithm is proposed, termed **“Dynamic Resource Allocation Management – Network Function Virtualization”**

(**DRAM-NFV**). This algorithm has been designed to manage the resources within and across DiffServ domains. The DRAM-NFV algorithm is based on the Dynamic Weighted Fair Queuing DWFQ algorithm which, as currently implemented/used, is not able to manage the resources across different DiffServ domains. The recently established technologies of the Cloud and Software Defined Networks (SDNs) participate in the deployment of the resources management process ‘virtually’ across DiffServ domains and within the edge routers of DiffServ domains, using the concept of Network Function Virtualization (NFV).

## **1.6 Research Aims and Objectives:**

The aim of this project is to make use of the NFV and SDN technologies in order to manage resource allocation within and among DiffServ domains so that better resource management for service class queues and greater fairness in the distribution of surplus bandwidth during the events of congestion within the DiffServ domains are achieved.

The key objectives of this research are as follows:

1. Explore a Dynamic Resource Allocation Management – Network Function Virtualization DRAM-NFV algorithm which is intended to manage the resources within and among DiffServ domains, making use of the NFV and SDN concepts.
2. Build a mathematical model that represents the DARM-NFV algorithm.
3. Implement a prototype of the DRAM-NFV algorithm and use a simulation environment to run a number of test scenarios for the validation and evaluation the DRAM-NFV algorithm.
4. Identify conditions which enable the DRAM-NFV algorithm to manage resources across DiffServ domains.

## **1.7 Research Questions:**

Throughout the course of this research, the following questions have been addressed:

1. How can the resources across different DiffServ domains be managed?
2. What viable test scenarios can be proposed to test the performance of the DRAM-NFV algorithm?

## 1.8 Research Methodology:

The approach (as shown in Figure 1-3) adopted in this research can be itemised as follows:

### 1. Review previous research:

Previous relevant works have been reviewed in order to gain a good understanding of the research field and to investigate issues related to DiffServ networks, the Cloud, SDN and NFV. The various DiffServ models and the methods of allocating resources used by the proportional delay relative differentiated service model are presented in this thesis. This review of previous work also includes an identification of the most appropriate infrastructure for implementing the NFV which is the Network as a Service (NaaS) Cloud model. In addition, many areas of research that have emerged recently in the telecommunication and computer networks field which use the cutting-edge techniques of SDN and NFV are examined.

### 2. Identify the research problem:

The research proper starts by identifying the drawbacks associated with DiffServ networks. The current proportional DiffServ model allocates resources purely depending on the traffic conditions prevalent at each edge DiffServ router and so does not enable the management of resources across different DiffServ domains. Thus, when a packet crosses the boundary between two domains, the forwarding probability of the packet may be changed, inappropriately, because there are not enough available resources, at the next domain, for the service class that the packet belong to.

In identifying the research problem, it was necessary to consider the foundation on which a new resource allocation algorithm, called the DRAM-NFV algorithm, should be developed. Such an algorithm, clearly, must be capable of dealing with the issues identified and improving the performance of a proportional delay DiffServ by managing resources within a DiffServ router and also across different DiffServ domains in the event of congestion.

### 3. Define the parameters relevant to resource allocation management:

The parameters of the DRAM-NFV algorithm must be defined so that they construct a mathematical model for the algorithm. This mathematical model has to express the behavior of the proportional delay DiffServ model when using the DRAM-NFV algorithm and was used to implement a prototype to test and evaluate the performance of the DRAM-NFV algorithm within the simulation environment ns3.

### 4. Design a framework for the system.

To make the DRAM-NFV algorithm able to manage the resources within and across DiffServ domains, a framework is required which enables the DRAM-NFV algorithm to work. Both

Cloud and SDN technologies participate in supporting the DRAM-NFV algorithm. The Network-as-a-Service NaaS Cloud which allows for the virtualization of computing and networking resources in a Cloud environment is considered as an infrastructure for the DRAM-NFV algorithm. SDN is different from traditional networking models in that the SDN controller is not bundled with the SDN forwarding device and so can be represented as a software setup on the NaaS Cloud model. Deploying and communicating between instances of SDN controllers on the Cloud enables consolidated management of the DiffServ domain routers, increases network efficiency, reduces the cost of equipment, and reduces energy consumption.

5. Simulate the designed framework and run a number of test scenarios.

To achieve the research aim, test scenarios must be implemented and presented within a simulation environment in order to test the performance of the DRAM-NFV algorithm. The scenarios must take into consideration different topologies of DiffServ domains and enable the study of the performance of the DRAM-NFV algorithm when at least some of the DiffServ domains cannot manage their own resources appropriately via the NFV technology.

6. Evaluate and validate the DRAM-NFV algorithm.

The results of implementing the test scenarios via the network simulator, ns3, must be studied to evaluate the performance of the DRAM-NFV algorithm. The performance of the DRAM-NFV algorithm is to be compared with the performance of the DWFQ algorithm under the same simulation conditions. The DWFQ algorithm cannot manage resources across DiffServ domains – it allocates the resources dynamically and separately within each DiffServ router.

7. Make any modifications necessary to improve the DRAM-NFV algorithm.

If modifications are necessary to improve the performance of the DRAM-NFV algorithm, then this step allows for such modifications (to the algorithm and to its implementation) to take place according to the evaluation of the results which are produced by the experiments carried out in step 6.

8- Draw conclusions and recommendations and disseminate results.

Conclusions and recommendations must be drawn up after the results have been finalized. Here, the results are presented for the final time so that the thesis can be written up and the completed work presented.

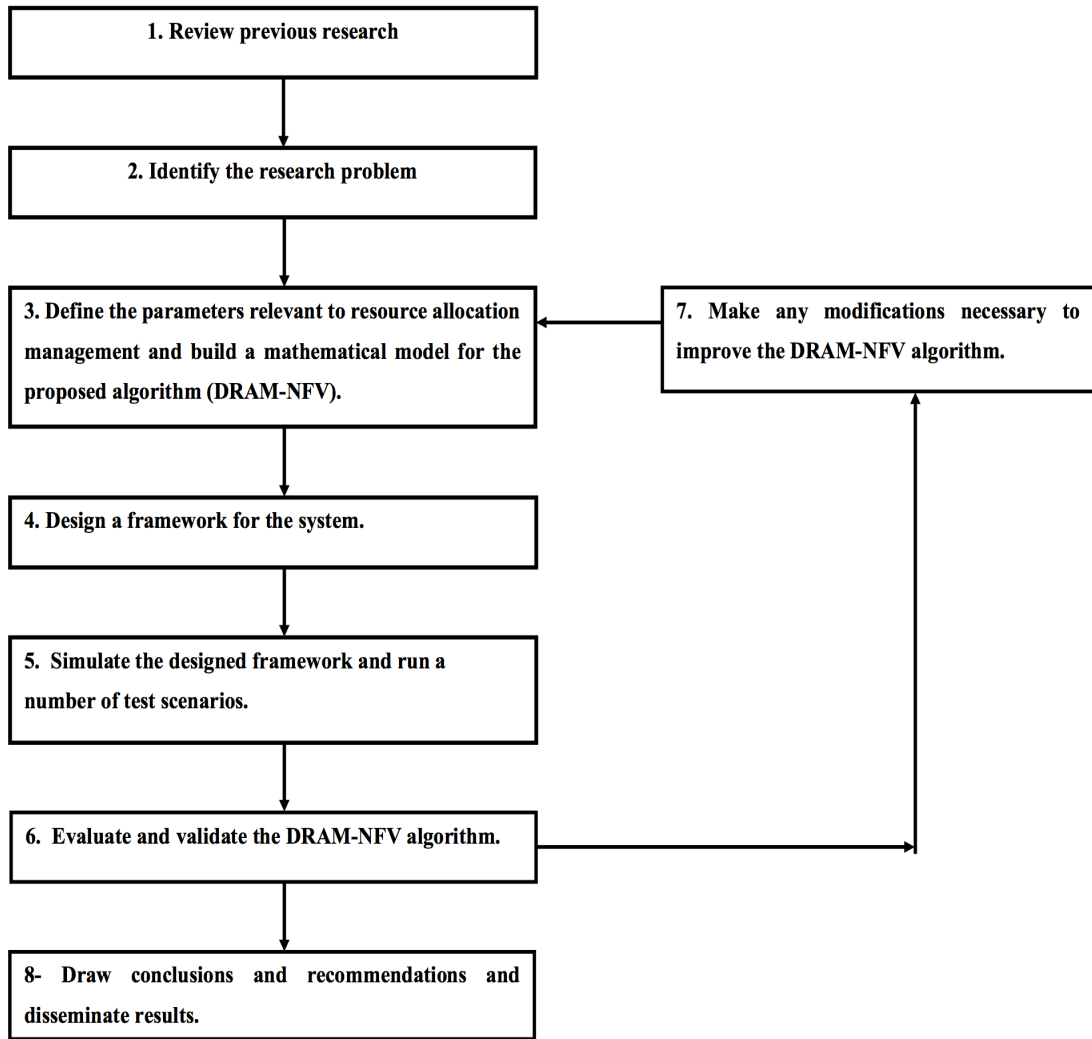


Figure 1-3, Research methodology.

## 1.9 Thesis Outlines:

The remainder of this thesis is arranged as follows:

- Chapter 2 outlines the proportional delay DiffServ and recent development on networking QoS.
- Chapter 3 consists of the literature review. This places emphasis on resources allocation in proportional delay DiffServ implementations and on recent applications of the SDN and NFV technologies; it also presents the contribution of the present research.
- Chapter 4 explains the DRAM-NFV algorithm principle and mathematical model.
- Chapter 5 describes the prototype of the DRAM-NFV algorithm which was implemented within a simulation environment (Network Simulator - NS3).
- The results, and the validation and evaluation of the DRAM-NFV algorithm are addressed in Chapter 6.
- Finally, conclusions and recommendations for further work are presented in Chapter 7.

# Chapter Two

## Background on Proportional Delay DiffServ and Recent Development in Quality of Service

### 2.1 Introduction:

This chapter provides an overview of the Proportional Delay DiffServ (PDD) technology and other recent technologies which have become prevalent and can be used to achieve Quality of Service (QoS), viz: Network Virtualisation, Software Defined Networks (SDNs), and Network Function Virtualisation (NFV).

The DiffServ architecture allows both absolute (quantitative) and relative (qualitative) service differentiation. The absolute service differentiation aims to provide a service class with a quantitative performance level (minimum forwarding rate, or maximum loss rate) at certain links or network paths. On the other hand, the relative differentiation service provides a number of classes with preferential performance. The higher classes will provide better QoS to flows than the lower classes will (Dovrolis & Ramanathan 1999) – in terms of both queuing delays and packet losses. However, the exact QoS for each class is not specified and depends on the traffic conditions and the DiffServ mechanisms that the network deploys.

There are four different models which can be used in regard to the relative delay differentiation approach (Dovrolis & Ramanathan 1999). First, there is price differentiation which is based on an appropriate pricing scheme such that higher classes are more expensive. This is considered the simplest relative differentiation model and it is informed by the assumption that “*higher prices will lead to lower loads in the higher classes*”. Such a differentiation cannot always provide consistent class differentiation because when the higher classes get overloaded, they will, in fact, offer worse packet forwarding than the lower classes. Consequently, the relative QoS differentiation between classes varies with the class loads. Capacity differentiation is another approach to achieving relative differentiation. In this approach, a larger amount of forwarding resources, in terms of bandwidth or buffer space, are allocated to the higher classes – relative to the expected loads in each class. Dedicating a larger relative share of the link bandwidth to the higher classes leads to lower average delays for the higher classes. However, increasing the traffic in the higher classes causes these classes to be offered worse packet forwarding than lower classes and increases their average delays so this scheme also cannot provide consistent differentiation between classes. Strict prioritization is another relative

differentiation model. Here, the higher priority classes are serviced first before lower classes. This service scheme provides a consistent differentiation between classes that does not depend on load variations (i.e. higher classes are always better). However, if the higher classes are persistently backlogged, this situation can result in long ‘starvation’ periods for the lower classes if no restriction is placed on the load of the higher classes. In addition, a strict prioritization scheme does not achieve a controllable differentiation between service classes because such a scheme does not allow the network operator to adjust the relative QoS between classes. Finally, proportional delay differentiation is another type of relative differentiation model and this model will be discussed in the next section.

Throughout the successive sections of this chapter, the standard DiffServ classes and queue management techniques will be illustrated. In addition: we will explain how the Cloud and the SDN technologies participate in supporting the NFV technology.

## 2.2 Proportional Delay DiffServ (PDD) Model:

In general, the Proportional Differentiated model is a relative differentiation model which provides a controllable and predictable differentiation scheme (Dovrolis & Ramanathan 1999). Controllability means that the quality spacing between classes can be adjusted by the network operator based on policy criteria. Predictability means that the differentiation scheme among classes is consistent (i.e. higher classes are always better or at least no worse than lower classes). These properties must be maintained even across short timescales and independent of the variation in class loads.

The proportional differentiation model states that “*certain class performance metrics (queuing delay or packet losses) should be proportional to the differentiation parameters the network operator chooses*” (Dovrolis 2000). In detail, the scheme can be described as follows. Suppose that  $\bar{M}_i(t, t + \tau)$  is a performance measure for class (i) in time interval  $(t, t + \tau)$ , where  $\tau > 0$ ; it represents the monitoring timescale (the value of  $\tau$  should be relatively small to achieve differentiation over a short timescale). The model imposes constraints as illustrated in Equation 2-1 for all pairs of classes and for all time intervals  $(t, t + \tau)$  in which  $\bar{M}_i(t, t + \tau)$  and  $\bar{M}_j(t, t + \tau)$  are defined.

$$\frac{\bar{M}_i(t, t + \tau)}{\bar{M}_j(t, t + \tau)} = \frac{c_i}{c_j}$$

2-1

Where  $c_1 < c_2 < \dots < c_n$  are the generic quality differentiation parameters.

Although the actual quality level of each class in the proportional differentiation model will vary with class loads, the quality ratio between classes will remain in place and controllable by the network operator, independent of class loads.

The proportional differentiation model can be applied in the context of queuing delays (Dovrolis 2000) by setting  $\bar{M}_i(t, t + \tau) = \frac{1}{\bar{d}_i(t, t + \tau)}$  in Equation 2-1, where  $\bar{d}_i(t, t + \tau)$  is the average queuing delay of the class (i) packets that depart in the time interval  $(t, t + \tau)$ . If there are no packets,  $(t, t + \tau)$  is not defined.

The proportional delay differentiation model states that “for all pairs of classes and for all time intervals  $(t, t + \tau)$  in which both  $\bar{d}_i(t, t + \tau)$  and  $\bar{d}_j(t, t + \tau)$  are defined, then:

$$\frac{\bar{d}_i(t, t + \tau)}{\bar{d}_j(t, t + \tau)} = \frac{\delta_i}{\delta_j}$$

2-2

where the parameters  $\delta$  in Equation 2-2 are the Delay Differentiation Parameters (DDP), ordered as  $\delta_1 > \delta_2 > \dots > \delta_N$ , that are configured by the domain administrator. The proportional delay differentiation model is used as the differentiation model for this research.

### 2.2.1 Standard Differentiated Service Classes:

The DiffServ service class is specified in each IP packet via a short label (eight bits) in IPv4 or IPv6 headers (it is called as Type of Service in IPv4 and as Traffic Class in IPv6). Figure 2-1 shows the IPv4 and IPv6 headers fields (Cisco Systems 2006). This mentioned label is also called the (DSCP). In fact, just six bits of the DiffServ field constitute the (DSCP) (Nichols et al. 1998). This value identifies a processing action to be performed by routers on the incoming packet called a per hop behaviour (PHB) (Blake et al. 1998) (Carpenter & Faucheur 2000). Figure 2-2 and Figure 2-3 show the DiffServ field in an IP header and a DSCP allocation table (WANG 2001) respectively.

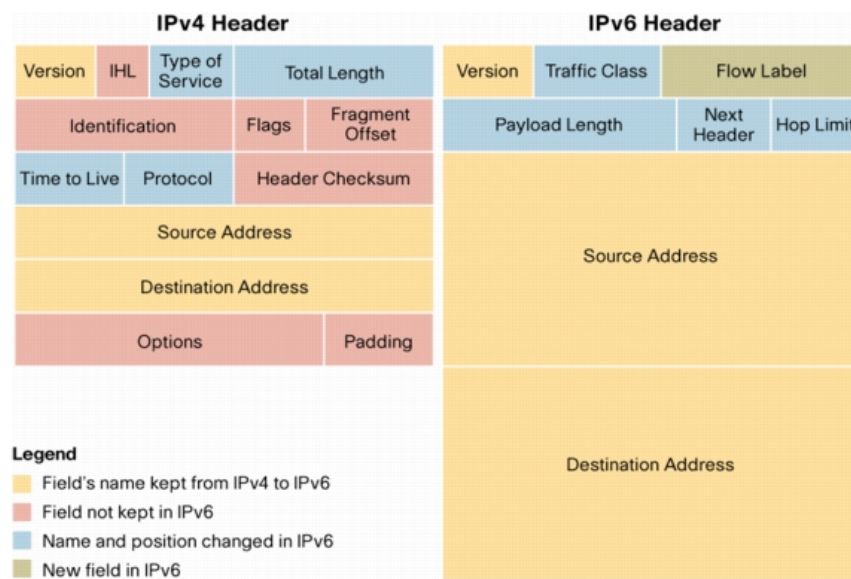
The (IETF) identified three types of differentiated forwarding service:

- Expedited Forwarding (EF) (Nichols et al. 1999) provides minimal delay, jitter and packet loss and guarantees the required bandwidth. The (EF) service is suitable for delay-sensitive applications such as voice and video. The expedited packet queue is given priority over the regular one via the use of a priority scheduler. Expedited packets see a network as unloaded, even when there is, in fact, a heavy load of regular traffic (Tanenbaum & Wetherall 2011).
- Assured Forwarding (AF) (Heinanen et al. 1999) classifies IP packets into four traffic classes and three levels of drop precedence (low, medium, and high). In the case of congestion,



high-drop-precedence packets are more likely to be dropped than low-drop-precedence packets. These two factors (the traffic classes and the drop precedences) together define 12 service classes. The implementation of an Assured Forwarding service requires an active queue management algorithm capable of solving possible long-term congestion problems.

- Best-Effort (BE) forwarding represents the simplest type of service that a network can offer; it does not provide any form of resource assurance to traffic flows (WANG 2001). The network treats all packets equally. When a link is congested, packets are simply pushed out as the queue overflows. A best effort (BE) service is appropriate for some applications that can tolerate large delay variations and packet losses, such as file transfer and email.



Field	Description	Field	Description
Version	Indicate the version of IP currently used.	Time-to-Live (IPv6 : Hop Limit)	Maintains a counter that gradually decrements down to zero, at which point the datagram is discarded.
IHL (IP-Header Length)	Indicates the datagram header length in 32 bits words	Protocol (IPv6: Next Header)	Indicates which upper-layer protocol receives incoming packets after IP processing is complete
Type of Service (IPv6: Traffic Class)	Differentiated Services Code Point (DSCP) – 6 bits	Header Checksum	Helps ensure IP header integrity.
Total Length (IPv6: Payload Length)	Specifies the length, in bytes, of the entire IP packet, including the data and header.	Source Address	Specifies the sending node.
Identification	Identifies the current datagram.	Destination Address	Specifies the receiving node.
Flags	Consists of a 3-bit field of which the two low-order (least-significant) bits control fragmentation. The low-order bit specifies whether the packet can be fragmented. The middle bit specifies whether the packet is the last fragment in a series of fragmented packets. The third or high-order bit is not used.	Options	Allows IP to support various options, such as security.
Fragment Offset	Indicates the position of the fragment's data relative to the beginning of the data in the original datagram, which allows the destination IP process to properly reconstruct the original datagram.	Flow Label (IPv6 only)	20 bits, originally created for giving real-time applications special service
		Data (variable)	Contains upper-layer information.

Figure 2-1, IPv4 and IPv6 headers fields.

Precedence	D	T	R	0	0
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Differentiated Service Field in IP Header

Field Definition	
Bit	Description
0-2	Precedence
3	0 = Normal Delay 1 = Low Delay
4	0 = Normal throughput 1 = High throughput
5	0 = Normal reliability 1 = High reliability
6-7	Reserved for future use

Application	Bandwidth	Delay	Jitter	Loss
Email	Low	Low	Low	Medium
File Sharing	High	Low	Low	Medium
Web Access	Medium	Medium	Low	Medium
Remote Login	Low	Medium	Medium	Medium
Audio on Demand	Low	Low	High	Low
Video on Demand	High	Low	High	Low
Telephony	Low	High	High	Low
Videoconferencing	High	High	High	Low

Figure 2-2, DiffServ field in IP header.

DSCP	PHB	DSCP	PHB	AF <sub>xy</sub> : Assured Forwarding PHB (Class <i>x</i> , drop precedence <i>y</i> ).
000000	CS0(DE)	100000	CS4	
000001	EXP/LU	100001	EXP/LU	CS <sub>x</sub> : Class selector PHB <i>x</i> .
000010	-	100010	AF41	
000011	EXP/LU	100011	EXP/LU	DE: Default PHB (CS0).
000100	-	100100	AF42	
000101	EXP/LU	100101	EXP/LU	EF: Expedited Forwarding PHB.
000110	-	100110	AF43	
000111	EXP/LU	100111	EXP/LU	EXP/LU: Experimental/ Local use.
001000	CS1	101000	CS5	
001001	EXP/LU	101001	EXP/LU	
001010	AF11	101010	-	
001011	EXP/LU	101011	EXP/LU	
001100	AF12	101100	-	
001101	EXP/LU	101101	EXP/LU	
001110	AF13	101110	EF	
001111	EXP/LU	101111	EXP/LU	
010000	CS2	110000	CS6	
010001	EXP/LU	110001	EXP/LU	
010010	AF21	110010	-	
010011	EXP/LU	110011	EXP/LU	
010100	AF22	110100	-	
010101	EXP/LU	110101	EXP/LU	
010110	AF23	110110	-	
010111	EXP/LU	110111	EXP/LU	
011000	CS3	111000	CS7	
011001	EXP/LU	111001	EXP/LU	
011010	AF31	111010	-	
011011	EXP/LU	111011	EXP/LU	
011100	AF32	111100	-	
011101	EXP/LU	111101	EXP/LU	
011110	AF33	111110	-	
011111	EXP/LU	111111	EXP/LU	

Figure 2-3, DSCP allocation table.

## 2.2.2 Queue Management Techniques for Differentiated Services:

Many packet drop algorithms have been proposed. Tail drop is a simple passive queue management technique. All the packets arriving at the buffer are dropped indiscriminately when the buffer is full (Bodin & Schelen 2001). This method cannot provide any control of congestion; the congested situation tends to go into an oscillation between peaked congestion and off-congestion (Kun I. Park 2005). The Random Early Detection Algorithm (RED) is used in a router to detect congestion and to control it by dropping packets early in congestion intervals. Here, the average queue size for a particular period in the router is computed and when this average queue size exceeds a certain threshold, arriving packets are dropped with a particular probability that is a function of the average queue size. A high average queue size mean that the probability of packets being dropped is high (Clark & Fang 1998).

The Weighted Random Early Detection algorithm (WRED) is the same as the RED algorithm, but with multiple drop profiles. In WRED, rather than using a single drop profile for all queues, different drop profiles may be defined for individual queues. In addition, multiple drop profiles may be defined within a single queue (Kun I. Park 2005). WRED is the algorithm used in this research.

### 2.3 Network Virtualisation, SDN and NFV:

Network virtualization in the Internet can be described as a networking technology that creates dedicated Virtual Networks (VNs) over a physical infrastructure (Han et al. 2016). It can also be described as a networking environment which allows one or multiple service providers to form heterogeneous virtual networks that co-exist together but in isolation from each other and to deploy customized end-to-end services on those virtual networks by effectively sharing and utilizing the underlying network resources furnished by the infrastructure providers (Chowdhury & Boutaba 2009). The underlying network resources (the physical network infrastructure) consist of physical links and nodes that are virtualized and made available to virtual networks. Figure 2-4 illustrates a network virtualization environment (Duan et al. 2012). With network virtualization, network services provisioning is decoupled from the data transportation mechanisms.

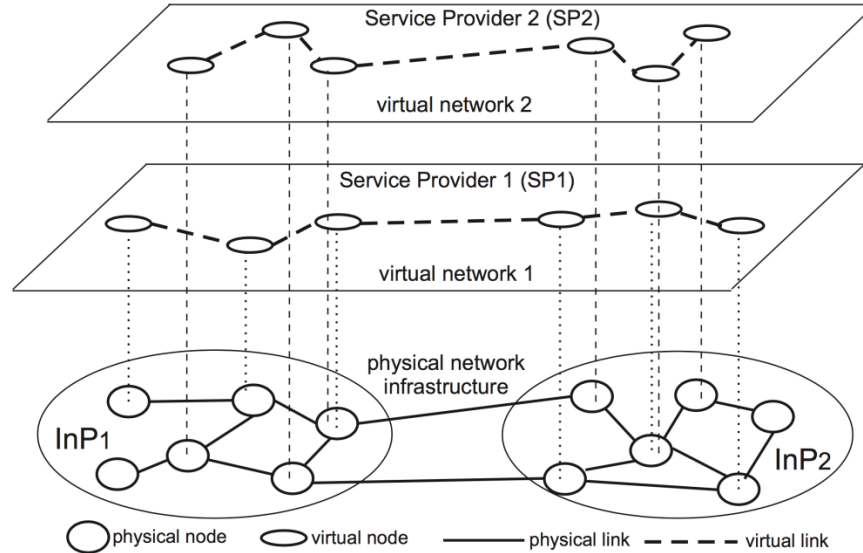


Figure 2-4, Illustration of a network virtualization environment.

Cloud computing refers to both applications and hardware (storage and processing) that can be delivered as virtual services over the Internet (Armbrust et al. 2009) (Mell & Grance 2011). It can also be defined as a large scale distributed computing paradigm in which a pool of

abstracted, virtualized, dynamically scalable computing functions and services are delivered on demand to external customers over the Internet (Foster et al. 2008).

There are four main classifications of Cloud services (Dong Xu 2010).

- i. Software-as-a-Service (SaaS) is a Cloud based service model whereby applications reside on the Cloud and are offered to end-users. The application's services can be provided as utilities to its customers.
- ii. Platform-as-a-Service (PaaS) is a Cloud based service model used by Cloud computing developers to implement and deploy their applications onto the cloud.
- iii. Infrastructure-as-a-Service (IaaS) or Hardware as a service (HaaS) is a Cloud based service model. It provides computing resources in terms of virtual hardware (storage and processing) (Buyya et al. 2013) (Hill et al. 2013) (Ashon & Ilyas 2011). The virtual hardware services can be provided as utilities to its customers.
- iv. Network as a Service (NaaS): is a Cloud based service model that offers network connectivity services virtually over the Internet. The network connectivity services can be provided as utilities to its customers (M.P.V. Manthena 2015).

Networking plays an important role in Cloud computing because all Cloud services represent the remote delivery of computing resources via the Internet. Networking also provides data communications within Cloud data centres and among such data centres distributed at different locations. So, Cloud services consist of computing functions (storage and processing) that are provided by the Cloud infrastructure and communications functions (Networking) offered by the Internet. The convergence of networking and Cloud computing through the virtualization of networking and computing resources allows for a Network-as-a-Service (NaaS) paradigm. The services delivered to end users using the NaaS paradigm include computing services provided by the Cloud infrastructure and network services offered by the network infrastructure. Figure 2-5 shows a NaaS based framework for network–Cloud convergence.

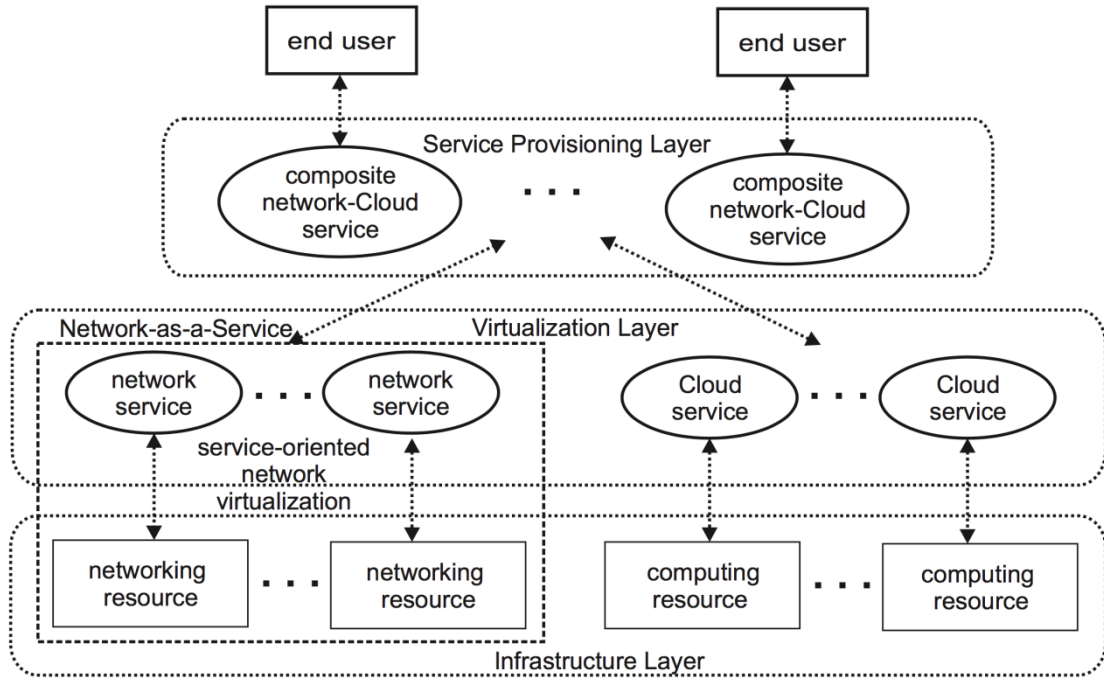


Figure 2-5, NaaS based framework for network cloud convergence.

SDN is a networking model that achieves network virtualisation. It offers an opportunity to loosen the limitations inherent in conventional network infrastructures in which the network operators need to configure each individual network device separately in order to implement the desired network policies because the control plane and the data plane are bundled inside the networking devices (Theophilus Benson 2009). SDN was originally defined by a research team at Stanford University (Kate Greene 2009) (McKeown et al. 2008) as “a network architecture where the forwarding state in the data plane is managed by a remote-control plane decoupled from the data plane”. In an SDN context, the forwarding decisions are flow based, rather than destination based. The flow abstraction allows for the unification of the behaviours of different types of network devices, including routers, switches, firewalls and so on (Jamjoom et al. 2014). As a result of this new infrastructure, network switches and routers become simple forwarding devices and the control logic is implemented in a logically centralized controller. Thus, the processes of policy enforcement and network (re)configuration and evolution become much simpler (Kim & Feamster 2013) and a network environment can become capable of dynamically dealing with faults and load changes (Raghavan et al. 2012) (Kim & Feamster 2013).

The control plane in SDN is considered to be the core of the SDN; it manages and monitors the SDN domain by involving one or more centralized SDN controllers. An SDN controller or Network Operating System (NOS) is a piece of software that runs on commodity server

technology. It takes requests from the Management plane (Management Application) via a well-defined (API) and accomplishes consolidated management and monitoring of forwarding devices in data plane via another well-defined (API).

There are two control models which can be used in an SDN (Rao 2014) (Kreutz et al. 2015). These are the centralized controller model, whereby a single entity manages all SDN forwarding devices; and the distributed controller model, which controls a cluster of nodes and the distributed controllers are connected to each other using also a well-defined (APIs) to import/export data. The distributed controller model is more scalable and dependable than the centralized model.

The Management plane in SDN is an array of network services applications run separately and directly on the controller within each SDN domain (McKeown et al. 2008).

The Network Functions or Services (NF) can be divided into control plane functionalities and data plane functionalities. Control plane functionalities are responsible for controlling the network operations: e.g., resources management and controlling network access. Data plane functionalities are responsible for forwarding the traffic (Soares et al. 2014).

The European Telecommunications Standards Institute (ETSI) initiated a new technology called NFV. It exploits the virtualization technique in order to separate network functions from the underlying hardware equipment's, through software applications running on high-volume servers (Cloud). Virtualized network functions can be instantiated in various locations, datacentres or network nodes (Chiosi et al. 2012) (ETSI). NFV reduces network Capital Expenditure (CAPEX), Operating Expenses (OPEX) and complexity by reducing equipment costs and power consumption.

NFV can be considered a complementary technology to SDN and can be combined with it to achieve greater efficiency (Hawilo et al. 2014) (Chen et al. 2015) (Rao 2014) (Han et al. 2015). High volume servers (Cloud) hold multiple (VMs) on which multiple Network Functions (NFs) run. A virtual machine is a software application which can be used to provide the physical computing and networking resources of a Cloud as virtual resources to the SDN network operating system (controller) which is set up on a virtual machine. Each VM represents an SDN controller and these controllers communicate with one another. Consequently, the virtualized network functions can be deployed with less hardware as the virtual machines can be deployed across the system resulting in an increase in system efficiency and flexibility.

With the use of the SDN and NFV technologies, algorithms can be applied flexibly and bring intelligence to the system because, firstly, the SDN controller maintains the network state and the resource allocation and produces the computing results for an optimized network status.

Secondly, NFV provides the infrastructure on which the SDN controller applications run and also provides connections among the virtualized network functions through the connection of the SDN controllers.

The selection of which NFs to virtualize depends on: (A) from a technical perspective, whether the selected NF can actually be virtualized or not (e.g., the issues might be the availability of hardware and performance limitations); and (B) from an economic and operational perspective, will the effort of virtualizing the NF bring real value? (Soares et al. 2014).

## **2.4 Chapter Summary:**

The Proportional Delay DiffServ (PDD) model is a relative differentiation service model. It provides a number of service classes with preferential performance such that the higher classes receive a better QoS in terms of both queuing delays and packet losses. The exact QoS for each service class in this model is not specified but depends on the traffic conditions in the DiffServ router (the per hop behaviour).

Network Function Virtualisation (NFV) aims to implement network services or functions via software elements executed on a virtualisation infrastructure. The Network-as-a-Service (NaaS) Cloud based service model constitutes the infrastructure for an NFV. It provides the physical computing and networking resources of a Cloud environment as virtualised services to the network function that requires to be virtualised.

The Software Defined Network (SDN) model is a networking model that provides support for network virtualisation. In SDN, the control layer (Controller) of a forwarding device is separated from the data layer. SDN makes the data layer of physical network devices (forwarding devices) programmable remotely and accomplishes the consolidated management and monitoring of forwarding devices in relation to the requests that are received from the management application of a network function (the Management layer).

The SDN and NFV technologies can be used to implement a new model for Proportional Delay DiffServ systems. In this model, managing the service classes' bandwidth resources at the edge routers of the DiffServ domains is selected as a network function which must be virtualised using the concept of NFV for a number of reasons. Firstly, only some information about the traffic conditions (service class queue lengths and delays) in the edge routers of the DiffServ domains need be sent to the NFV in order to configure resources while the SDN forwarding devices keep forwarding the service class packets according to the configuration settings that are received from the (NFV) management application through their controllers. Secondly, this process of configuring resources via the NFV cannot cause extra delays, especially if there are



enough virtualised computing and networking resources to accommodate the necessary computing and communicating processes. The edge routers of DiffServ domains in this model should be SDN devices and their controllers are set up as software on the Cloud. The controllers will communicate to each other in order to import and export information concerning traffic congestion in any of the service class at the edge routers of the DiffServ domain. They accomplish dynamic consolidated management for the service class bandwidth resources at the edge routers within and between PDD domains, rather than isolated management, because they monitor the traffic conditions in the service classes at the SDN forwarding devices (the edge routers) of the DiffServ domains. The virtualised function for managing service class bandwidth resources takes into consideration the congestion level in the service classes at the ingress router of the downstream DiffServ domain and the traffic conditions at the egress router of the upstream DiffServ domain - to manage service class resources across both domains.

# **Chapter Three**

## **Literature Review Relating to Proportional Delay DiffServ Resource Management and the Recent Uses of SDN and NFV**

### **3.1 Introduction:**

This chapter focuses on the packet scheduling algorithms that can be used to implement the Proportional Delay Differentiated PDD model. The PDD model is achieved by providing a lower delay for the highest priority service class as compared to other classes in long and short timescales, and also by approximating the ratios of average service class delays to the ratios specified by their corresponding differentiated delay parameters. Within the scope of the PDD model, resources can be allocated in a number of ways. They can be allocated based on the normalized average class delay, on the normalized packet delay at the head of the service class queue as in Proportional Average Delay PAD, Waiting Time Priority WTP and Hybrid Proportional Delay HPD scheduling algorithms - or they can be allocated by providing each class with a minimum bandwidth share of the router out-link's capacity, as in Link Sharing Scheduling algorithms and Backlogged Proportional Rate scheduling algorithms.

Some recent contributions related to the NFV and SDN technologies are also presented in this chapter. In addition, the contribution of this present study is presented in this chapter. This latter contribution centres on the use of NFV and SDN technologies to improve the performance of the Dynamic Weighted Fair Queuing (DWFQ) algorithm in allocating resources within and across different DiffServ domains. The reasons for selecting the DWFQ algorithm as opposed to other scheduling algorithms are explained in the last section of this chapter.

### **3.2 Resources Allocation in Proportional Delay DiffServ domains:**

In this section, the scheduling algorithms that are used to implement the PDD model are discussed. A good scheduler for the PDD model should not only achieve or closely approximate the constraints specified by the PDD model, as per Equation 3-1, but it should also provide predictable delay differentiation within short timescales (Dovrolis et al. 2002). This latter desideratum simply means that the performance of higher classes should be better

than the performance of lower classes independent of the aggregate load, the class load distribution, and the timescales in which the performance is measured.

$$\frac{\bar{d}_i}{\bar{d}_j} = \frac{\delta_i}{\delta_j}$$

3-1

Proportional Average Delay (PAD) scheduling (Dovrolis et al. 2002) maintains the average delay  $d_i(t)$  for each service class  $i$ , normalized by the corresponding DDP ( $\delta_i$ ), as illustrated in Equation 3-2. A packet is chosen for forwarding at time  $t$  from the backlogged class with the maximum normalized average delay.

$$\tilde{d}_i(t) = d_i(t)/\delta_i$$

3-2

This algorithm aims to equalize the normalized average delays among all classes in long term timescales through reducing the differences among their normalized average delays. (Dovrolis et al. 2002) considered the PAD algorithm to be an excellent scheduler in terms of meeting the constraints of the PDD model when certain conditions are met with regard to selecting the DDPs. (Dovrolis et al. 2002) also examined the behaviour of the PAD scheduling algorithm by comparing the ratios of short term average delays between classes and the ratios of DDPs between classes. He found that this scheduling algorithm does not take into account the waiting times of the backlogged packets, and it occasionally allows higher classes to experience much larger queuing delays than their long-term average and the queuing delays of lower classes. Thus, the PAD algorithm does not achieve the predictability criterion required by the PDD model.

Waiting Time Priority (WTP) scheduling was another scheduling algorithm which was examined by (Dovrolis et al. 2002). In WTP scheduling, a packet is assigned a priority that increases proportionally to the packet's waiting time. The higher classes in the WTP algorithm have larger priority increase factors and the packet with the highest priority is serviced first in non-pre-emptive order. WTP maintains the waiting time  $w_i(t)$  of the packet at the head of each service class  $i$ , normalized by the corresponding DDP ( $\delta_i$ ) as illustrated in Equation 3-3.

$$\tilde{w}_i(t) = w_i(t)/\delta_i$$

3-3

Whereas the PAD scheduler chooses for service the class with the maximum normalized average delay, WTP chooses for service the class with the maximum normalized head waiting time: i.e., a packet is sent from that backlogged class with the maximum normalised head waiting time. In this manner, WTP attempts also to minimize the differences between the normalized waiting times of the successively departing packets. (Dovrolis et al. 2002) demonstrated the behaviour of the WTP scheduler. That study found that WTP achieves a proportional delay differentiation between successive packet departures, especially when the delays are sufficiently large. Also, (Dovrolis et al. 2002) tested the performance of WTP in short timescales. It was found that the WTP is an excellent scheduler in terms of providing higher classes with lower delays in short timescales and thus, this scheme provides a predictable delay differentiation. Moreover, it also approximates to the PDD model in heavy load conditions as the utilization tends to 100% and the aggregate backlog of classes increases. For lower load conditions, (Dovrolis et al. 2002) found that the WTP algorithm deviates, technically, from the PDD model.

Hybrid Proportional Delay (HPD) scheduling is another scheduling algorithm which was presented by (Dovrolis et al. 2002). This scheduling algorithm combines the methods of the PAD and WTP algorithms in order to create a scheduler that is approximate to the PDD model, when certain conditions are met in relation to selecting DDPs, and that provides a predictable delay differentiation in short timescales. HPD maintains a delay metric for each service class  $i$ , normalized by the corresponding DDP ( $\delta_i$ ). The delay metric for each class includes the normalized average delay  $\tilde{d}_i(t)$  for a service class  $i$  and the normalized head waiting time,  $\tilde{w}_i(t)$ , for a service class  $i$ , as illustrated in Equation 3-4,

$$\tilde{h}_i(t) = g\tilde{d}_i(t) + (1 - g)\tilde{w}_i(t)$$

3-4

where:

$\tilde{h}_i(t)$  is the normalized hybrid delay for a service class  $i$ .

$\tilde{d}_i(t)$  is the normalized average delay for a service class  $i$ .

$\tilde{w}_i(t)$  is the normalized waiting time of the packet at the head of service class  $i$ .

$g$  is known as the HPD parameter  $0 \leq g \leq 1$ .

When  $g$  is zero HPD becomes equivalent to WTP, when  $g$  is one HPD becomes equivalent to PAD. For other values of  $g$ , HPD combines the attributes of the PAD and WTP schedulers. (Dovrolis et al. 2002) used a simulator to estimate the best value of  $g$  (0.875) whereby HPD combines the attributes of the PAD and WTP schedulers.

In contrast to PAD and WTP schedulers, HPD choose for service the class with the maximum normalized hybrid delay: i.e., a packet will be sent from that backlogged class which has the maximum normalised hybrid delay. (Dovrolis et al. 2002) tested the performance of the HPD algorithm and concluded the following.

- i. In heavy load conditions, about 90% utilization, all three algorithms, PAD, WTP, and HPD, meet the PDD model criteria: i.e., they keep within the constraints on ratios specified by the PDD model.
- ii. In lower load conditions, HPD is closer to the PDD model than WTP but it does not reach the optimal behaviour which is exhibited by PAD.
- iii. HPD closely approximates the PDD model independent of class load distribution. The approximate error increases as the utilization decreases and as the delay differentiation between classes becomes more extreme.
- iv. HPD does not have the predictability problem that PAD has. This means that the HPD algorithm manages to provide lower delays to higher classes in both short and long term timescales.

Link sharing scheduling algorithms aim to provide each class with a minimum bandwidth share of the link's capacity. Examples of such schedulers include the Generalized Processor Sharing (GPS) scheduler (Heinanen et al. 1999) (Demers et al. 1989) and its approximations such as Class Based Queuing (CBQ) (Floyd & Jacobson 1995), Weighted Fair Queuing (WFQ) (Demers et al. 1989) and Hierarchical Packet Fair Queuing (H-PFQ) (Bennett & Zhang 1997). GPS defines a packet based scheduler that provides a minimum service rate  $r_i(t)$  to each backlogged class  $i$  at time  $t$ , as illustrated in Equation 3-5. Such a scheduler (and the equation) takes into account class weights  $\{w_j, j = 1 \dots N\}$ , where  $N$  represents the number of service classes.

$$r_i(t) = C \frac{w_i}{\sum_{j \in B(t)} w_j}$$

3-5

where:

$C$  is the link bandwidth (capacity), and

$B(t)$  is the set of backlogged classes at time  $t$ .

The weights used by GPS are the performance controls that determine the delay differentiation between classes. If these weights are selected such that one or more classes have a larger share

of the link capacity than other (lower) classes, relative to the class input loads, then higher classes can be expected to encounter lower queuing delays.

The service classes in CBQ, WFQ and H-PFQ schedulers are guaranteed a certain fraction of a link's capacity, sharing any available excess bandwidth. Link sharing scheduling algorithms can be used in the proportional DiffServ model. (Dovrolis 2000) demonstrated that they can provide controllable delay differentiation, but they are too sensitive to class load distribution. Class Based Queuing (CBQ) allows traffic to share bandwidth. It divides traffic into queues and assigns each queue a specific amount of network bandwidth (Floyd & Jacobson 1995).

Weighted Fair Queuing (WFQ) is widely used in QoS routers (Chin-Chang Li et al. 2000), it offers fair queuing that divides the available bandwidth across queues of traffic based on static weights assigned independently to each service class queue (Panza et al. 2006) by the administrator of the domain. The weight of a class can be specified via any QoS parameter – such as service rate or delay. An absolute service class rate can be reserved easily by assigning a fixed weight to a service class queue (Chin-Chang Li et al. 2000).

WFQ is a controllable delay differentiation scheduler wherein the load distribution across service classes is uniform. However, the drawback of WFQ is that slight changes in the class load distribution dramatically affect the resulting delay differentiation and can cause a priority inversion between the offered service classes. This variation in differentiation and inversion of priority are not acceptable for differentiated services networks. (Dovrolis 2000) suggests a solution to this drawback by presenting an algorithm that can adjust the service class queues weights dynamically based on current class loads.

Backlog Proportional Rate (BPR) scheduling is considered to be a dynamic version of the GPS scheduler (Dovrolis et al. 1999). The class weights are dynamically adjusted based on the class loads at any given moment in time. For two backlogged classes  $i$  and  $j$ , the service rate allocation used by BPR follows the proportional constraint shown in Equation 3-6. The sum of the assigned service rates  $\sum_{i=1}^N r_i(t)$  must be equal to the link capacity  $C$  when the scheduler is busy.

$$\frac{r_i(t)}{r_j(t)} = \frac{\delta_j q_i(t)}{\delta_i q_j(t)}$$

3-6

where  $q_i(t)$  is the backlog for queue  $i$  at time  $t$ .

(Dovrolis et al. 1999) studied the performance of BPR scheduling. In heavy load conditions, BPR approaches the PDD model asymptotically but it does not do as well as WTP. Moreover,

BPR suffers from a simultaneous queue clearing property such that, after busy periods, all queues controlled by a BPR scheduler become empty at the same time.

(Moret, Yan 1998) and (Chin-Chang Li et al. 2000) have proposed two more proportional delay schedulers called the Proportional Queue Control Mechanism (PQCM) and Dynamic Weighted Fair Queuing (D-WFQ) respectively. Both these scheduler schemes are based on the GPS rate allocation mechanism. They both adjust the GPS weights periodically, every  $\Delta$  seconds, based on the measured service class queue length as represented by the class backlogs and the input rates. In this manner, they attempt to achieve proportional average delay differentiation in the next time interval of  $\Delta$  seconds. PQCM uses the backlogs as measured on the instant to adjust the weights while D-WFQ uses exponential average estimators for the computation of the service class queue lengths. The difference between PQCM and D-WFQ schedulers and the BPR scheduler is that BPR adjusts the weights after each packet departure and so is not based on a fixed period  $\Delta$  while PQCM and D-WFQ adjust their weights periodically. This latter behaviour introduces a feedback loop in the network such that the future service rates depend on the service class queue length over a previous time interval.

(Sharma et al. 2014) has designed a dynamic packet scheduling scheme for DiffServ called Dynamic Benefit Weight Scheduling (DBWS). It is based on the weighted round robin (WRR) policy. DBWS avoids the overbooking of resources and provides a guaranteed service for the Expedited Forwarding class (EF). The weight of the (EF) class is predicted based on the previous (EF) allocated weight at time  $(t-1)$  and the estimated average increase in the queue length of the (EF) buffer.

### **3.3 Contributions of the SDN and NFV Technologies:**

(Guck & Kellerer 2014) proposed a model for achieving end-to-end real time QoS with Software Defined Networking through providing a centralized real time communication service.

(Karaman et al. 2015) utilized SDN to give an ensured QoS to premium clients in a VoIP system congested by both VoIP and non-VoIP backlogged traffic through a constraining bandwidth. Their methodology allocated flows to the various available queues and adapted its routing decisions based on network conditions.

(Alipio et al. 2016) implemented a real time testbed for Software Defined Networks in order to demonstrate how the QoS mechanism (Priority Queuing) can be implemented on an OpenFlow testbed. The testbed used Raspberry Pi to implement the SDN forwarding device (OpenFlow switch) and also the L2 forwarding module of an SDN controller (POX) as the basis of the

customized controllers in the testbed implementation. The scheduling algorithm which was implemented for priority queuing manages the flows with respect to the different service classes' QoS requirements and provides priority levels to these different service classes.

(An et al. 2016) used the network simulator NS2 to present a dynamic priority adjustment algorithm which guarantees different delay deadlines for real time flows in SDN based networks by achieving a centralized view of the overall network.

(Hu et al. 2015) proposed a framework for network management and resources allocation which enables current Internet implementations to provide better QoS guarantees for end users and applications. (Hu et al. 2015) built a software defined overlay network, over the IP network, and took advantage of the characteristics of per-flow management in SDN to satisfy the resource demands of these applications. Hu's methodology introduced SDN into the network edge and reconstructed the access network to improve the integration and flexibility of network management and control. Its SDN-based overlay network demonstrated resource scheduling mechanisms which guarantee prescribed levels of QoS and also demonstrated the coupling mechanism which was designed to implement the collaboration between software defined networks and IP networks to achieve better resource utilization. In addition, this overlay network implemented packet classification, flow labels and state maintenance to assist the core network to ensure the QoS of important applications.

(Wibowo et al. 2017) provided a general review of the evolution of multi-domain SDNs and the major challenges which exist for future research. This review analysed the main research issues and approaches relating to the development of multi-domain networks in the future, and it provided an overview of the implementation of a controller which used open source resources as well as commercial systems. This illustrated the differences in controller features.

(Karakus & Durresi 2017) presented a survey of relevant research on the maintenance of QoS using OpenFlow-enabled SDN networks from the point of view of examining the role of SDN. They looked at Multimedia flow routing mechanisms, inter-domain routing mechanisms, resource reservation mechanisms, queue management and scheduling mechanisms, Quality of Experience (QoE) - aware mechanisms, network monitoring mechanisms, and other QoS-centric mechanisms such as virtualization based QoS provisioning and QoS policy management, etc. In addition, they discussed the QoS capabilities of the OpenFlow protocol by reviewing its versions along with some well-known, open-source, and community-driven controller projects. Furthermore, their review outlined the potential challenges and the open questions which need to be addressed further in order to produce better and more complete QoS abilities in SDN/OpenFlow networks.



(Soares et al. 2014) and (M.P.V. Manthena 2015) presented a platform infrastructure architecture for NFV and SDN. Many studies have emerged recently in the telecommunication and computer networks fields that use the cutting-edge techniques of NFV and SDN. (Batalle et al. 2013) presented a design and analysis for the virtualizing of one of the primary network functions (the routing function) that is offered by network routers. (Lee et al. 2016) used the NFV technique to develop a new low latency handover technology for real-time applications in mobile environments – such as mobile cloud access and VoIP. (Volvach & Globa 2016) proposed a methodology, using the SDN and NFV technologies, for the fast recovery of mobile network node functionality in the event of a disaster. (Al-Quzweeni et al. 2016) proposed a framework for improving energy efficiency using NFV in 5G networks. (Luo et al. 2016) proposed advanced algorithms for controllers in industrial wireless sensor networks based on SDN and NFV techniques which also improve energy efficiency. (Urgun & Kavak 2016) discussed the use of SDN and NFV approaches in the QoS mechanism for a cellular core network in terms of traffic classification, traffic monitoring and traffic engineering methods. The above is a quick overview of some of the most recent research work in the telecommunication and computer networks fields that has made use of the NFV and SDN concepts. There are other studies which have emerged in this field but our focus does not allow us to detail them here.

### 3.4 The Contribution of this Research:

No previous work has attempted to orchestrate the management of resources across different proportional delay DiffServ domains. The concept of NFV and SDN technologies are used in this research in order to enhance the performance of the DWFQ algorithm (Chin-Chang Li et al. 2000). The contribution of this research is to introduce a new resource management algorithm to allocate resources within and between proportional delay DiffServ domains; this new algorithm is called **“Dynamic Resource Allocation Management – Network Function Virtualization (DRAM-NFV)”**. The DRAM-NFV algorithm manages the resources across different proportional delay differentiated services domains dynamically in the event of congestion in addition to managing the resources dynamically within the edge routers of domains.

A previous work (Chin-Chang Li et al. 2000) studied the performance of the DWFQ scheduling algorithm in achieving a proportional delay differentiation among service classes within a single edge router of a DiffServ domain. In contrast, the work presented here aims to improve the performance of the DWFQ algorithm so that it manages the resources across different

DiffServ domains in the event of congestion in addition to managing resources within the edge routers of the DiffServ domains. There are many differences between the present work and previous studies. In this research, we study the dynamic allocation of resources in a simulated environment that represents multiple inter-related DiffServ domains. We took into consideration the effects of the availability of core routers and the effects of the queue management technique Weighted Random Early Detection (WRED) on resource management. These factors were not considered in the cited previous work. The performance of the DWFQ algorithm in managing resources within a single edge DiffServ router was studied in (Chin-Chang Li et al. 2000) while this present research uses the concept of NFV and SDN technologies to enhance the performance of DWFQ in managing resources across DiffServ domains in the event of congestion, (in addition to managing resources within the edge DiffServ routers). The new algorithm has been implemented and tested, via the network simulator ns3, using a number of test scenarios with various classes and sources of traffic passing through the network elements as illustrated in subsequent chapters.

### **3.5 Chapter Summary:**

Our objectives in this chapter were to define the scheduling algorithms that are used to implement the proportional delay differentiated model and to define some recent applications of the NFV and SDN technologies. The concept of NFV and SDN technologies are used in this research to enhance the performance of the DWFQ algorithm through presenting a new scheduling algorithm called (DRAM-NFV) that allocates dynamically the resources within the edge routers of the DiffServ domain and across different proportional delay DiffServ domains in the event of congestion. The reasons for selecting DWFQ as opposed to other scheduling algorithms are that the DWFQ can be considered as the dynamic version of the WFQ scheduling algorithm which is used widely in QoS DiffServ routers, that DWFQ shares any available excess bandwidth in the router out link among the service classes, and that this algorithm introduces a feedback loop in the DiffServ network such that the service classes rates and weights are assigned periodically in short timescales and based on the traffic conditions in the service class queues of the proportional delay DiffServ router. The next chapter is related to identifying the mathematical model for the DRAM-NFV algorithm and testing the algorithm on different topologies of DiffServ domains, as explained in the subsequent chapters.

# Chapter Four

## DRAM-NFV Algorithm: Principles and Mathematical Model

### 4.1 Introduction:

In this chapter, the principle of the DRAM-NFV algorithm to manage resources within and across different Proportional Delay DiffServ domains will be described in the first section of this chapter, and in the second section of this chapter the set of mathematical expressions on which the DRAM-NFV algorithm is based are presented. These mathematical expressions describe the Proportional Delay DiffServ model and its queue performance in term of average queue delay, average queue length, queue scheduling rate and packet forwarding scheme. Finally, a mathematical expression for managing resources across different Proportional Delay DiffServ domains is also presented. The following mathematical principles will be taken into consideration when implementing the simulation of the test scenarios for the DRAM-NFV algorithm (as described in the next chapters).

### 4.2 Algorithm Principles:

DiffServ networks are composed of edge (ingress and egress) and core routers. The edge routers process the incoming traffic via a sequence of operations and the core routers forward service class traffic. A DiffServ domain is static, meaning that each router or domain allocates its resources locally or separately based on its traffic condition and the domain configuration parameters [Per Hop Behaviour (PHB)]. Once the domain administrator sets the configuration parameters of a domain, these parameters will not change. In addition, the traffic conditions within a DiffServ domain change continuously and randomly and therefore, congestion is possible in the core routers of the DiffServ domain as a result of increasing traffic from the domain edge routers or from adjacent DiffServ domains, as well as when the resources of the domain core routers cannot cope. Traffic congestion means that the DiffServ domain favours certain amounts of traffic while other network traffic will suffer. This congestion is reflected proportionally in the traffic states of the edge routers of the domain and causes further congestion – much like congestion at road junctions does. If congestion occurs at a road junction (i), this will cause additional congestion at a previous junction (i-1), and so on.

Although the DiffServ has Per Hop Behaviour (PHB) and has a static approach of allocating its resources separately per each domain, it would be better if the Differentiated services domains are managing their resources dynamically rather than statically owing to reasons already mentioned. Instead of having a static DiffServ, we can monitor traffic condition in one domain and make changes to managing resources of other domains so we can now calibrate between DiffServ domains in managing resources. This calibration can be achieved through using the SDN and NFV technologies. The SDN switches, for example OpenFlow switches, can be programmed automatically and remotely according to the traffic conditions within or between DiffServ domains and based on the QoS requirements of the Internet applications traffic. The NFV technology can be used to extract and implement virtually a certain function in a DiffServ network, which is resource management function. Using NFV technology, the management process will be deployed across DiffServ domains rather than being deployed locally. It is still locally, if the traffic condition within a DiffServ domain is normal (there is no congestion). Figure 4-1 demonstrates the principle of resource management across DiffServ domains using the (NFV) concept.

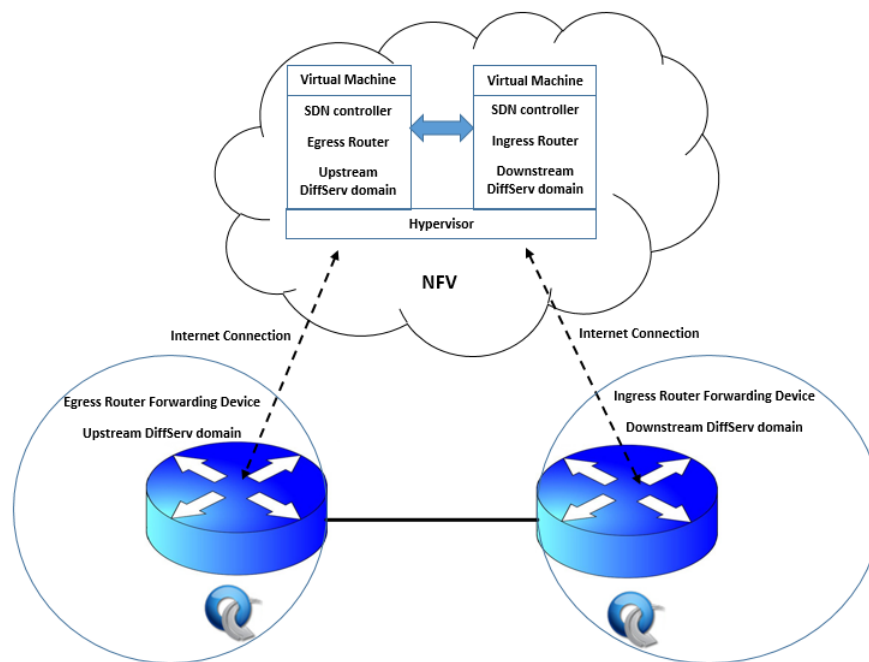


Figure 4-1, Principle of the dynamic resource management algorithm within and between the DiffServ domains using the concept of the NFV.

The edge routers of a DiffServ domain can be represented as OpenFlow switches and are connected to the NFV server (Cloud) via the Internet connection. The DiffServ domain controller is represented as a SDN controller, for example Floodlight, ONOS. The SDN controller is considered as the network operating system of a DiffServ domain over which the network management application runs. The SDN controller sets and manages the queues of OpenFlow switches using the OpenFlow protocol (Open Networking Foundation (ONF) 2012) and OpenFlow Management and Configuration Protocol, OF-CONFIG (Open Networking Foundation (ONF) 2014a). The Floodlight or ONOS controller is a software deployed on the NFV server (Cloud). The Cloud is a high volume physical server and the virtual machines in Figure 4-1 are used to provide the resources of the Cloud, in terms of processing, storage and networking as virtual or shared resources to the SDN controllers of DiffServ domains. Thus, it can be considered that each virtual machine in Figure 4-1 represents an SDN controller for an edge router of a DiffServ domain. The hypervisor in Figure 4-1 is necessary for managing the cloud resources between or among virtual machines. The dynamic resource management process within and between DiffServ domains should occur at regular time intervals, and the network management application concerned with the edge routers of DiffServ domains needs to be informed about the traffic state in the edge router service class queues. Figure 4-2 illustrates the operation of the dynamic resources allocation management algorithm as it is carried out within and across different DiffServ domains using the NFV concept. This management process will request, from the SDN controllers of the edge routers (OpenFlow switches), statistical information about the traffic state in their service class queues. This information will be used to configure the resources among service classes within and between DiffServ domains.

As we have SDN networks, we can monitor and manage the queues and congestion level in DiffServ domains remotely. The network statics of DiffServ domains like average service class queue delay and average service class queue size need to be sent to the NFV server through the DiffServ domain controller in order to achieve a consolidated and dynamic or automatic management for the resources across DiffServ domains. Section 4.3 presents an introduction to using the OpenFlow switches and SDN controller to build a DiffServ module based on the SDN technology.

After sending the network statistics of DiffServ domains to NFV server, the NFV server calculates the congestion levels in each service class at the ingress router of the downstream domain (the domain to which the traffic flow is moving). If there is no congestion, then the domain controller of the upstream DiffServ domain sets the service class weights based on its

configuration parameters. If there is congestion in one of the service classes at the ingress router of the downstream domain, then the NFV server calculates the scheduling rate update factor  $\beta$  and updates the scheduling rate of the equivalent service class at the egress router of the upstream domain.

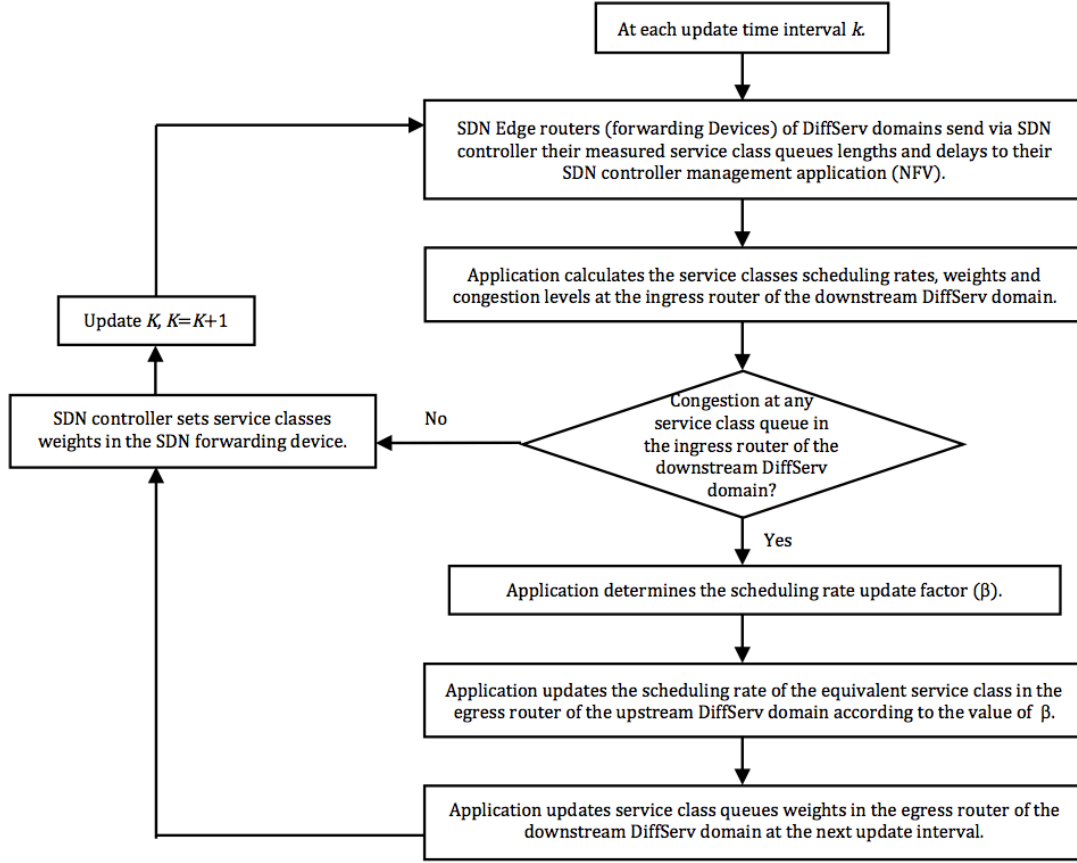


Figure 4-2, flow chart of the dynamic resources allocation management algorithm within and between DiffServ domains using the concept of the NFV.

The condition that I actually decide if the service class is congested or not depends on the depth or size of the service class queue and the low and high threshold values of service class queue. The parameters that we are using in managing resources between DiffServ domains are:

1.  $\alpha_{i(\text{Low})}$  and  $\alpha_{i(\text{High})}$ , which define the low and high threshold values for the congestion level  $\alpha_i^{k+1}$  in service class  $i$  at the next update time interval  $(k+1)$  based on the low and high threshold values of a service class queue  $T_{i(\text{Low})}$  and  $T_{i(\text{High})}$  respectively.
2. Average queue depth or size, which is used to measure the congestion level in a service class  $i$  ( $\alpha_i^{k+1}$ ) at the next update time interval  $(k+1)$  as a ratio of average service class queue size to service class buffer size.
3. Scheduling rate update factor for service class  $i$  at the next update time interval  $(k+1)$  ( $\beta_i^{k+1}$ ).

Thus, the job of the NFV server is to decide the amount of reduction in the service class scheduling rate between DiffServ domains. Figure 4-3 shows the flow chart for the procedure for managing resources across different DiffServ domains. If the service class  $i$  congestion level ( $\alpha_i^{k+1}$ ) at the ingress router of the downstream domain at the next update time interval ( $k+1$ ) is less than the low threshold value, then there is no need to reduce the scheduling rate of the equivalent service class queue at the upstream DiffServ domain. It is instead configured as the domain administrator set. If it is between the low and high threshold values  $\alpha_{i(Low)} < \alpha_i^{k+1} < \alpha_{i(High)}$ , then the scheduling rate is reduced by ( $\beta_i^{k+1}$ ) and the relation between  $\beta_i^{k+1}$  and congestion level  $\alpha_i^{k+1}$  is linear such that the value of ( $\beta_i^{k+1}$ ) is directly proportional to the value of congestion level ( $\alpha_i^{k+1}$ ). If the service class is very congested, more so than the high threshold value ( $\alpha_i^{k+1} > \alpha_{i(High)}$ ), then ( $\beta_i^{k+1}$ ) is set to the maximum value  $\beta_{max}$ . in order to dampen the service class throughput at the egress router of the upstream domain. The presented algorithm, which is run on the NFV server and uses the SDN technology, takes into consideration all the possibilities of congestion occurring in a service class queue.

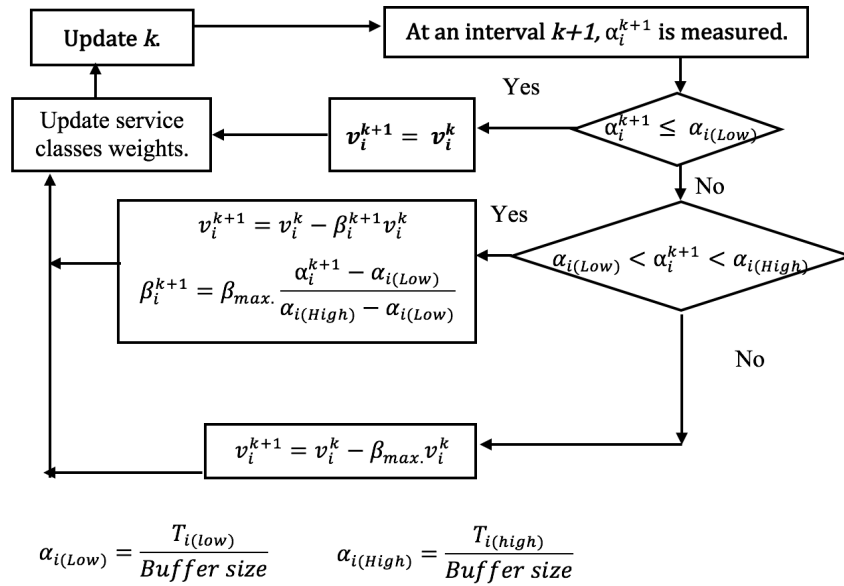


Figure 4-3, Flow chart of illustrating the procedure of managing resources between DiffServ domains.

Managing the resources across DiffServ domains can cause a few milliseconds latency delay owing to the information related to the traffic state being sent from OpenFlow switches to NFV server for processing and sending back the configuration information to configure the queues of OpenFlow switches. However, this delay can be trivial if the NFV server virtual resources which are allocated for the SDN controllers are enough for processing this information and

configuring the queues quickly. If the latency delay exceeds hundreds of milliseconds, this might cause a problem in the performance of a DiffServ domain through increasing the average End to End Delay of the application traffic and affecting the throughput of service classes. The effect of this mentioned delay is not considered in this study because of the limitation of the Network Simulator, NS3 which is used in implementing a prototype of the DRAM-NFV algorithm. Studying the effect of the latency delay, as a future work, needs to provide a physical infrastructure for implementing DRAM-NFV algorithm practically and deserves further attention.

The sliding window protocol in TCP flow ensures that a sender is not flooding a receiver by sending more packets when the receive buffer is already full, as the receiver would not be able to handle them and would need to drop these packets (Tanenbaum & Wetherall 2011). When the UDP traffic increases in a DiffServ domain (downstream domain), the DRAM-NFV algorithm reduces its resources at the egress router of the upstream domain and allocates extra resources for TCP traffic at that router. Consequently, the window size of the sliding window protocol of TCP flow and its throughput will increase and the number of TCP dropped packets will decrease.

The dynamic processes used for calculating the service class average queue lengths, service class average queue delays, service class scheduling rates, service class weights, and for measuring the service class congestion levels, and managing the service class resources across DiffServ domains will be illustrated in detail in the next chapter.

### **4.3 OpenFlow Switches and SDN Controller:**

The Open Network Foundation (ONF) divides the SDN architecture into three major planes, as shown in Figure 4-4. These are (Open Networking Foundation (ONF) 2014b):

- Data Plane: It consists of network devices such as routers, physical/virtual switches, access points etc. These devices are accessible and managed or programmed by SDN controller(s) through an interface called Controller-Data Plane Interfaces, (C-DPI). This interface is used to facilitate the dynamic configuration of OpenFlow switches in a consolidated manner. OpenFlow protocol (McKeown et al. 2008) (Vaughan-Nichols 2011) is the most common standard C-DPI used for communication between controller and a data plane device. It provides the SDN controller with information related to OpenFlow switches, such as flow statistics, and to any changes in device links or ports.



- **Controller Plane:** It comprises one or more software based SDN controller(s) to provide control functionality by monitoring the network forwarding behaviour through C-DPI. It has interfaces to enable communication among controllers using Intermediate-Controller Plane Interface, I-CPI (Lin et al. 2015), between controllers and network devices using C-DPI and between controllers and applications using Application-Controller Plane Interface, A-CPI for network security or management.

A controller consists of two essential components: Functional components (Coordinator, Virtualizer etc.) to manage controller behaviour. The other component is the control logic, which maps networking requirements of applications into instructions for network element resources (Open Networking Foundation (ONF) 2014b).

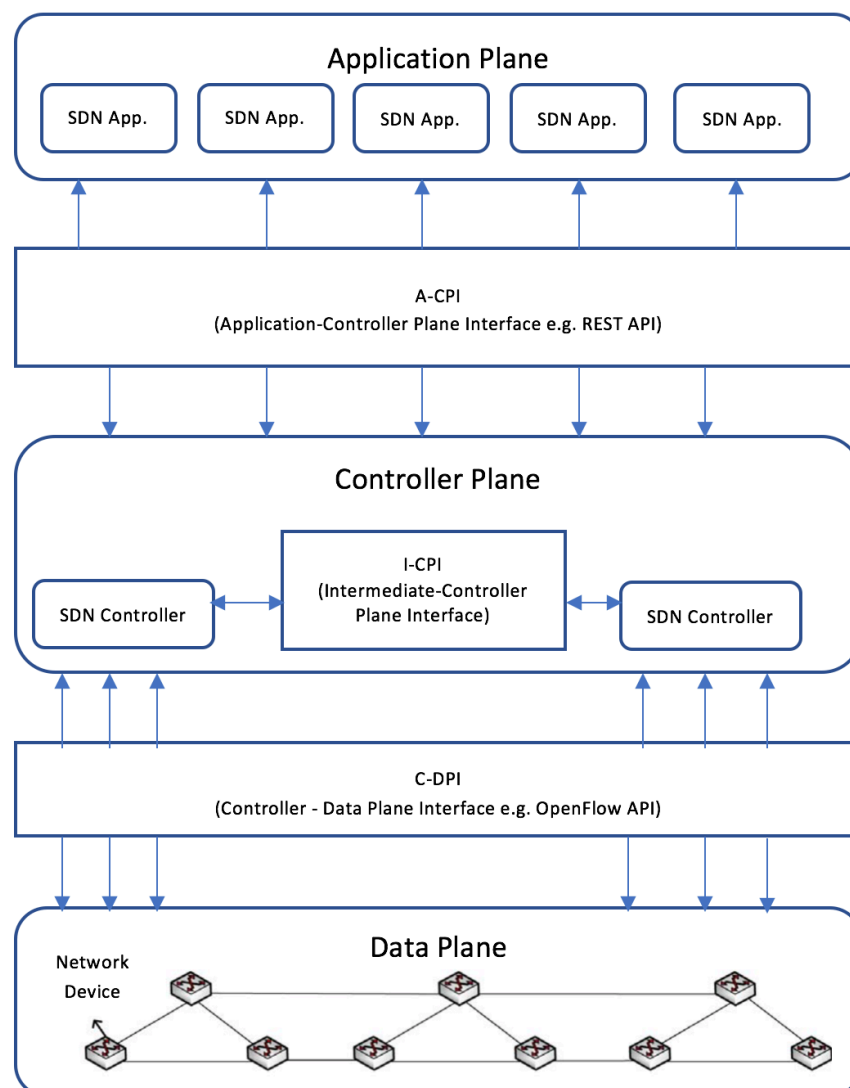


Figure 4-4, An overview of SDN architecture.

- Application Plane: An SDN application plane consists of one or more network applications (e.g. security, visualization, management etc.) that interact with controller(s) to utilize abstract views of the network for their internal decision-making processes. These applications communicate with controller(s) via an open Application-Controller Plane Interface, A-CPI (e.g. REST API).

An example of SDN forwarding device (Data Plane) is the OpenFlow switch. It is based on pipelines of flow tables defining how packets should be handled; each entry of a flow table consists of a matching rule, an action to be executed on matching packets (dropping, forwarding, etc.) and counters that maintain statistics of matching packets. These rules and actions of flow tables are installed by the SDN controller. Consequently, an OpenFlow switch can be programmed by the SDN controller to behave like a router, a switch, a firewall, or perform other roles (e.g., load balancing or traffic shaping) (Rao 2014) (Kreutz et al. 2015). The operation of an OpenFlow SDN forwarding device can be explained as follows:

When a new packet arrives at an OpenFlow switch, a path through a sequence of flow tables is selected. The lookup procedure begins in the first table and finishes either with a match or a miss (the latter occurring when no rule is found for that packet). A flow rule can be defined also by combining various matching fields. If there is no default rule, the packet will be discarded or forwarded to a non OpenFlow pipeline. Actions on matching packets can either be to forward the packet to an outgoing port, or encapsulate it and forward it to the controller, or to drop it, or to send it to the normal processing pipeline, or send it the next flow table. Data plane devices can be grouped into one or more separate controller domains. Table 4-1 illustrates the QoS-related features and changes implemented in the different versions of OpenFlow specification. Figure 4-6 shows, in diagrammatic form, an OpenFlow-enabled SDN controller with its OpenFlow switch.

Table 4-1, The QoS-related features and changes implemented in the different versions of OpenFlow specification.

OpenFlow Version	QoS features and changes implemented
OpenFlow 1.0 (Ben Pfaff, Brandon Heller 2009)	<ul style="list-style-type: none"> <li>➤ An OpenFlow enabled switch can have one or more queues depending on its ports.</li> <li>➤ There is an optional action called “enqueue<sup>2</sup>” which forwards packet through a queue attached to a port.</li> <li>➤ An OpenFlow controller can query information about queues of a switch. However, the behaviour of the queue is determined outside the scope of OpenFlow, which can be configured through OF-CONFIG protocol (Open Networking Foundation (ONF) 2014a) but requires OpenFlow 1.2 or later versions.</li> <li>➤ Also, header fields can include IP ToS, so packets can be matched against rules and their associated header fields.</li> </ul>
OpenFlow 1.1 (Ben Pfaff, Bob Lantz 2011)	<ul style="list-style-type: none"> <li>➤ Performs matching and tagging of VLAN and MPLS labels and traffic classes.</li> </ul>
OpenFlow 1.2 (Open Networking Foundation (ONF) 2011)	<ul style="list-style-type: none"> <li>➤ It has added an ability that enables a controller to query all queues in a switch.</li> <li>➤ Another QoS related improvement in this version is that it has added a max-rate queue property.</li> <li>➤ This version specifies that queues can be attached to ports and be used to map flows on them.</li> </ul>
OpenFlow 1.3 (Open Networking Foundation (ONF) 2012)	<ul style="list-style-type: none"> <li>➤ It introduces the rate limiting functionality by means of meter tables consisting of meter entries.</li> <li>➤ A meter entry includes Meter Identifier, Meter Bands and Counters. A Meter Band consists of “Band Type” (e.g. drop or remark DSCP etc.), “Rate” (e.g. kb/s burst), “Counters” and optional “Type specific arguments”, such as drop and DSCP remark, as seen in Figure 4-5. Counters may be maintained per-queue, per-meter, and per-meter band etc. They help controller collect statistics about the network. There may be one or more meter bands per meter table entry.</li> <li>➤ Meters can be combined with the optional set queue action, which associates a packet to a per-port queue to implement complex QoS frameworks such as DiffServ.</li> <li>➤ These meters allow for the rate-monitoring of traffic prior to output. More specifically, with meters, we can monitor the ingress rate of traffic as defined by a flow rule. Packets can be directed to a specific meter using the optional meter (meter_id) instruction, where the meter can then perform some operations based on the rate it receives packets.</li> </ul> <div style="text-align: center;"> <pre> graph TD     A["(a) Meter Identifier   Meter Bands   Counters"]     B["(b) Band Type   Rate   Counters   Type Specific Arguments"]     A -.- B     </pre> <p>Figure 4-5, OpenFlow 1.2-meter table.</p> </div>
OpenFlow 1.4 (Open Networking Foundation (ONF) 2013)	<ul style="list-style-type: none"> <li>➤ It presents the flow monitoring framework that allows a controller to monitor the changes done by other controllers to any subsets of the flow tables in real time. A controller can define a number of monitors, each selecting a subset of the flow tables. Each monitor includes a table id and a match pattern that defines the subset monitored. When any flow entry is added, modified or removed in one of the subsets defined by a flow monitor, an event is sent to the controller to inform it about the change.</li> </ul>
OpenFlow 1.5 (Open Networking Foundation (ONF) 2014)	<ul style="list-style-type: none"> <li>➤ It replaces the meter instruction, which was used for metering in previous versions (1.3 and 1.4), with a meter action. As a result, multiple meters can be attached to a flow entry, and meters can be used in group buckets.</li> </ul>

<sup>2</sup> This action has been renamed to “set\_queue” in OpenFlow 1.1 and later versions.

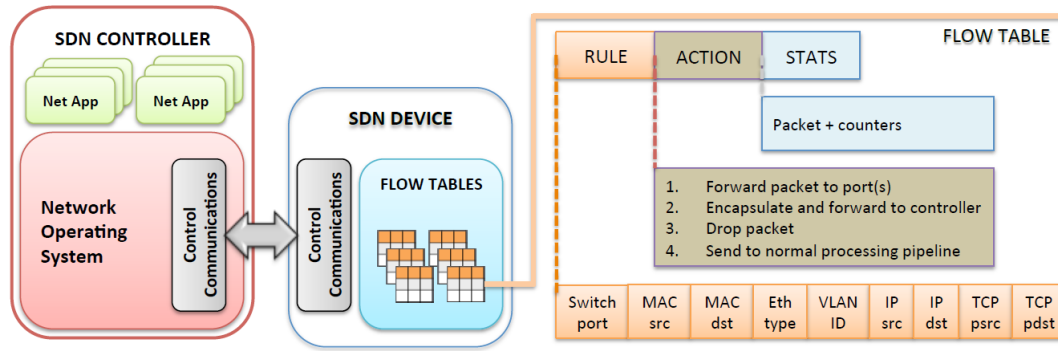


Figure 4-6, OpenFlow enable SDN controller and forwarding devices diagram.

The abovementioned specifications of OpenFlow switch, Table 4-1, does not currently provide support for queue configuration. The queue configuration in OpenFlow switch is handled by specific OpenFlow Management and Configuration Protocol, OF-CONFIG (Open Networking Foundation (ONF) 2014a) which is being standardized by ONF and Open vSwitch Database Management Protocol, OVSDB (IETF 2013) (Palma et al. 2014) which is standardized by the IETF.

OpenFlow switches can be grouped into one or more separate controller domains. The controller domain takes requests from the control application (management application) via an A-CPI (e.g. REST API) and accomplishes consolidated management and monitoring of OpenFlow switches via OpenFlow and OF-CONFIG protocols. There are two control models, which can be used in an SDN (Rao 2014) (Kreutz et al. 2015). These are the centralized controller model, whereby a single entity manages all OpenFlow switches; and the distributed controller model, which controls a cluster of nodes and controllers that are connected to each other using I-CPI or eastbound and westbound (API) interfaces to import/export data; such (API) interfaces are used to identify controllers and to provide compatibility among different kinds of controllers. The distributed controller model is more scalable and dependable than the centralized model. Figure 4-7 shows the connection of distributed controllers.

There is no standardized controller to provide queues management yet. There are many various SDN controller platforms offering different features for users. Table 4-2 summarizes some of the SDN controller projects with regards to their QoS support.

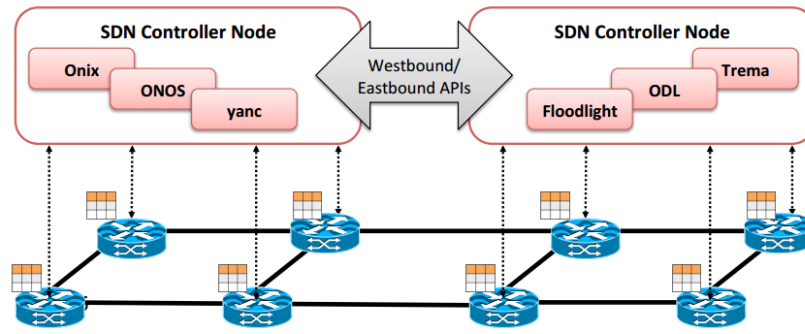


Figure 4-7, Distributed controllers' connections.

Table 4-2, Some of SDN controller projects with regards to their QoS support.

SDN Controller	Features
OpenDaylight (OpenDaylight Project 2013)	<ul style="list-style-type: none"> <li>➤ It is an open-source controller platform.</li> <li>➤ Programming language is Linux.</li> <li>➤ It consists of many other sub-projects, such as C-DPI plugins (e.g. OpenFlow, NetCONF, SNMP, and BGP) and applications (e.g. DDoS Protection and Virtualization Coordinator), complementing each other to compose a complete reference controller platform for heterogeneous networks.</li> </ul>
Open Network Operating System (ONOS Project 2015)	<ul style="list-style-type: none"> <li>➤ It is a distributed SDN control platform aimed at improving scalability, performance and availability of networks for service providers.</li> <li>➤ It is also an open-source platform.</li> <li>➤ ONOS has limited QoS support currently. It supports OpenFlow metering mechanism, but this feature is rarely implemented in existing switches. The idea behind this support is based on implementation of OpenFlow “set_queue” functionality in ONOS.</li> </ul>
Floodlight Project (Floodlight Project 2013)	<ul style="list-style-type: none"> <li>➤ It is a Java-based open-source SDN controller.</li> <li>➤ QoS module (Wallner &amp; Cannistra 2013) implemented for Floodlight controller aims at providing an application that does matching, classification, flow insertion, flow deletion, and policy handling for QoS. The module utilizes OpenFlow 1.0 “enqueue” action and the network ToS bits. It controls tracking and storing services with their DSCP values, applying policies for services class, and tracking of policies in switches.</li> <li>➤ Another QoS module implemented for Floodlight controller, the “QueuePusher” (Palma et al. 2014). It utilizes OVSDB protocol integrated with A-CPI API of Floodlight to generate appropriate queue configuration messages. The “QueuePusher” module uses a CRUD (Create, Read, Update, Delete) API, exposed by Floodlight, that allows external entities to manage Open vSwitch.</li> </ul>

#### 4.4 The Algorithm's Mathematical Model:

In this section, mathematical expressions for managing resources within the edge routers of a DiffServ domain and across different DiffServ domains at an update time interval  $k$  are developed. Figure 4-8 presents a representation of a service class queue at an update time interval  $k$ . According to queuing theory, the queuing system is considered in this model non-preemptive priority queuing (J. Virtamo 2005) such that:

- Each priority class has a separate logical queue,
- Queues are serving sequentially,
- When the scheduler/server becomes free, a packet from the head of the highest priority non-empty queue enters the scheduler,
- The packets interarrival times in the highest priority service class has a constant distribution while the packets interarrival times of other queues have exponential distributions.

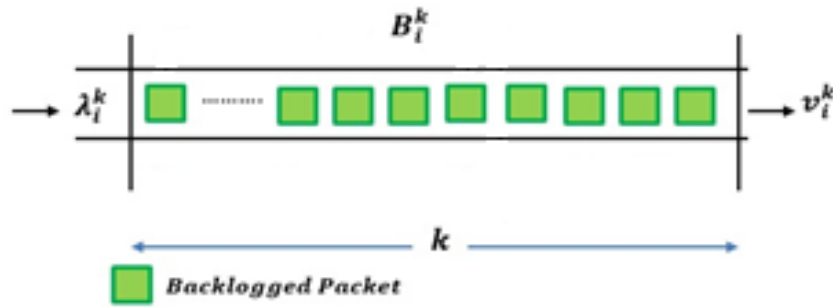


Figure 4-8, A representation of the service class queue at an update time interval  $K$ .

The proportional delay differentiation model (PDD) is a relative differentiation model. It states that “for all pairs of classes and for all time intervals  $(t, t + \tau)$  in which both  $\bar{d}_i(t, t + \tau)$  and  $\bar{d}_j(t, t + \tau)$  are defined for service classes  $i$  and  $j$  respectively, the delay ratios between classes will remain constant and controllable independently of the class load (Chin-Chang Li et al. 2000), as shown in Equation 2-2.

The instantaneous value of the queue length  $q_i^k$  in bytes for a service class  $(i)$  at the update time interval  $k$  can be expressed in terms of the amount of class  $(i)$  backlogged packets  $B_i^k$ , as shown in Equation 4-1:

$$q_i^k = B_i^k * L_i^k \quad (\text{Bytes})$$

4-1

where:

$B_i^k$  is the number of backlogged packets in the service class ( $i$ ) at the update time interval  $k$ .

$L_i^k$  is the average packet size in bytes for service class ( $i$ ) at update time interval  $k$ .

The average service class queue length at update time interval  $k$  can be measured using an exponential weighted moving formula (Sharma et al. 2014) (Wang et al. 2001) as illustrated in Equation 4-2:

$$\overline{q_i^k} = (1 - \Delta) * \overline{q_i^{k-1}} + \Delta * q_i^k$$

4-2

where  $\overline{q_i^k}$  is the average queue size at the update time interval  $k$ ,  $q_i^k$  is the instantaneous queue size at the update time interval  $k$ ,  $\overline{q_i^{k-1}}$  is the average queue size at the previous update time interval  $k-1$  and  $\Delta$  is a scaling factor. As illustrated in the next chapter, a simulation is used to choose a value of  $\Delta$  such that the fluctuation in the instantaneous queue length is reduced.

From Figure 4-8, the packet ( $N$ ) in the service class ( $i$ ) queue suffers a delay ( $D_N$ ) equal to the aggregate delays caused by the other packets that are awaiting service. This delay can be measured by taking the difference between the packet's dequeue time and its enqueue time, as demonstrated in Equation 4-3:

$$D_N = t_{Deq.}^N - t_{Enq.}^N$$

4-3

where  $D_N$  is the delay of packet ( $N$ ) which has a dequeue time of  $t_{Deq.}^N$  and an enqueue time of  $t_{Enq.}^N$ .

The average queue delay  $d_i^k$  for the service class ( $i$ ) at the update time interval  $k$  represents the ratio of the aggregate cumulative delays of the backlogged packets to the total number of backlogged packets in the queue ( $i$ ), as shown in Equation 4-4:

$$d_i^k = \frac{\sum_{j=1}^B D_j}{B}$$

4-4

where  $B$  represents the total number of backlogged packets in the service class ( $i$ ) queue.

The scheduling rate  $v_i^k$  of service class ( $i$ ) at an update time interval  $k$  represents the ratio of the average service class queue length to the average service class delay, as shown in Equation 4-5:

$$v_i^k = \frac{\overline{q_i^k}}{\overline{d_i^k}}$$

4-5

From Equation 2-2, by substituting Equation 4-5 with respect to service classes ( $i$ ) and ( $j$ ) respectively we get Equation 4-6:

$$\frac{v_i^k}{v_j^k} = \frac{\frac{1}{\delta_i} \overline{q_i^k}}{\frac{1}{\delta_j} \overline{q_j^k}}$$

4-6

From Equation 4-6, it can be seen that the service class scheduling rate can be changed dynamically enabling it to maintain the delay differentiation parameter ratios between classes relative to the predefined specifications of the delay differentiation parameter for each class ( $\delta$ ) and the amount of backlogged packets in the service class queues. If there are three service classes ( $SC_1$ ,  $SC_2$  and  $SC_3$ ) in a DiffServ domain and their delay differentiation parameters are ( $\delta_1$ ,  $\delta_2$ ,  $\delta_3$ ) respectively, such that the  $SC_1$  is considered the highest priority service class,  $SC_2$  is considered the medium priority service class and  $SC_3$  is considered the lowest priority service class. The scheduling rates for these service classes ( $v_{SC1}^k$ ,  $v_{SC2}^k$  and  $v_{SC3}^k$ ) at an update time interval  $k$  can be calculated as shown in Equation 4-7 and Equation 4-8. For the highest priority service class  $SC_1$ ; which occupy the required bandwidth for its traffic, its scheduling rate at an update time interval  $k$  can be calculated as below:

$$v_{SC1}^k = \frac{\overline{q_{SC1}^k}}{\overline{d_{SC1}^k}}$$

4-7

For the medium and lowest priorities service classes  $SC_2$ ,  $SC_3$  respectively, using the relation between service class scheduling rates between any two service classes, Equation 4-6, the scheduling rates for these classes at an update time interval  $k$ :

$$v_{SC2}^k = \frac{\delta_1}{\delta_2} * v_{SC1}^k * \frac{\overline{q_{SC2}^k}}{\overline{q_{SC1}^k}} \quad \text{and} \quad v_{SC3}^k = \frac{\delta_1}{\delta_3} * v_{SC1}^k * \frac{\overline{q_{SC3}^k}}{\overline{q_{SC1}^k}}$$

4-8



Where:

- $(\delta_1, \delta_2 \text{ and } \delta_3)$ : Delay differentiation parameters for service classes (SC<sub>1</sub>, SC<sub>2</sub> and SC<sub>3</sub>) respectively, which are configurable parameters set by the DiffServ domain administrator.
- $(\overline{q_{SC1}^k}, \overline{q_{SC2}^k} \text{ and } \overline{q_{SC3}^k})$ : Average queue size for service classes (SC<sub>1</sub>, SC<sub>2</sub> and SC<sub>3</sub>) respectively at an update time interval  $k$ . It can be calculated using Equation 4-2.
- $d_{SC1}^k$ : Average SC<sub>1</sub> queue delay at an update time interval  $k$ . It can be calculated using Equation 4-4.

For the multi core router DiffServ domains in scenarios 2 and 3 in sections 6.3.2 and 6.3.3 respectively, each out-port of a DiffServ router contains a number of service class queues. The number of these queues is equivalent to the number of defined service classes within that DiffServ domain. All the service class queues at each out-port of all the ingress routers of a multi core DiffServ domain have the same scheduling rate configuration. This rate can be set by selecting a port with the maximum average service class queue length and a port with the minimum average SC<sub>1</sub> queue delay because the service class scheduling rate is directly proportional to the average service class queue length and inversely proportional to the average service class queue delay. These procedures lead to the maximization of throughput for the highest priority service class and the other classes within a multi-core DiffServ domain.

To illustrate the procedure of calculating the maximum average queue size for a service class  $i$  ( $\overline{Q_{SCi}^k}$ ) for a router consists from  $j$  ports at an update time interval  $k$ ; the average queue size for a service class  $i$  ( $\overline{q_{SCi}^k}$ ) at each port of a multi-core router at an update time interval  $k$  is calculated first using Equation 4-2 then the maximum average queue size for this class ( $\overline{Q_{SCi}^k}$ ) at all ports of a multi-core router at an update time interval  $k$  can be calculated as below, Equation 4-9:

$$\overline{Q_{SCi}^k} = \max. \left( \overline{q_{SCi1}^k}, \overline{q_{SCi2}^k}, \dots, \overline{q_{SCij}^k} \right)$$

4-9

where  $j$  represents the number of ports of a multi-core router.

The same procedure is applied when calculating the minimum average SC<sub>1</sub> queue delay ( $D_{SC1}^k$ ) at all ports of a multi-core router and at an update time interval  $k$ , Equation 4-10:

$$D_{SC1}^k = \min. \left( \overline{d_{SC11}^k}, \overline{d_{SC12}^k}, \dots, \overline{d_{SC1j}^k} \right)$$

4-10

Where  $j$  represents the number of ports of a multi-core router.

The service classes scheduling rates for the multi-out-port ingress routers (or multi-core router) at an update time interval  $k$  with considering the maximum average service class queue size and minimum average SC1 queue delay can be illustrated as shown in Equation 4-11 and Equation 4-12:

For the highest priority service class SC1, considering the maximum average SC1 queue size and minimum average SC1 queue delay, Equation 4-11:

$$v_{SC1}^k = \frac{\overline{Q_{SC1}^k}}{\overline{D_{SC1}^k}}$$

4-11

For the medium and lowest priorities service classes SC2, SC3 respectively, using the relation between service class scheduling rates between any two service classes, Equation 4-6 and the maximum average service class queue size. The scheduling rates for these classes at an update time interval  $k$ :

$$v_{SC2}^k = \frac{\delta_1}{\delta_2} * v_{SC1}^k * \frac{\overline{Q_{SC2}^k}}{\overline{Q_{SC1}^k}} \text{ and } v_{SC3}^k = \frac{\delta_1}{\delta_3} * v_{SC1}^k * \frac{\overline{Q_{SC3}^k}}{\overline{Q_{SC1}^k}}$$

4-12

where:

- $\overline{Q_{SC1}^k}$  is the maximum average service class (SC<sub>1</sub>) queue length at any port of the multi-port ingress routers at an update time interval  $k$ .
- $\overline{D_{SC1}^k}$  is the minimum average service class (SC<sub>1</sub>) queue delay at any port of the multi-port ingress routers at an update time interval  $k$ .
- $\overline{Q_{SC2}^k}$  and  $\overline{Q_{SC3}^k}$  are the maximum average service class (SC<sub>2</sub> and SC<sub>3</sub>) queue lengths respectively at any port of the multi-port ingress routers at an update time interval  $k$ .

The weights used by the WFQ scheduling algorithm can be changed dynamically within an update time interval  $k$  by changing the scheduling rates of the service classes according to Equation 4-8. The weight of a service class represents its share of the router's out link

bandwidth (BW). The weight can be determined by calculating the Greatest Common Divisor (GCD) of the normalised percentage values of the rates of all the service classes.

The normalised percentage value  $\overline{SC}_i^k$  of a service class' ( $i$ ) scheduling rate can be calculated as shown in Equation 4-13:

$$\overline{SC}_i^k = \frac{v_{sci}^k}{\sum_{j=1}^M v_{scj}^k} * 100\%$$

4-13

The weight of service class  $i$  ( $W_i^k$ ) within the update time interval  $k$  can be calculated as shown in Equation 4-14:

$$W_i^k = \left. \begin{array}{l} \frac{\overline{SC}_i^k * \text{Output link capacity (bits)}}{100 * X * \text{Average packet size in queue (bits)}} \\ X = \text{GCD} (\overline{SC}_1^k, \overline{SC}_2^k, \dots, \overline{SC}_M^k) \end{array} \right\}$$

4-14

where  $M$  is the number of service class queues in the DiffServ domain.

All the core routers within the DiffServ domains forward the service class packets according to the fixed weights. These weights are configured by the domain administrator and chosen such that the priority levels of the service classes are maintained.

In order to manage resources across different DiffServ domains in cases where any service class queue at the ingress router of the downstream domain suffers from traffic congestion, the congestion level ( $\alpha_i^{k+1}$ ) for the service class queue  $i$  within an update time interval  $(k+1)$  is calculated as shown in Equation 4-15:

$$\alpha_i^{k+1} = \frac{\overline{q}_i^k}{\text{Buffer Size}}$$

4-15

Each service class queue has threshold parameters ( $T_{i(\text{High})}$  and  $T_{i(\text{Low})}$ ). These two parameters define the lower and higher boundaries of congestion levels in the service class queue  $i$ .

The lower boundary ( $\alpha_{i(\text{Low})}$ ) is a configurable parameter, it represents the ratio of the Low congestion threshold parameter ( $T_{i(\text{Low})}$ ) for the service class queue  $i$  to the service class buffer size as shown in Equation 4-16:

$$\alpha_{i(Low)} = \frac{T_{i(Low)}}{\text{Buffer Size}}$$

4-16

The higher boundary ( $\alpha_{i(High)}$ ) is also a configurable parameter, it represents the ratio of the Higher congestion threshold parameter ( $T_{i(High)}$ ) for the service class queue  $i$  to the service class buffer size as shown in Equation 4-17:

$$\alpha_{i(High)} = \frac{T_{i(High)}}{\text{Buffer Size}}$$

4-17

The measured congestion level  $\alpha_i^{k+1}$  is compared with the two threshold parameters  $\alpha_{i(High)}$  and  $\alpha_{i(Low)}$  of that service class queue. If the congestion level is below  $\alpha_{i(Low)}$  then there is no need to reduce the scheduling rate for the equivalent service class queue at the egress router of the upstream DiffServ domain. If  $\alpha_i^{k+1}$  is within the congestion threshold range  $\alpha_{i(Low)}$  and  $\alpha_{i(High)}$ , then the scheduling rate ( $v_i^{k+1}$ ) for the equivalent service class queue  $i$  at the egress router of the upstream DiffServ domain is reduced by a congestion rate update factor ( $\beta_i^{k+1}$ ) at the next update time interval ( $k+1$ ). For multi-port ingress routers, the measured congestion level ( $\alpha_i^{k+1}$ ) is set to that of the port with the maximum average service class queue length, i.e.: assume a multi-core router consists from  $j$  ports, the congestion level ( $\alpha_i^{k+1}$ ) for a service class queue at each port is calculated first using Equation 4-15 at an update time interval  $k$  then the maximum congestion level ( $\alpha_{i(max)}^{k+1}$ ) for this service class at all ports and at an update time interval  $k$  as shown in Equation 4-18.

$$\alpha_{i(max)}^{k+1} = \max(\alpha_{i1}^{k+1}, \alpha_{i2}^{k+1}, \dots, \alpha_{ij}^{k+1})$$

4-18

where  $j$  represents the number of ports of a multi-core router.

The scheduling rate update factor ( $\beta_i^{k+1}$ ) for the service class queue  $i$  within an update time interval ( $k+1$ ) is chosen such that it is linearly proportional to the measured congestion level ( $\alpha_i^{k+1}$ ). This means that the congestion update factor ( $\beta_i^{k+1}$ ) increases with increments in the congestion level ( $\alpha_i^{k+1}$ ) when  $\alpha_{i(Low)} < \alpha_i^{k+1} < \alpha_{i(High)}$ . Figure 4-9 illustrates the relationship between the congestion level ( $\alpha_i^{k+1}$ ) and the scheduling rate update factor ( $\beta_i^{k+1}$ ).

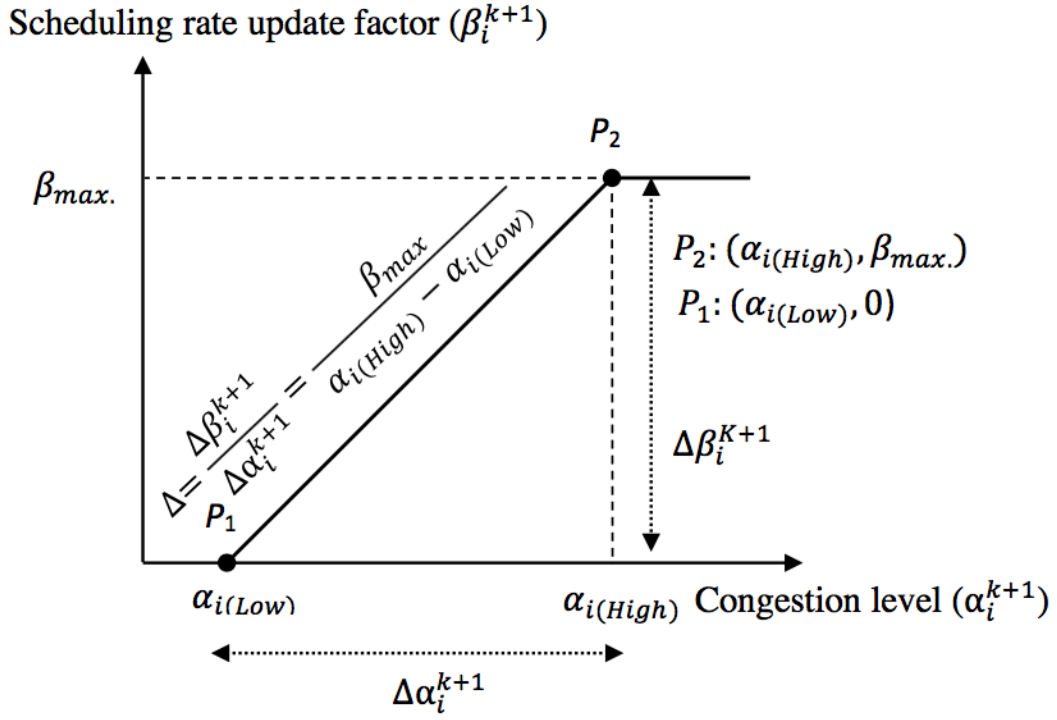


Figure 4-9, Relation between the congestion level and the scheduling rate update factor.

In order to develop an expression for the scheduling rate update factor ( $\beta_i^{k+1}$ ) for  $\alpha_{i(Low)} < \alpha_i^{k+1} < \alpha_{i(High)}$ , the line equation above is taken to represent an expression for this factor ( $\beta_i^{k+1}$ ). This expression can be formulated as below:

$$y = mx + b$$

$$\beta_i^{k+1} = m\alpha_i^{k+1} + b$$

4-19

Where  $y$  represents the scheduling rate update factor ( $\beta_i^{k+1}$ ) for the service class queue  $i$  within an update time interval  $(k+1)$ ,  $x$  represents the congestion level, ( $\alpha_i^{k+1}$ ) for the service class queue  $i$  within an update time interval  $(k+1)$ ,  $m$  is the slope of the line and  $b$  is a constant which represent the line's intersection point with the  $y$  axis. The slope of this line  $m$  can be expressed as illustrated in Equation 4-20:

$$m = \frac{\Delta y}{\Delta x} = \frac{\Delta \beta_i^{k+1}}{\Delta \alpha_i^{k+1}} = \frac{\beta_{max.}}{\alpha_{i(High)} - \alpha_{i(Low)}}$$

4-20

By substituting either  $P_1$  or  $P_2$  of Figure 4-9 and the value of slope  $m$  in Equation 4-19, the scheduling rate update factor ( $\beta_i^{k+1}$ ) for  $\alpha_{i(Low)} < \alpha_i^{k+1} < \alpha_{i(High)}$  can be expressed as shown in Equation 4-21:

$$\beta_i^{k+1} = \beta_{max} \cdot \frac{\alpha_i^{k+1} - \alpha_{i(Low)}}{\alpha_{i(High)} - \alpha_{i(Low)}} \quad 4-21$$

If the congestion level exceeds the  $\alpha_{High}$ , then the scheduling rate update factor ( $\beta_i^{k+1}$ ) is set to a fixed configurable value ( $\beta_{max}$ ).

where, ( $T_{i(Low)}$ ) and ( $T_{i(High)}$ ) are the low and high threshold parameters for the service class queue as configured by the DiffServ domain administrator.  $v_i^k$  represents the scheduling rate for the service class queue at the egress router of the upstream domain according to its traffic state at the current update interval  $k$ .  $v_i^{k+1}$  represents the scheduling rate for the service class queue at the egress router of the upstream domain according to the traffic states in the upstream and downstream DiffServ domains at the next update interval  $k+1$ .

By reducing the scheduling rate of the congested service class at the egress router of the upstream domain, the service class queue's normalised values and weights are also changed. Consequently, the bandwidth resource of the link that connects the DiffServ domains is redistributed in a different way such that it may be possible to improve the performance of some service classes in the case of traffic congestion in a downstream DiffServ domain.

#### 4.5 Chapter Summary:

This chapter presents the DRAM-NFV algorithm for managing resources within and across different DiffServ domains. An overview of the framework that can be used to enable the use of this algorithm has been provided. In addition, all the mathematical expressions that are related to the resources management within Proportional Delay DiffServ routers and across different administration Proportional Delay DiffServ domains have been introduced in this chapter. These mathematical expressions have been used to create a DiffServ domain module and implement the algorithm test scenarios using a simulation environment, as we will see in the next two chapters.

# **Chapter Five**

## **DRAM-NFV Algorithm Implementation using a Simulated Environment**

### **5.1 Introduction:**

The procedures related to the implementation of the DiffServ module and to the application of the DRAM-NFV algorithm within a simulated environment are described in this chapter. The network simulation platform NS3 has been used for this research. Three main application programs are built from scratch using the NS3 module library and the C++ language. These include the network queuing program which represents the differentiation queuing model for the proportional delay DiffServ domains, the application traffic program which generates different types of network traffic and the network topology program that represents the topologies of the test scenarios. Code listings of the algorithm implementation is found in Appendix A-3. All these application programs work together in order to facilitate an environment in which a prototype of the (DRAM-NFV) algorithm could be constructed. Application traffic will be discussed in the next chapter while this chapter covers the DiffServ network queuing simulation model.

### **5.2 The (NS3) Network Simulation Program:**

The Network Simulator (NS3) is one of a number of discrete event network simulators available. The term discrete event network simulator means a framework within which the process of codifying the behaviours of a complex system as a set of well-defined events takes place which allows for their simulation by the execution of these events in a scheduled manner. Conceptually, the simulator keeps track of a number of events that are scheduled to execute at a time prescribed by other events. The job of the simulator is to execute the events in a logical order. Once the completion of an event occurs, the simulator will move to the next event (or will exit if there are no more events to be processed).

There are many available discrete events simulators such as Opnet, Matlab, NS2, NS3, and etc. The NS3 simulator is an open source piece of software, licensed under the GNU GPLv2 license. It is available publicly for research and dedicated to research and educational purposes. The NS3 simulation software supports research on both IP and non-IP based networks. It should be noted that NS3 is different from NS2, and NS3 does not support NS2 models. NS2 (NS-2 lists

contributors. 1995) is implemented using a combination of oTCL (for scripts describing the network topology) and C++ (The core of the simulator), making the debugging process complex, while NS3 is written in C++ and Python programming languages. These latter programming languages can be optionally used as an interface, making the debugging process easier than it is for NS2; a knowledge of just C++ is sufficient, in terms of languages. The NS3 simulator version (3.23) is used to simulate the test scenarios that will be described in the next chapter; this is the current version of NS3, released in May 2015. There are some educational materials available for those who wish to learn about NS3 and the C++ programming language. These are (NS3 Tutorial 2015), (NS3 Manual 2015), (NS3 Model library 2015),(NS3 Doxygen 2015),(NS3 software users group) and (C++ Programming Language 2016).

### **5.3 Queue Model Simulation:**

One of the challenges of NS3 is that it does not cover all the functions needed to represent the functionalities of a DiffServ router as required for this research. These functionalities include:

- I. sharing multiple service class queues to one out link;
- II. packets classification, dropping and scheduling mechanisms for service class queues; and
- III. DiffServ queue and link performance measurements in terms of average queue delays, average queue lengths, average queue scheduling rates, DiffServ link utilization and average end-to-end delay metrics for service classes traffic.

Building multiple queues using standard (NS3::Queue Class Reference) is required multiple output link interfaces; this means that each queue needs an output link interface. This is not acceptable when implementing a DiffServ domain. In order to implement the test scenarios of sections 6.3.1, 6.3.2 and 6.6.3 using the NS3 simulation environment, a new queuing model had to be designed covering all of the abovementioned functions but using some of the classes defined in the NS3 simulator and the C++ programming language packages. This new queueing model is called “RDWQueue”, and it forms the main part of the specifically written simulation code. The (RDWQueue) model is inherited or derived from the NS3 queue class reference in order to provide a consistent way to enqueue and dequeue packet from service class sub queues or queues.

This new queueing module had to be defined within the modules of the NS3 package. The procedures for adding a new module to the NS3 package is illustrated in (NS3 project 2010). Appendix A-2.1 illustrates the NS3 class references which are used to build the RDWQueue model and their purposes.



The packets in RDWQueue will be banded into sub queues according to their service class tags. All these bands or sub queues<sup>3</sup> must share one out-link in order to forward their traffic. For each service class sub queue, there are configurable parameters which describe the behaviour of that service class sub queue, these parameters are as follows.

- I. **Buffer Size** - the maximum capacity of the service class sub queue in bytes.
- II. **Lower and Higher threshold queue length parameters** – these parameters specify the lower and higher boundaries of the service class queue length (in bytes) to the WRED packet queue management algorithm.
- III. **Lower and Higher packet drop probabilities** – these two parameters specify the threshold values of packet drop probability to the WRED packet queue management algorithm.
- IV. **Lower and Higher threshold of congestion levels** – these parameters specify the lower and higher boundary values for the service class congestion condition and are used in managing resources across different DiffServ domains.

To achieve a differentiation among these bands or sub queues in the RDWQueue model, a packet scheduling scheme (Scheduler) is also implemented in order to forward packets from these sub queues to the one out-link. This scheduler is based on calculating the normalized percentage values for the service classes scheduling rates as explained in the previous chapter. The weights for the implemented DiffServ scheduler can be configured dynamically in the case of the edges routers of a DiffServ domain or pre-configured as fixed weights in the case of the core routers of a DiffServ domain. The most important functions that the RDWQueue model can perform are:

### 5.3.1 Initialising the Simulation:

An object of the RDWQueue model must be instantiated when creating the queues for the test scenarios. The constructor function (RDWQueue::RDWQueue(int id)) is used for this purpose. Constructors are typically used also (as here) to initialize member variables of the class to appropriate default values; a destructor is executed when an object of a class is destroyed. In the constructor function, the periodic processes of calculating the scheduling rates for the service class queues, producing statistical reports about the queues, obtaining the out-link service classes traffic utilizations of the DiffServ router and the number of forwarded packets

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<sup>3</sup> In this thesis, the terms sub-queue and queue are used frequently. Both have the same meaning: the service class queue.

from sub queues – all in relation to a standard interval  $k$  - are also defined and need to be identified and scheduled-in by the NS3 scheduler during the simulation run.

### 5.3.2 Traffic Queuing:

When an incoming packet arrives at the DiffServ router, it must be accommodated or queued in a suitable service class queue, compatible with its service type. The (RDWQueue::DoEnqueue (Ptr<Packet> p)) function performs the queuing process of a DiffServ router.

The Enqueue function takes the packet from the input link of the RDWQueue. It inspects the packet service class tag using the RDWQueue classification function (Classify) and inserts it into the appropriate sub queue if the service class (x) sub queue size (SCxByteLength) does not exceed the lower threshold queue length parameter (REDSCxTLOW) for that service class sub queue (x). After that, the sub queue state of the service class needs to be updated and the sub queue size (SCxByteLength) for service class (x) should be increased by the size of the inserted packet. In addition, the unique ID and the simulation time of the inserted packet must be stored into (SCxEnqueueTime) so that the packet delay can be calculated later. If the packet is unknown, then the Enqueue function insert it in any service class queue depending on the available resources in these queues.

If the service class (x) sub queue size exceeds the lower or higher threshold queue length parameters for that service class sub queue (REDSCxTLOW) or REDSCxTHIGH) respectively, then random numbers of service class packets will be dropped. Based on the WRED configuration of the Cisco<sup>TM</sup> 4 10000 series QoS routers (Cisco 2013), the lower and higher drop probabilities (REDSCxLDROP or REDSCxHDROP) of each service class queue should not exceed on 0.1. An exponential random distribution function called (GenRand) is designed using (NS3::Exponential Random Distribution) to generate a random number (Ran) ranging between 0 and 0.5 to determine how many packets should be dropped from a service class queue. If (Ran) is outside the range defined by the Lower or Higher packet drop probability values (0.1), then the packet will be inserted into the service class sub queue, otherwise it will be dropped and the function will increase the counter that counts the number of dropped packets (SCxdropcount) for that service class (x) by one. In addition, this function also drops packet if the service class (x) sub queue size (SCxByteLength) exceeds the service class (x) buffer size (SCxBYTEBUFFER). Figure 5-1 illustrates the flow chart of the Enqueue function and the code of this function is shown in page 147 of Appendix A-3.

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<sup>4</sup> Cisco<sup>TM</sup> is a trade mark of Cisco Systems, Inc. and its affiliates.

- There are three defined service class queues (SC<sub>1</sub> queue, SC<sub>2</sub> queue, SC<sub>3</sub> queue).
- x represents the QoS tag or band of a received packet. It could be SC<sub>1</sub> or SC<sub>2</sub> or SC<sub>3</sub> or unknown.

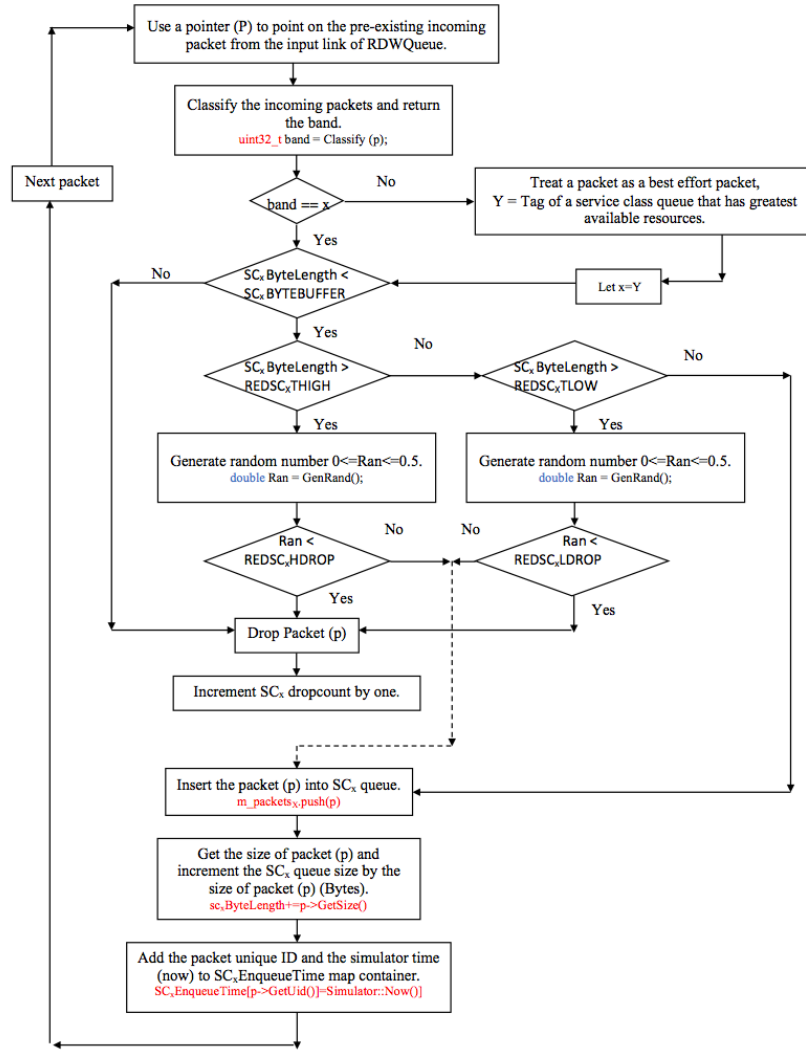


Figure 5-1, Flow chart illustrating the enqueueing process.

### 5.3.3 Traffic Scheduling:

A scheduler is needed for forwarding the packets from the service class queues to the out link of the DiffServ router based on the weights that are allocated for the service classes. The (RDWQueue::DoDequeue (void)) function performs the scheduling function of a DiffServ router. It forwards packets from the service class sub queues to the out link of a DiffServ router based on their weights. These weights are configured either dynamically for the edge routers of a DiffServ domain or pre-configured as fixed weights for the core routers of a DiffServ domain. The weights represent the number of packets that should be forwarded from the service class sub queues. The procedures for specifying the weights of the service class sub queues are explained later in this chapter. Where there are three service classes within a

DiffServ domain, this forwarding scheme keeps to the following sequence for packet forwarding ( $SC_1, SC_2, SC_3, SC_1, SC_2, SC_3, \dots$ ). After forwarding a packet from a service class sub queue, the sub queue state must be updated; the sub queue size ( $SCxByteLength$ ) for service class (x) should be decreased by the size of the forwarded packet in bytes. Moreover, the unique ID and simulation time of the send packet event should be stored into ( $SCxDequeueTime$ ) so that the packet delay can be calculated later. The DiffServ router out link utilization for service class (x) should also be increased by the size of the sent packet in bytes. This scheduler must always be active; if there is no packet in a service class sub queue then the scheduler should move to the next sub queue to forward its packets according to their weights. Figure 5-2 illustrates the flow chart of the Dequeue function and the code of this function is shown in page 156 of Appendix A-3.

#### **5.3.4 Classification of Incoming Packets:**

The incoming, marked packets should be classified before being inserted into their dedicated queues. The function ( $RDWQueue::Classify(ptr<const Packet> p)$ ) is used to classify incoming packets. The code of this function is shown in page 162 of Appendix A-3.

- If the DiffServ domain supports three service classes (SC):  $SC_1$ ,  $SC_2$ , and  $SC_3$ .

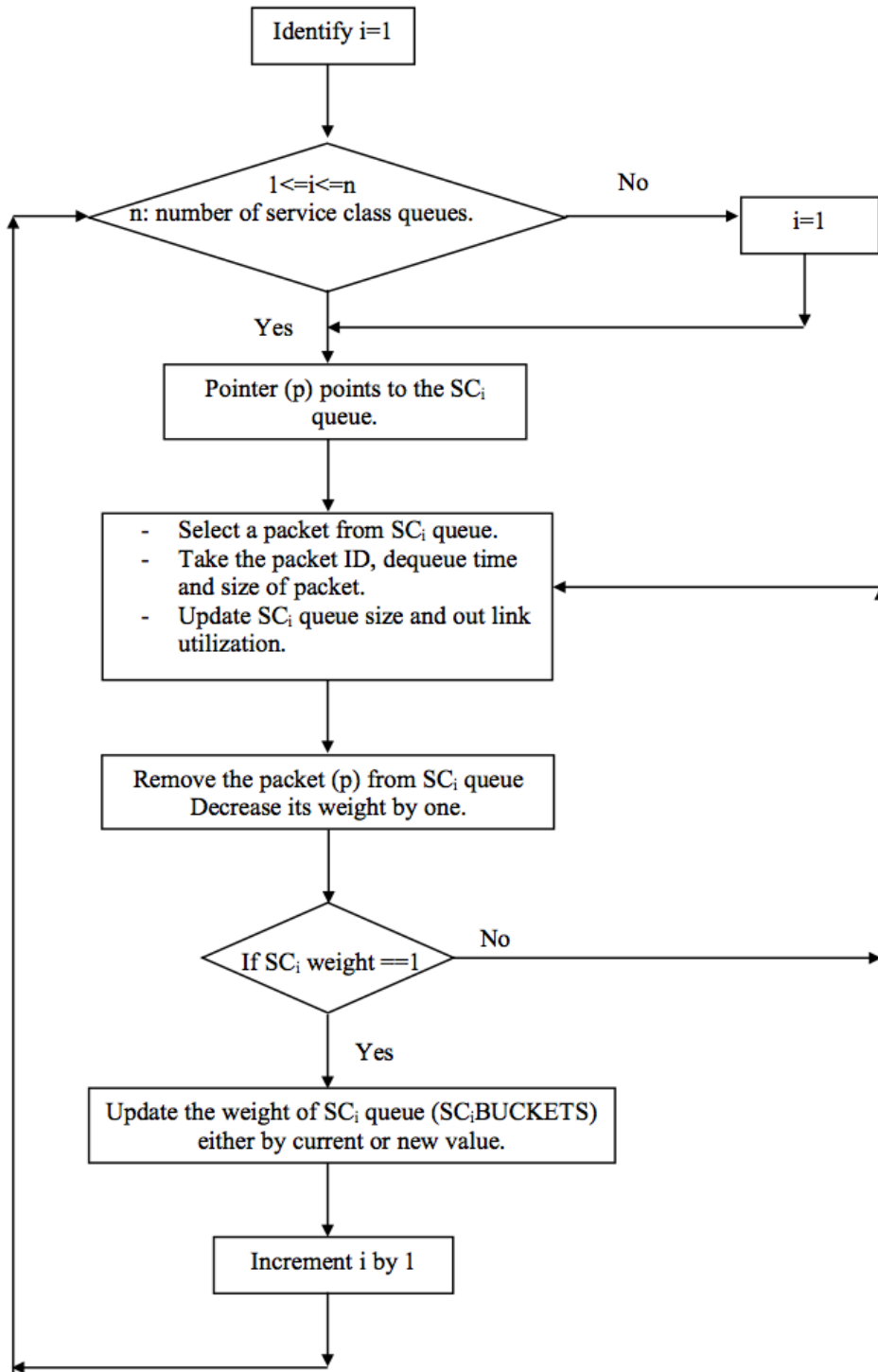


Figure 5-2, Flow chart illustrating the scheduling (dequeuing) process.

### 5.3.5 Calculation of Service Classes' Scheduling Rates:

The procedure for calculating service classes' scheduling rates must be performed by the edge routers of a DiffServ domain. The function `RDWQueue::CalculateSchedulingRateByte` performs this task. Within an interval of  $k$  sec, the function measures the average queue length, the average queue delay and the scheduling rate for each service class sub queue by using the mathematical equations Equation 4-2, Equation 4-3, Equation 4-4 and Equation 4-8 that were given in the previous chapter.

#### 5.3.5.1 Measuring the Average Service Class Queue Length:

The average service class queue length can be calculated by using the mathematical equation Equation 4-2 which was explained in the previous chapter and as illustrated in the flow chart of Figure 5-3. The code of this process is shown in page 167 of Appendix A-3.

*If the DiffServ domain supports three service classes, then  $x$  could be  $SC_1$  or  $SC_2$  or  $SC_3$ .*

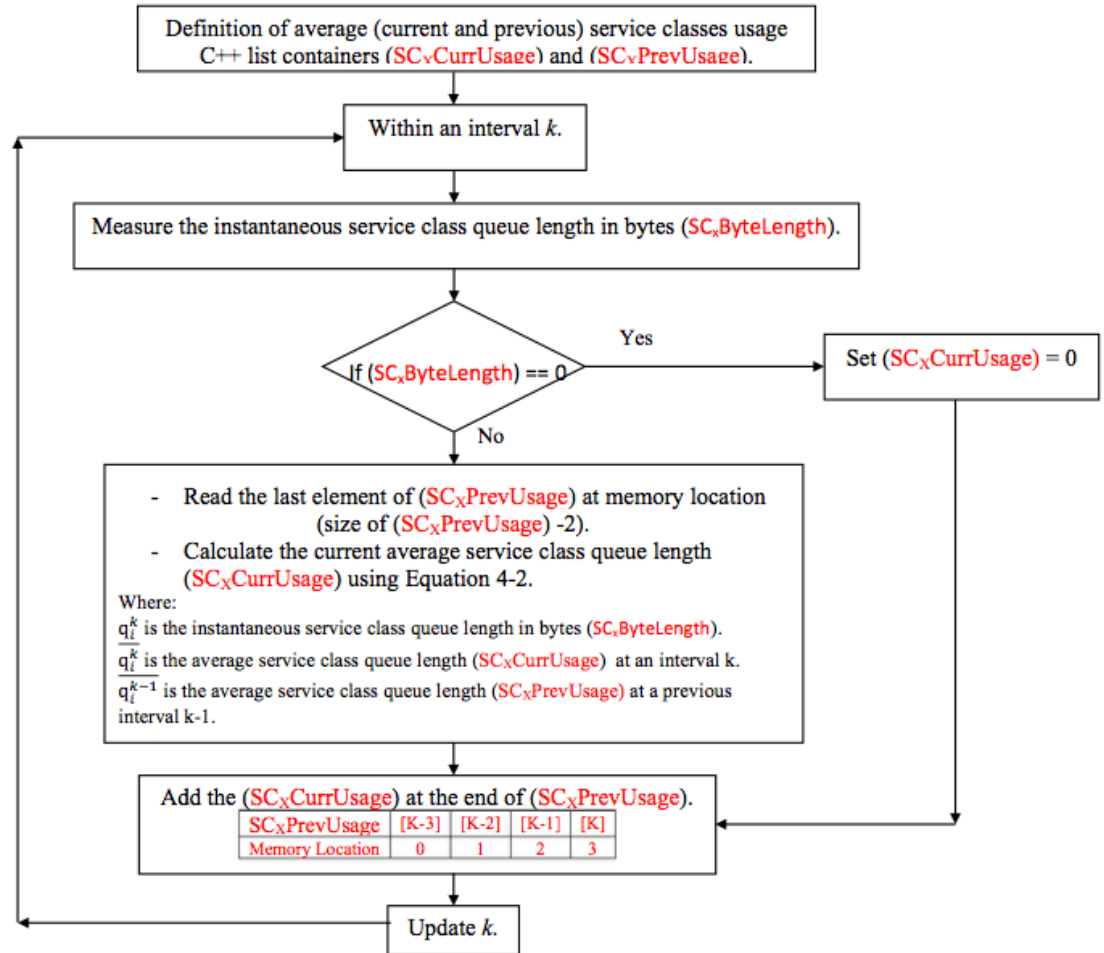


Figure 5-3, Flow chart illustrating the process of calculating the average queue length for service class queues.

### 5.3.5.2 Determining the Optimum Scaling Factor ( $\Delta$ ):

An experiment was conducted, using the NS3 simulator, to estimate an optimum value for the scaling factor  $\Delta$  used within the estimated average service class queue length Equation 4-2. The value of  $\Delta$  should be between 0 and 1. In the experiment, four values of  $\Delta$  were tried (0.9, 0.5, 0.1 and 0.01) in order to find an estimated average service class queue length from the instantaneous queue length value. The result of this experiment is illustrated in Figure 5-4. The dashed curve represents the instantaneous value of the service class queue length while the other curves represent the estimated average service class queue length at the above indicated values of  $\Delta$ . When  $\Delta$  is equal to 0.9 or 0.5, the estimated average queue length fluctuates and this will affect the differentiation pattern between service classes through the destabilizing of the scheduling rates for the service classes. When  $\Delta$  equals 0.01, the estimated average queue length does not reflect the current queue length, although here the value does not fluctuate. When  $\Delta$  is equal to 0.1, the fluctuation in the estimated average queue length is reduced and the result of the equation reflects an approximate view of the current service class queue length. Thus,  $\Delta=0.1$  is used in the simulation as an optimum value for the scaling factor  $\Delta$  for the estimated average service class queue length Equation 4-2.

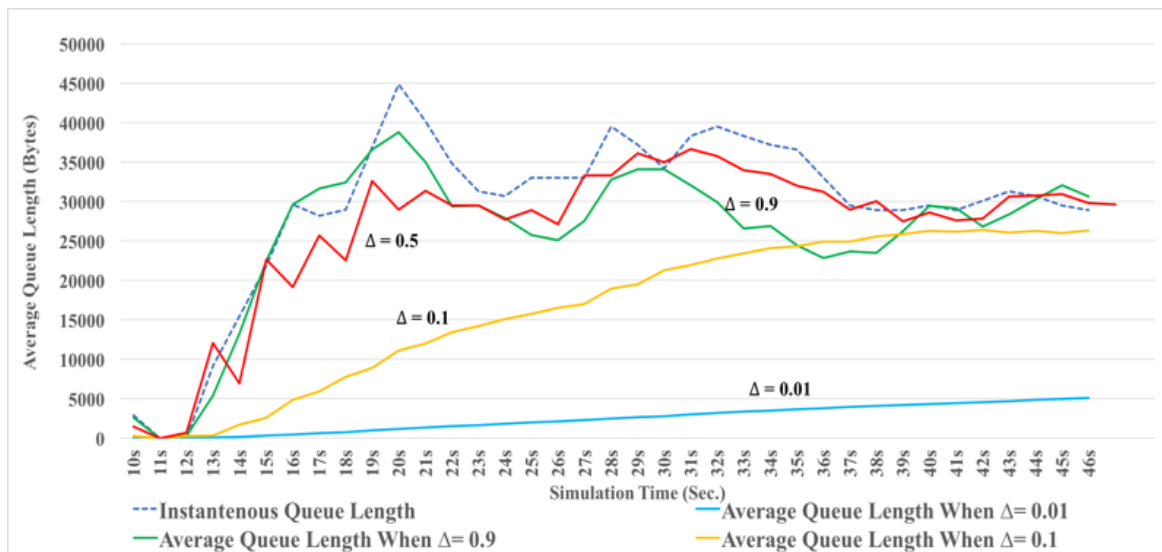


Figure 5-4, Estimating an optimum value for the scaling factor  $\Delta$ .

### 5.3.5.3 Measuring the Average Service Class Queue Delay:

The packet delay represents the difference in time between a packet's insertion time into a queue and its departure time from the queue as explained in relation to Equation 4-3 of the previous chapter. Figure 5-5 illustrates the flow chart for calculating the packet delay of service class (x).

*If the DiffServ domain supports three service classes, then  $x$  could be  $SC_1$  or  $SC_2$  or  $SC_3$ .*

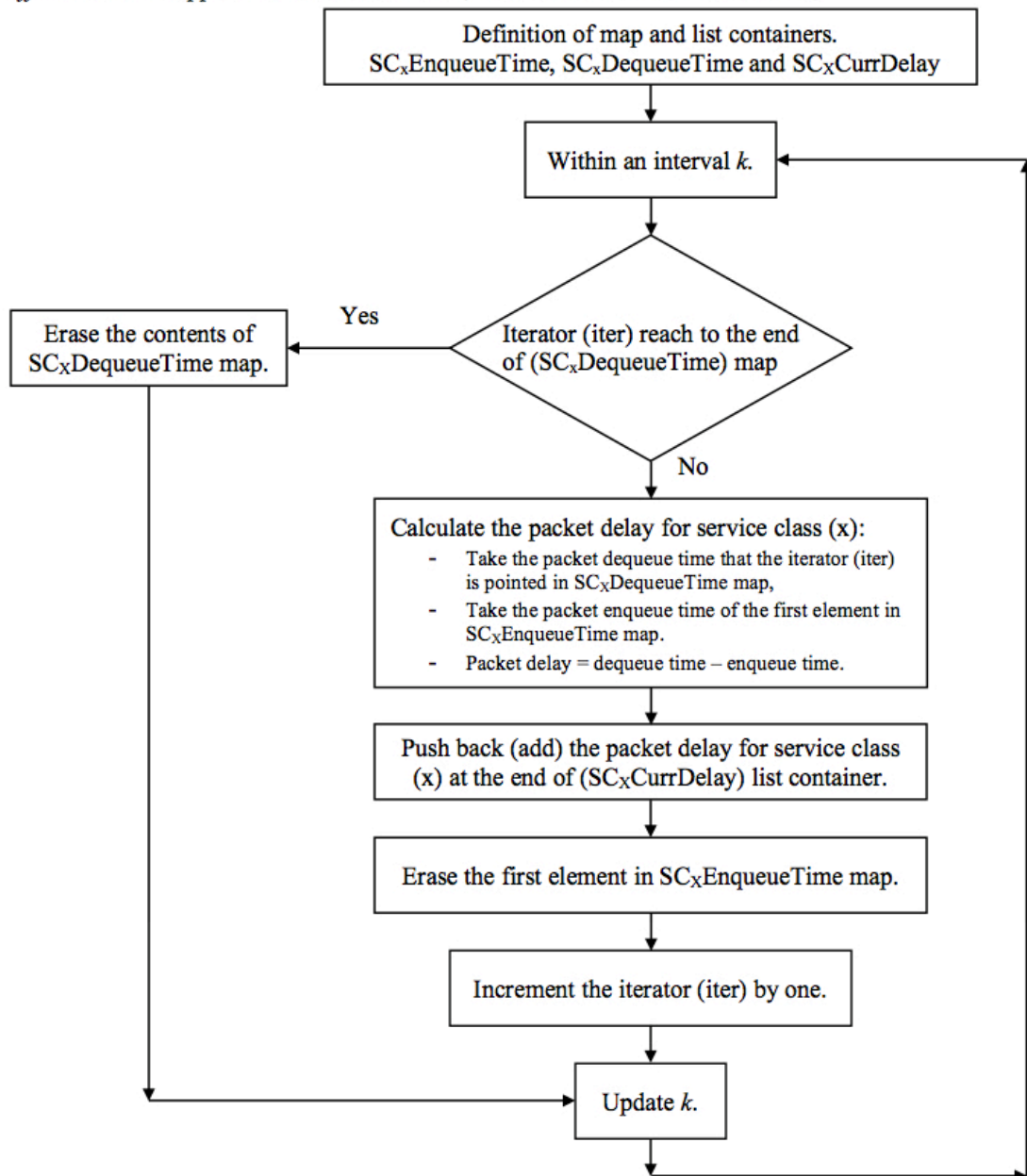


Figure 5-5, Flow chart illustrating the process of calculating the packet delay for a service class.



The average queue delay is equal to the aggregate of the packet delays in the queue divided by the number of packets in the queue, as explained in relation to Equation 4-4 of the previous chapter. Within an interval  $k$ , this delay can be calculated by taking the average of the packet delay values. Figure 5-6 illustrates the flow chart for calculating the average service class queue delay and the code of this process is shown in page 169 of appendix A-3.

*If the DiffServ domain supports three service classes, then  $x$  could be  $SC_1$  or  $SC_2$  or  $SC_3$ .*

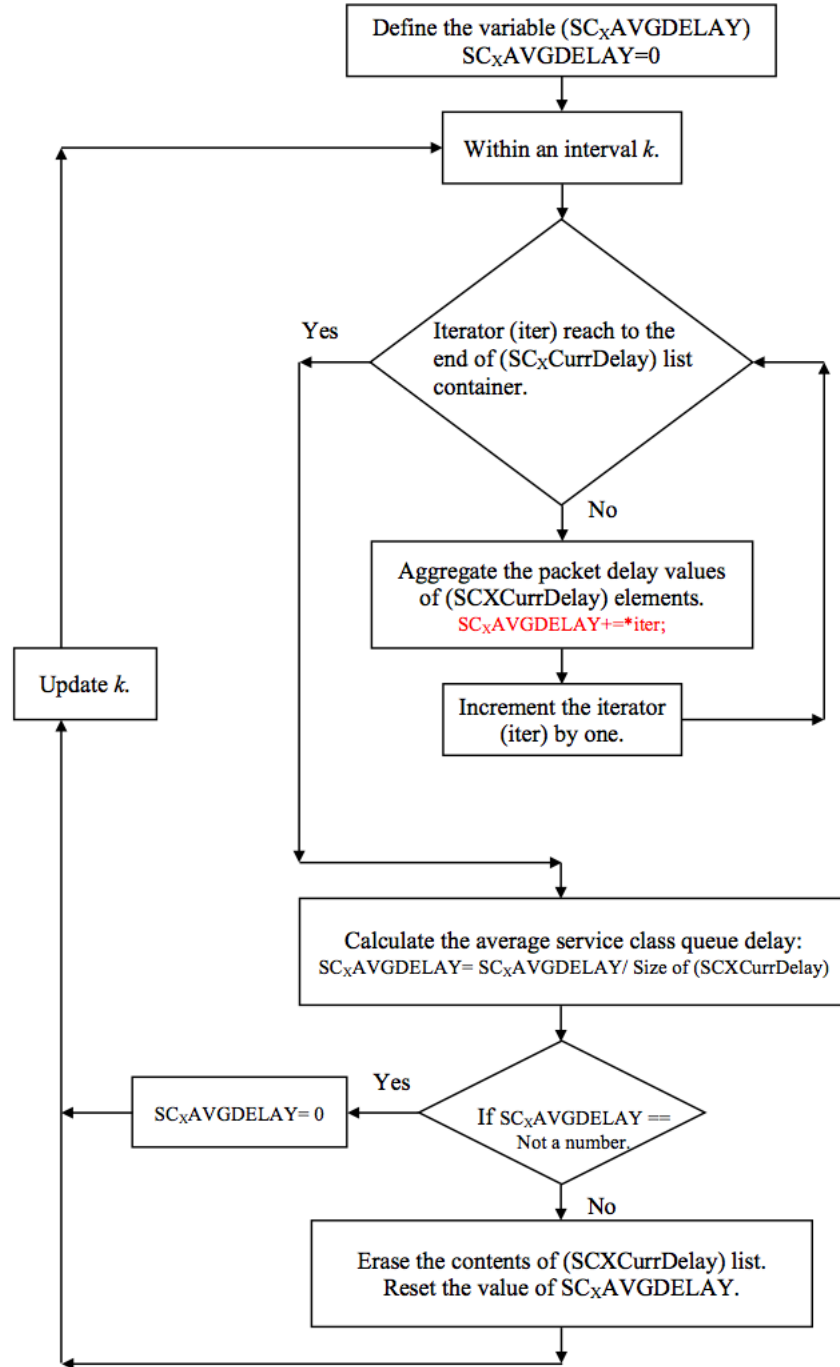


Figure 5-6, Flow chart illustrating the process of calculating the average service class queue delay.

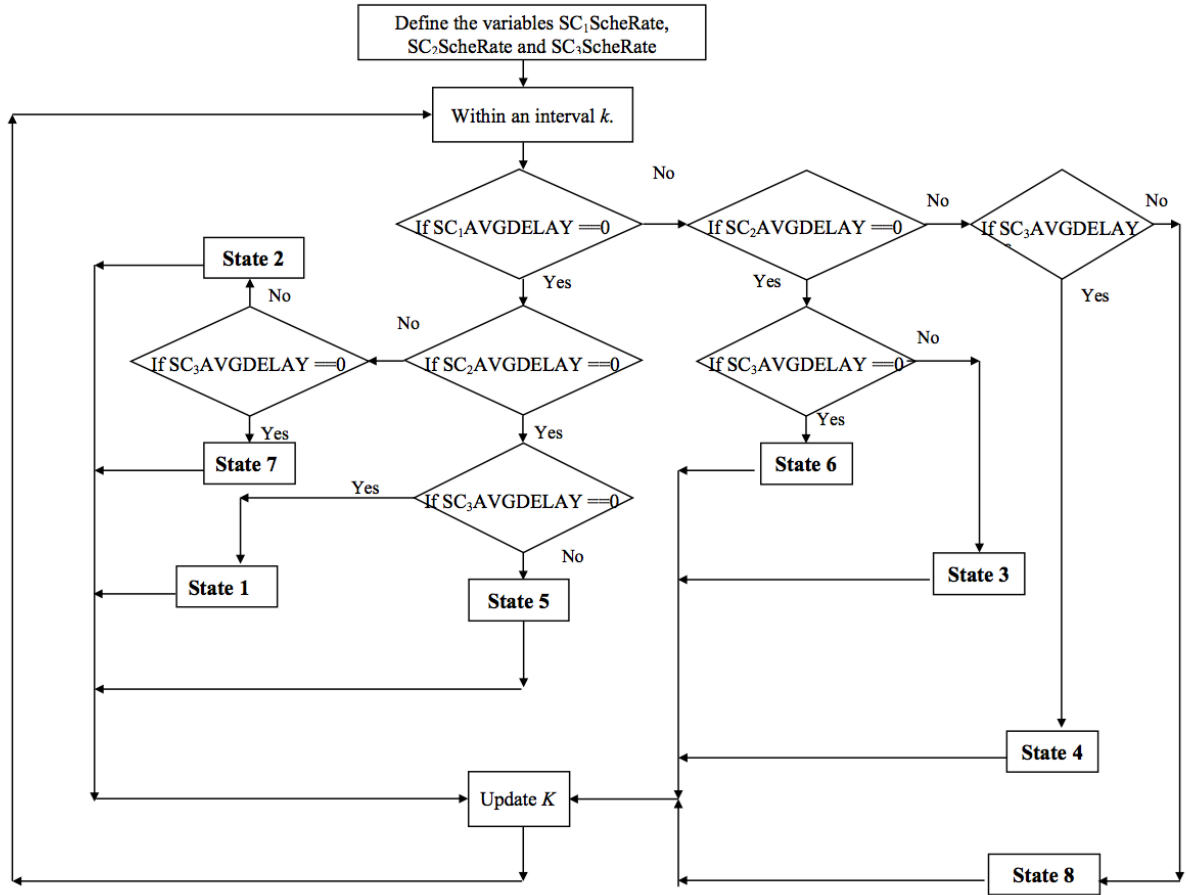
By calculating the average service class queue lengths and delays within an interval  $k$ , the scheduling rates for service class queues can easily be calculated using Equation 4-8 as described in the previous chapter. If the DiffServ domain supports three service classes, then there are eight possible states in relation to calculating service class scheduling rates. These states are generated based on the availability of packets in the service class queues and are illustrated in Table 5-1. All these states can occur, but it is not absolutely necessitated that all these states do occur during a simulation. For each of the states mentioned in Table 5-1, there is a set of delay differentiation parameters ( $\delta$ ) which are defined between service classes ( $\delta_1:\delta_2:\delta_3$ ).

*Table 5-1, States for calculating service classes scheduling rates.*

State	Description	SC1 Scheduling Rate	SC2 Scheduling Rate	SC3 Scheduling Rate
1	No packets in all queues	✖	✖	✖
2	No packets in SC1 queue	✖	✓	✓
3	No packets in SC2 queue.	✓	✖	✓
4	No packets in SC3 queue.	✓	✓	✖
5	No packets in SC1 and SC2 queues	✖	✖	✓
6	No packets in SC2 and SC3 queues	✓	✖	✖
7	No packets in SC1 and SC3 queues	✖	✓	✖
8	All queues have packets	✓	✓	✓

Within an interval  $k$ , the scheduling rates for the service classes in each state are calculated based on the defined ( $\delta_1:\delta_2:\delta_3$ ) set, the average service class queue lengths and the delays associated with that state. If there are no packets in one particular service class queue, then the scheduling rate for that queue is set to 0 and the other two classes share all the out-link resources of the DiffServ router, according to their configurable priorities ( $\delta_1:\delta_2:\delta_3$ ). If any two service class queues have zero packets, then the out-link resources of the DiffServ router are allocated in entirety to the queue that does have packets. Figure 5-7 illustrates the flow chart for calculating the scheduling rates for service class queues where the DiffServ domain supports three service classes and the code of this process is shown in page 171 of Appendix A-3.

If the DiffServ domain supports three service classes ( $SC_1, SC_2, SC_3$ )



State	Description	State	Description
1	Set $\delta_1: \delta_2: \delta_3$ to 0:0:0 $SC1ScheRate = SC2ScheRate = SC3ScheRate = 0$	5	Set $\delta_1: \delta_2: \delta_3$ to 0:0:1 $SC1ScheRate = SC2ScheRate = 0$ $SC3ScheRate = SC3CurrUsage / SC3AVGDELAY$
2	Set $\delta_1: \delta_2: \delta_3$ to 0:1:2 $SC1ScheRate = 0$ , $SC2ScheRate = SC2CurrUsage / SC2AVGDELAY$ , $SC3ScheRate = 0.5 * SC2ScheRate * SC3CurrUsage / SC3CurrUsage$	6	Set $\delta_1: \delta_2: \delta_3$ to 1:0:0 $SC2ScheRate = SC3ScheRate = 0$ $SC1ScheRate = SC1CurrUsage / SC1AVGDELAY$
3	Set $\delta_1: \delta_2: \delta_3$ to 1:0:2 $SC1ScheRate = SC1CurrUsage / SC1AVGDELAY$ , $SC2ScheRate = 0$ , $SC3ScheRate = 0.5 * SC1ScheRate * SC3CurrUsage / SC1CurrUsage$	7	Set $\delta_1: \delta_2: \delta_3$ to 0:1:0 $SC1ScheRate = SC3ScheRate = 0$ $SC2ScheRate = SC2CurrUsage / SC2AVGDELAY$
4	Set $\delta_1: \delta_2: \delta_3$ to 1:2:0 $SC1ScheRate = SC1CurrUsage / SC1AVGDELAY$ , $SC2ScheRate = 0.5 * SC1ScheRate * SC2CurrUsage / SC1CurrUsage$ , $SC3ScheRate = 0$ .	8	Set $\delta_1: \delta_2: \delta_3$ to 1:2:4 $SC1ScheRate = SC1CurrUsage / SC1AVGDELAY$ , $SC2ScheRate = 0.5 * SC1ScheRate * SC2CurrUsage / SC1CurrUsage$ , $SC3ScheRate = 0.25 * SC1ScheRate * SC3CurrUsage / SC1CurrUsage$ .

Figure 5-7, flow chart illustrating the process of calculating the scheduling rates for a DiffServ domain which has three service classes.

### 5.3.6 Calculating Service Class Weights:

After calculating the scheduling rates of the service class queues of the edge routers of a DiffServ domain, the weights of the service class queues of the edge and core routers of a DiffServ domain should also be determined for an interval  $k$ .

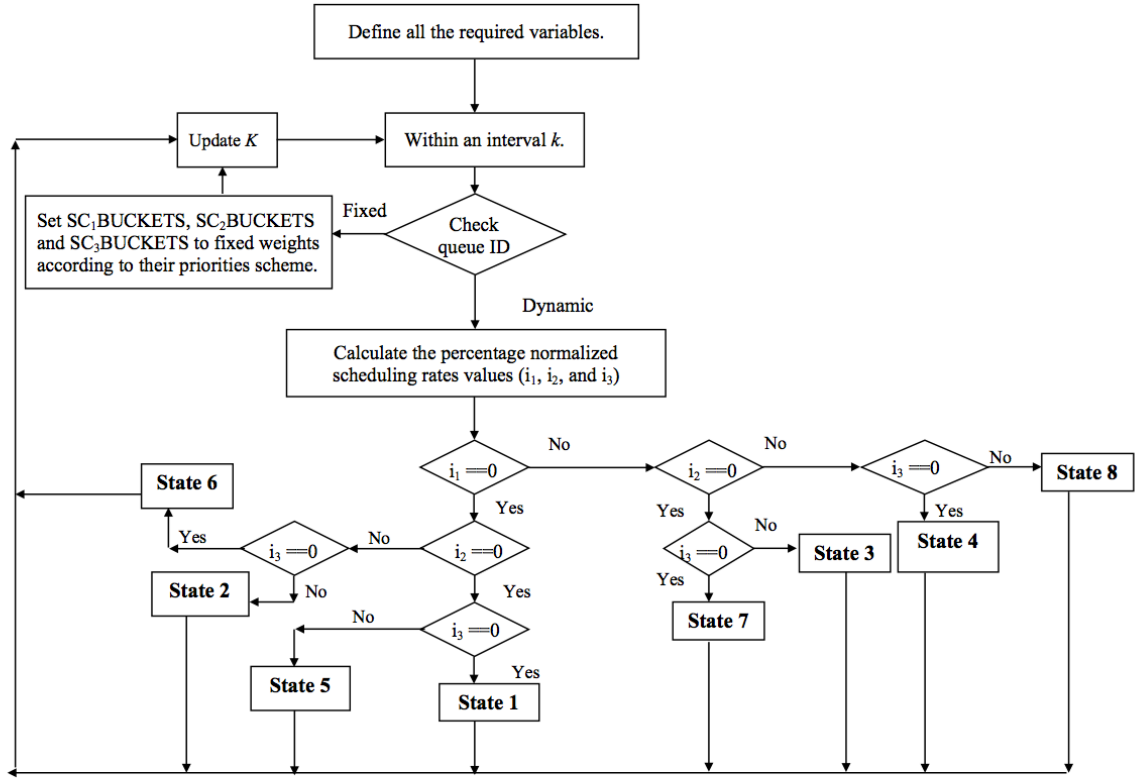
The function (RDWQueue::CalculateBucket) is used to determine the weights of service classes. The weight of a service class queue represents the share of the out-link resources of the DiffServ router that the service class queue can command. The queue weight is expressed, in this research, as the number of packets that are to be forwarded from a queue in a time interval  $k$ . The edge routers have dynamic weights while the core router have fixed weights which are pre-configured by the domain administrator. For a DiffServ domain that support three service classes, according to the priorities of these service classes, the fixed weights of core routers could be configured. The queue ID is used to distinguish between the methods for setting the weights of service class queues. Dynamic weights can be calculated using the mathematical Equation 4-13 and Equation 4-14 that have been described in the previous chapter. The normalized percentage scheduling rate values and their Greatest Common Divisor (GCD) and the average packet size of service class queues must also be calculated for an interval  $k$ . Figure 5-8 shows a flow chart for determining the service classes weights of a DiffServ domain that supports three service classes and the code of this process is shown in page 173 of Appendix A-3.

As in the procedure for calculating service classes' scheduling rates, there are eight possible states in which service class weights must be determined. These states are based on the normalised percentage scheduling rate values. If one service class queue has a zero normalised value, then its weight is set to zero and the other two classes share the out-link resources of the DiffServ router according to their normalised values. If any two service class queues have zero normalised values, then their weights are set to zero while the third service class occupies the whole out-link resource. All these eight states can occur but it is not absolutely necessitated that all these states do occur during the simulation. The states are illustrated in Table 5-2

Table 5-2, States related to calculating service classes weights.

State	Description	SC1 Weight	SC2 Weight	SC3 Weight
1	All queues have zero normalized values	*	*	*
2	SC1 queue has zero normalized value	*	✓	✓
3	SC2 queue has zero normalized value	✓	*	✓
4	SC3 queue has zero normalized value	✓	✓	*
5	SC1 and SC2 queues have zero normalized values	*	*	✓
6	SC1 and SC3 queues have zero normalized values	*	✓	*
7	SC2 and SC3 queues have zero normalized values	✓	*	*
8	All queues have normalized values	✓	✓	✓

If the DiffServ domain supports three service classes ( $SC_1, SC_2, SC_3$ )



State	Action	State	Action
1	Set GCD value to 1. Set SC1BUCKETS = 0, SC2BUCKETS = 0 and SC3BUCKETS = 0.	5	Set GCD value to i3. Set SC1BUCKETS = SC2BUCKETS = 0. Calculate average packets size in SC3 queue (sc3pktlth). SC3BUCKETS = Out link capacity / (100*8* sc3pktlth).
2	Calculate GCD (i2, i3). Calculate average packets size in SC2 and SC3 queues (sc2pktlth and sc3pktlth) respectively. Set SC1BUCKETS = 0. SC2BUCKETS = (i2/GCD) * [Out link capacity / (100*8* sc2pktlth)]. SC3BUCKETS = (i3/GCD) * [Out link capacity / (100*8* sc3pktlth)].	6	Set GCD value to i2. Set SC1BUCKETS = SC3BUCKETS = 0. Calculate average packets size in SC2 queue (sc2pktlth). SC2BUCKETS = Out link capacity / (100*8* sc2pktlth).
3	Calculate GCD (i1, i3). Calculate average packets size in SC1 and SC3 queues (sc1pktlth and sc3pktlth) respectively. Set SC2BUCKETS = 0. SC1BUCKETS = (i1/GCD) * [Out link capacity / (100*8* sc1pktlth)]. SC3BUCKETS = (i3/GCD) * [Out link capacity / (100*8* sc3pktlth)].	7	Set GCD value to i1. Set SC2BUCKETS = SC3BUCKETS = 0. Calculate average packets size in SC1 queue (sc1pktlth). SC1BUCKETS = Out link capacity / (100*8* sc1pktlth).
4	Calculate GCD (i1, i2). Calculate average packets size in SC1 and SC2 queues (sc1pktlth and sc2pktlth) respectively. Set SC2BUCKETS = 0. SC1BUCKETS = (i1/GCD) * [Out link capacity / (100*8* sc1pktlth)]. SC2BUCKETS = (i2/GCD) * [Out link capacity / (100*8* sc2pktlth)].	8	Calculate GCD (i1, i2, i3). Calculate average packets size in SC1, SC2 and SC3 queues (sc1pktlth, sc2pktlth and sc3pktlth) respectively. SC1BUCKETS = (i1/GCD) * [Out link capacity / (100*8* sc1pktlth)]. SC2BUCKETS = (i2/GCD) * [Out link capacity / (100*8* sc2pktlth)]. SC3BUCKETS = (i3/GCD) * [Out link capacity / (100*8* sc3pktlth)].

Figure 5-8, flow chart illustrating the procedure for calculating the weights of service class queues

### **5.3.7 Configuring Service Classes Scheduling Rates for the Edge Routers of a DiffServ Domain with Multiple Out-Ports:**

The service class queues in a DiffServ domain should all have the same management policy at all edge and core routers of the domain in terms of the size of service class buffers, the service class queue dropping probabilities, the service class queue thresholds values, the service class queue scheduling rates for edge routers with multiple out-port links and the fixed forwarding weight for the core routers. This management policy is applied by the domain administrator or by the domain management application. In a DiffServ domain with multi-core routers, the ingress domain routers can have multiple out-port links. At each port, there is a queue for each defined service class: i.e., if a domain has an ingress router with three out-port links, then there must be three queues for a service class (x), one for each of the out-ports of the router, and the scheduling rate for this service class (x) should be the same at all these ports.

The function (RDWQueue::SetSchedulingRateByAverage) is dedicated to the task of setting the same scheduling rate for each service class queue in the ingress routers with multi-out-port links.

In section 4.4, the method for calculating the scheduling rate for a service class queue at an ingress router with multi-out-port links was explained. The function calculates the service class scheduling rate based on the maximum and minimum values. The procedure for calculating the scheduling rates is the same as is explained in section 5.3.5 but with the difference that, here, the maximum and minimum values are used. After the scheduling rate for the specified service class sub queue at each out-port link of the ingress router has been set, the service class weight must then be calculated. The procedure for calculating the service classes' weights is the same as that explained in section 5.3.6. Figure 5-9 illustrates the flow chart of the function for configuring the service classes scheduling rates for the edge routers with multiple out-ports and the code of this function is shown in page 192 of Appendix A-3.

If the DiffServ domain supports three service classes, then  $x$  could be  $SC_1$  or  $SC_2$  or  $SC_3$ .

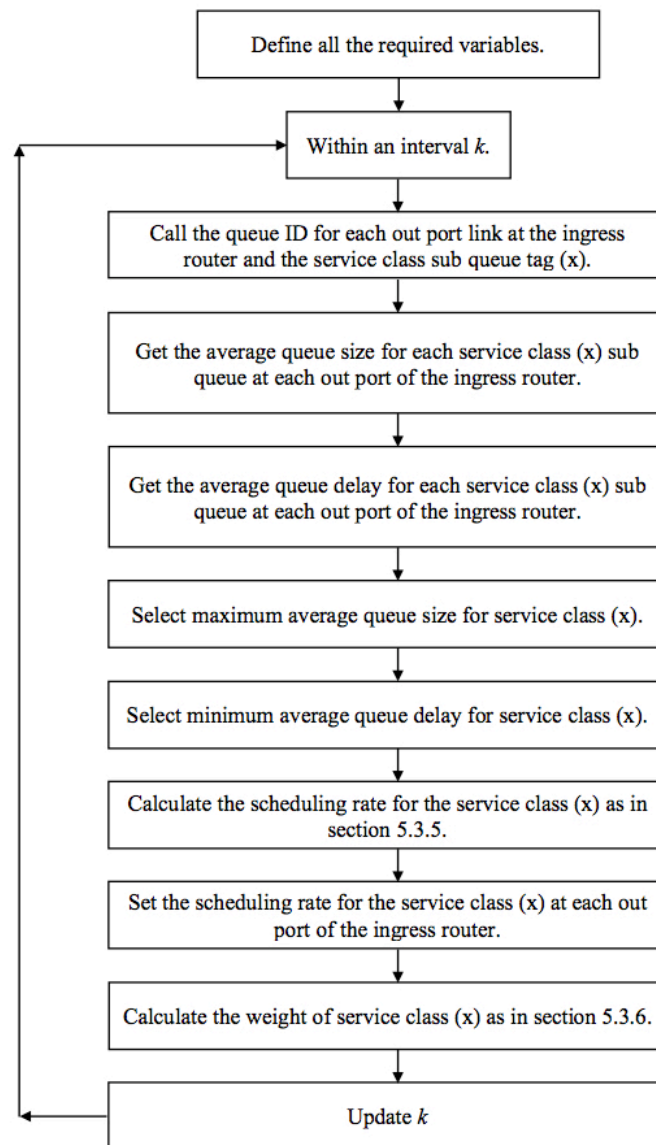


Figure 5-9, flow chart of configuring service classes scheduling rates for the edge routers with multiple out ports function.

### 5.3.8 Management of Service Classes Scheduling Rates across Different DiffServ Domains:

A function called (RDWQueue::SetSchedulingRateByLinearFactor) has been created in order to manage the service class resources (scheduling rates) across different proportional delay DiffServ in the event of congestion within an interval  $(k+1)$ . The average service class queue length (in bytes) at the ingress router of the downstream domain can be obtained by the use of the function (GetAverageQueueLength1, which is included in the RDWQueue application and used regardless of whether the ingress router has one or many out-ports). This function calculates the congestion level ( $\alpha$ ) for each service class queue and specifies the scheduling rate update factor ( $\beta$ ) for the equivalent service class queue at the egress router of the upstream

DiffServ domain. All these operations are based on the mathematical expressions which are relevant to them as explained in section 4.4 of the previous chapter. After these calculations, the updated scheduling rates for the service class queues should be set and their weights should also be calculated as has already been described in section 5.3.6. Figure 5-10 illustrates the flow chart for the management of service classes scheduling rates across different DiffServ domains and the code of this function is shown in page 196 of Appendix A-3.

*If the ingress router of the downstream DiffServ domain supports three service classes, then  $x$  could be  $SC_1$  or  $SC_2$  or  $SC_3$ .*

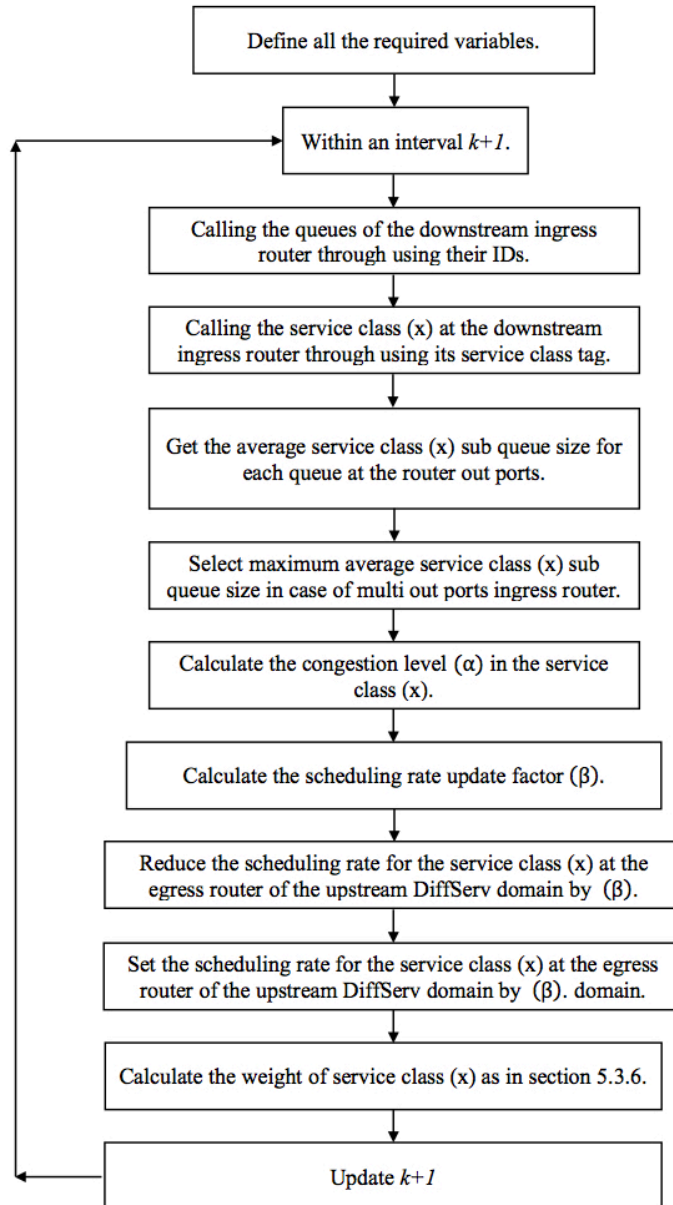


Figure 5-10, flow chart illustrating the function for the management of service classes' scheduling rates across different DiffServ domains.



## 5.4 OpenFlow Queue Message Layer:

The Queues of OpenFlow switches cannot be configured through OpenFlow flow protocol; however, OpenFlow Management and Configuration Protocol, OF-CONFIG (Open Networking Foundation (ONF) 2014a), which is being standardized by ONF, is used by a controller to configure the queues of OpenFlow switches and it requires OpenFlow 1.2 or later versions. The OF-CONFIG protocol configures the minimum and maximum rates of queue. Each OpenFlow message begins with the same header structure. Figure 5-11 illustrates the message header structure in OpenFlow (Open Networking Foundation (ONF) 2013) (Flowgrammable Research Team 2013).

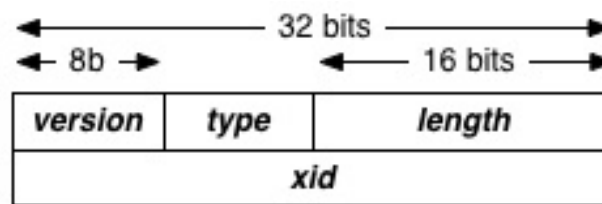


Figure 5-11, OpenFlow message structure.

The version field indicates the version of OpenFlow to which this message belongs. The length field indicates where this message will end in the byte stream starting from the first byte of the header. The xid, or transaction identifier, is a unique value used to match requests to responses while the type field indicates what type of message is present and how to interpret the payload. The controller uses the OpenFlow protocol to query the state of queues through exchanging two messages “QueueGetConfigReq” and “QueueGetConfigRes” to request a queue state and to respond to a controller respectively. Figure 5-12 shows the structure of these mentioned messages. The request message consists of the standard header followed by a 2 byte port identifier, and a 2 byte pad. The response message consists of the standard header, followed by the two byte port identifier, a six byte pad, and a sequence of queues properties.

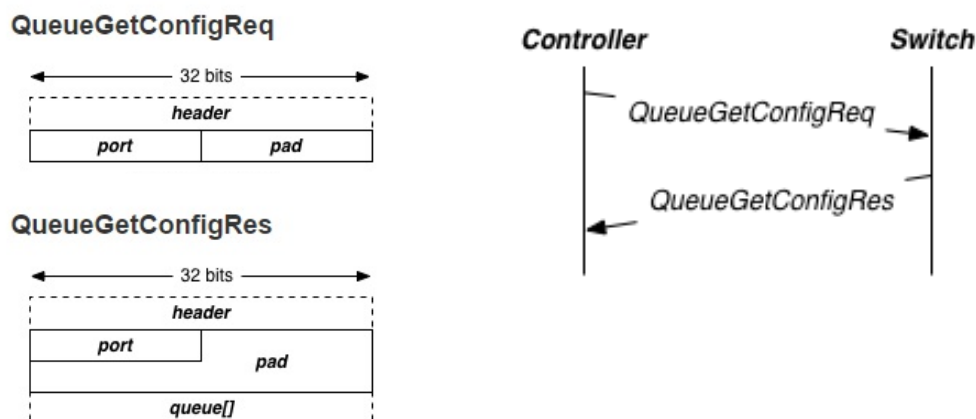


Figure 5-12, OpenFlow queue state query messages.

## **5.5 Chapter Summary:**

This chapter presented the NS3 prototype implementation of the DRAM-NFV scheduling algorithm for evaluation. The reasons for the selection of this specific type of simulation are that NS3 supports the implementation of IP networks and it is open source software that does not incur any financial costs. The methodology implemented is based on the mathematical model of the DRAM-NFV algorithm which was explained in section 4.4. All the NS3 applications mentioned in this chapter and the next chapter (RDWQueue, TosApp, and topologies) are constructed from scratch using the API features of NS3, the C++ programming language and the Linux shell code to analyse the results of the output file. Future work may be pursued by developing some test scenarios to validate and evaluate the DRAM-NFV algorithm implemented using the NS3 simulator; this will be discussed in the next chapter.

# **Chapter Six**

## **The DRAM-NFV Algorithm:**

### **Validation and Critical Evaluation**

#### **6.1 Introduction:**

This chapter explains the validation, evaluation, and the results generated by, the DiffServ module and the prototype DRAM-NFV algorithm implemented under the simulation software, NS3. The main goal of this evaluation is to determine whether the DRAM-NFV algorithm can provide better performance (than currently-used algorithms) in managing resources within and across proportional delay DiffServ domains in the event of congestion. Test scenarios have been presented in this chapter which facilitate the study and evaluation of the performance of the DRAM-NFV algorithm. These scenarios take into consideration different topologies of DiffServ domains and study the performance of the DRAM-NFV algorithm in the circumstance that some of the DiffServ domains cannot manage their resources via the NFV technology. The performance of the DRAM-NFV algorithm may be determined in terms of link utilization and average End to End Delay– in relation to these test scenarios. Furthermore, the results in these scenarios are compared with the results to be obtained by the use of the DWFQ algorithm, which manages resources only within the edge routers of the DiffServ domains. The criterion for evaluating the DRAM-NFV is that it should increase the traffic of certain service class queues within the implemented DiffServ module. The DRAM-NFV algorithm monitors the service class congestion levels at the ingress router of the downstream domain and manages resources across the implemented DiffServ domains. The evaluation will proceed by monitoring which service class queues suffer from congestion and measuring the average End to End Delay for service classes traffic and the utilization at the destination link and at the link that connects DiffServ domains. The DRAM-NFV algorithm is applied under the same conditions as the DWFQ algorithm, and the average End to End Delays and link utilizations are measured for both algorithms.

## 6.2 Traffic Model Simulation Test Applications:

The application module in the network simulator NS3.23 contains several built-in applications that can be used with representations of network topologies. These applications are as follows.

1. OnOff Application – this application generates traffic (TCP/UDP) according to an alternating On/Off pattern. Packets are sent at a constant rate but only in an on period.
2. PacketSink Application – this application consumes traffic (TCP/UDP) sent to an IP address and port. It is considered to be a complement to the OnOff Application but it can be used as a general- purpose application.
3. UdpEchoClient Application – this application transmits a UDP packets to a remote server which is echoed by the remote server and then received (back) by this application.
4. UdpEchoServer Application – this application receives a UDP packet from a remote client and sends it back to that remote client. It is considered to be a complement to the UdpEchoClient application.
5. UdpClient Application – this application transmits UDP packets carrying a 32-bit sequence number and one 64-bit time stamp.
6. UdpServer Application – this application receives UDP packets from a remote host and uses the sequence number to determine if any packets have been lost and the timestamp to determine the packet delay. It is considered to be a complement to the UdpServer application.

The application module of NS3.23 does not provide built-in applications which represent the profiles of real-time applications traffic such as that produced by the Voice over Internet Protocol (VoIP), Video traffic, the File Transfer Protocol (FTP), or Database request messages, etc.

A new application traffic model was created for this research to represent the profile of these abovementioned application traffic flows. This new application is called (TosApp). The NS3::Application class reference is considered as the base class for NS3 applications. All other traffic applications must be derived from this base class; thus the (TosApp) is inherited or derived from the NS3::Application class reference in order to provide a consistent way to start and stop this application. The (TosApp) application defines also the traffic characteristics in terms of the size of the generated packets, the number of generated packets, the rate of generated packets, the packet Type of Service (ToS) and the simulation scheduling of the event of sending a packet. Appendix A-2.2 illustrates the NS3 class references which are used to build the TosApp model and their purposes.

The number of generated packets (nPackets) represents the product of multiplying the simulation run time and the number of packets that must be forwarded in one second from a traffic source. This can be calculated using Equation 6-1:

$$\text{nPackets} = \left\lceil \frac{\text{Traffic Rate (bit/sec.)}}{\text{Packet size (Byte)} * 8} \right\rceil * \text{Simulation stop time}$$

6-1

The time at which the packet will be sent from the source is also calculated by this (TosApp) application by the use of the current simulation time, this time will be used to measure the End to End Delay of each service class traffic transmission.

The profiles of four standard application traffic sources can be used when generating packets in the NS3 simulation. These are implemented as the Voice over Internet Protocol application (VoIP), the Video traffic application, the File Transfer Protocol (FTP) application and the Database request messages application. The characteristics of these applications and their specified queues are shown in Table 6-1 and are explained in (Wendell Odom 2005) (Tim Szigeti 2005). These profiles are used in the simulated traffic model (TosApp) application to generate traffic appropriate to them. The code for (TosApp) traffic model is shown in Appendix A-4.

Table 6-1, Characteristics of the simulated traffic applications profiles.

Application	Service Class	Packet Size (Bytes)	Traffic Rate (Kbits/sec)	Packet rate (Packets/sec)	Inter-arrival time
<b>VoIP</b>	SC1	200 (Constant Distribution)	80	50 (constant)	Constant
<b>Video Traffic</b>	SC2	1300 – 1500 (Exponential Distribution)	384	34 (Average)	Random
<b>FTP</b>	SC3	200 – 500 (Exponential Distribution)	256	91 (Average)	Random
<b>Database Request</b>	Any class (Best Effort)	512 (Constant Distribution)	82	20 (Average)	Random

### 6.3 Experimental Design of the evaluation process:

In order to test and evaluate the performance of the DRAM-NFV algorithm in managing resources within and across different proportional delay DiffServ domains, different testing scenarios should be considered. In these test scenarios, the DiffServ domains topology can be constructed from either single core DiffServ domains or multi-core DiffServ domains. The marking strategies of all the DiffServ domains in all the test scenarios have been made to be identical in order to avoid increasing the complexity of the simulation programming. In addition: all the queues which belong to a specific service class in all DiffServ domains of test scenarios are assumed to have the same queue configuration in terms of queue buffer size (Wendell Odom 2005) (Chin-Chang Li et al. 2000), threshold parameters and drop probabilities for the packet drop algorithm (Cisco 2013); and the same threshold congestion parameters in relation to the management of resource between different proportional delay DiffServ domains. The threshold congestion parameters are based on queue threshold values and its buffer size. Three test scenarios are presented in this research as will be discussed in the following subsections; the procedures for simulating the topologies of these test scenarios using NS3 are explained in Appendix A-5.

The principle of the DRAM-NFV algorithm was explained in chapter 4. The resource management which is carried out across different proportional delay DiffServ domains is based on two factors. Firstly, reducing the scheduling rates of the congested service class queues at the egress router of the upstream domain based on the corresponding service class queue congestion levels at the ingress router of the downstream domain. Secondly, reallocating the resources of the link that connects the DiffServ domains, based on the updated scheduling rates. The algorithm is particularly effective when both the DiffServ domains (upstream and downstream) are underutilized but some of the service class queues at the ingress router of the downstream domain are either congested or suffering from various levels of congestion while other classes are not congested. Consequently, several case studies are presented in relation to each test scenario; each case study includes increasing the traffic of (a) particular service class(es) at the upstream domain to create congestion states in the service class queues at the ingress routers of the downstream domain. The performance of the DRAM-NFV algorithm in managing resources across different DiffServ domains is compared with the performance of the DWFQ algorithm which manages resources within the edge routers of a DiffServ domain but cannot manage resources across DiffServ domains. This performance will be measured, as explained in the section 6.4, by looking at the utilization of the link that connects the DiffServ

domains, the utilization of the link that connects the downstream domain to the destination and the average End to End Delay for service classes' traffic.

### **6.3.1 Test Scenario 1:**

In test scenario 1, both the upstream and downstream DiffServ domains are constructed around single core DiffServ routers. Three case studies are used to test the performance of the DRAM-NFV algorithm in managing resources within and between DiffServ domains in the event of congestion. The cases are as follows:

1. Case study 1 was instituted by increasing the traffic of the medium priority service class ( $SC_2$ ) at the upstream domain and studying the effects of this on the management of the resources for the lowest priority service class traffic ( $SC_3$ ) and the highest priority service class traffic ( $SC_1$ ) - across DiffServ domains when congestion occurs. The traffic of service class ( $SC_2$ ) is UDP traffic and competes with the TCP traffic (the lowest priority service class) to occupy more bandwidth. The analysis of this case study is shown in section 6.6.1.
2. Case study 2 was instituted by increasing the traffic of the medium priority service class ( $SC_2$ ) and the lowest priority service class ( $SC_3$ ) at the upstream domain and studying the effects of this on the management of the resources for the highest priority service class ( $SC_1$ ) traffic, across DiffServ domains when congestion occurs. The analysis of this case study is shown in section 6.6.2.
3. Case study 3 was instituted by increasing the traffic of all the service classes at the upstream domain to study how the DRAM-NFV algorithm achieves better utilizations for the service classes at the link that connects the DiffServ domains – when congestion occurs. The analysis of this case study is shown in section 6.6.3.

The topology for test scenario 1 is shown in Figure 6-1. The topology simulation diagram and the NS3 code for this scenario are presented in Appendix A-5.1.

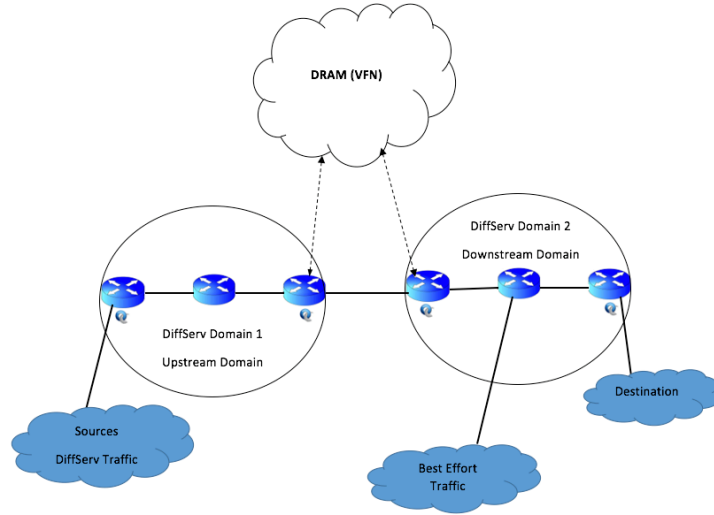


Figure 6-1, Test scenario 1.

### 6.3.2 Test Scenario 2:

in test scenario 2, the upstream DiffServ domain is constructed around a single core DiffServ router but the downstream DiffServ domain is constructed around multi core DiffServ routers. Two case studies were carried out to test the performance of the DRAM-NFV algorithm in managing resources within and between DiffServ domains when congestion occurs. These cases were:

1. Case study 1 was instituted by increasing the traffic of the lowest priority service class ( $SC_3$ ) at the upstream domain to study the performance of the DRAM-NFV algorithm in managing the resources for the highest priority service class traffic ( $SC_1$ ) and the medium priority service class traffic ( $SC_2$ ) across DiffServ domains when congestion occurs. The traffic of service class ( $SC_3$ ) is TCP-type traffic and it competes with the UDP traffic (the highest and medium priority service classes) to occupy bandwidth. The analysis of this case study is shown in section 6.7.1.
2. Case study 2 was instituted by increasing the traffic of all service classes at the upstream domain to study how the DRAM-NFV algorithm achieves better utilizations for service classes at the link that connects DiffServ domains and at the links connecting the downstream DiffServ domain with multi core routers with destination when congestion occurs. The analysis of this case study is shown in section 6.7.2.

The topology of test scenario 2 is shown in Figure 6-2. The topology simulation diagram and the NS3 code for this scenario are given in Appendix A-5.2.



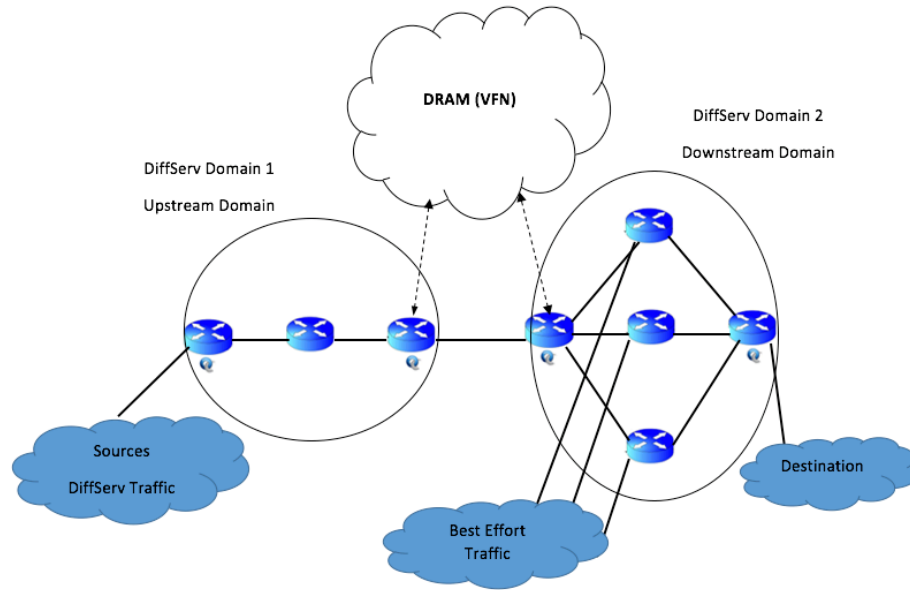


Figure 6-2, Test scenairo2.

### 6.3.3 Test Scenario 3:

In test scenario 3, there are two upstream DiffServ domains and one downstream DiffServ domain; the upstream domains are constructed around single core DiffServ routers while the downstream DiffServ domain is constructed around multi core DiffServ routers. The performance of the DRAM-NFV algorithm will be studied in relation to the situation where there is more than one upstream DiffServ domain and one of these cannot manage its resources via the NFV technology.

In this test scenario, all three case studies were carried out to test the performance of the DRAM-NFV algorithm in managing resources within and among DiffServ domains when congestion occurs. These cases are:

1. Case study 1 is instituted by increasing the traffic of the medium priority service class ( $SC_2$ ) at one of the upstream domains and studying the effects of this on the managing of the resources for the lowest priority service class traffic ( $SC_3$ ) and the highest priority service class traffic ( $SC_1$ ) among DiffServ domains and at the downstream domain, when congestion occurs. The analysis of this case study is shown in section 6.8.1.
2. Case study 2 is instituted by increasing the traffic of the lowest priority service class ( $SC_3$ ) at the upstream domains to study the performance of the DRAM-NFV algorithm in managing the resources for the highest priority service class ( $SC_1$ ) traffic between DiffServ domains when congestion occurs. The analysis of this case study is shown in section 6.8.2.

3. Case study 3 is instituted by increasing the traffic of all the service classes at the upstream domain to study how the DRAM-NFV algorithm achieves better utilizations for service class traffic at the link that connects the DiffServ domains - when congestion occurs. The analysis of this case study is shown in section 6.8.3.

The topology of test scenario 3 is shown in Figure 6-3. The topology simulation diagram and the NS3 code for this scenario are given in Appendix A-5.3.

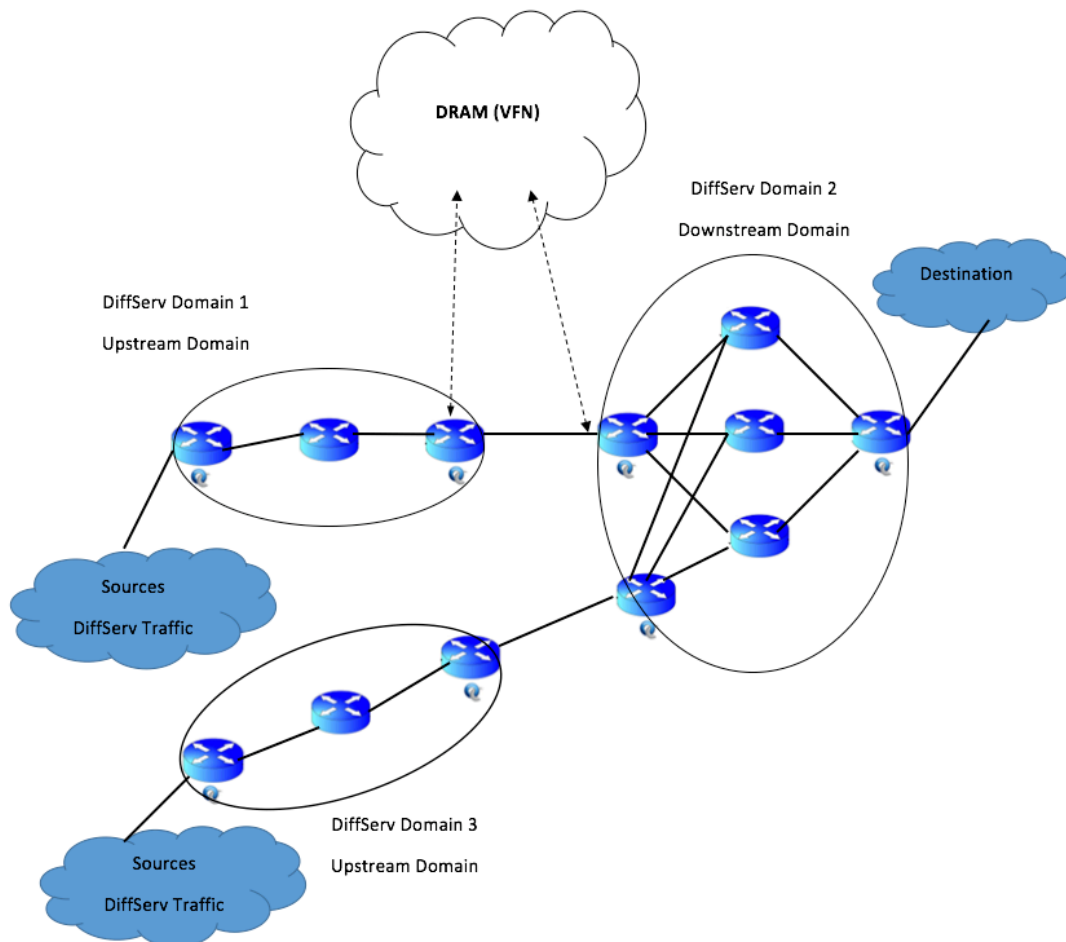


Figure 6-3, Test scenairo3.

## 6.4 Performance Measurements in Relation to the Simulation of the

### Differentiated Services Domains:

Two parameters are used to measure the performance of the DiffServ domains. First, there is the average End to End Delay for each service class' traffic and for the overall traffic. The second performance parameter is the link utilization in the DiffServ domains. The End to End Delay of each packet received at the destination node (EtE PktDelay) is calculated based on the difference between the time the packet was sent and the time the packet was received. The average end-to-end delay for a specific service class' traffic (*EtE SC<sub>i</sub>Delay*) represents the average of the accumulative End to End Delays of packets that are encountered by this service class. The average End to End Delay for the overall traffic (*EtE Delay*) represents the average end-to-end delays of all packets received at the destination node. The code for calculating the average End to End Delay for service classes traffic is shown in Appendix A-7.

The percentage utilization related to a service class' traffic at a certain link in the DiffServ domain is the ratio of current service class traffic (bits) at the link port per second to the maximum traffic that the link port can handle. The service class traffic represents the sum of the service class packets sizes in (bits) that are sent from the service class queue across the out-port link in one second. The service class' traffic link utilization measurements are accomplished in the `RDWQueue::DoDequeue`, `RDWQueue::UpdateLinkUtilization` and `RDWQueue::getLinkUtilization` functions. The code for calculating the service classes traffic utilizations is shown in page 160 of Appendix A-3.

All the results of the simulation, in terms of service classes' average queue delay, service classes' average queue length, service classes' scheduling rates, service classes' weights, service classes' link utilizations, packet sending time, packet receiving time, the average End to End Delay values, the measured congestion level values, the scheduling rate update factor values and other indicators are all stored in a (.txt) file during the running of the simulation. This (.txt) file needs to be put through a filtering process in order to extract the abovementioned values separately. The code for setting the values of simulation parameters and the code script for obtaining the simulation results from the execution file (.txt) are shown in Appendixes A-6 and A-8 respectively.

## 6.5 Analysis of the Simulated Proportional Delay DiffServ Queue model:

In this section, the performance of the simulated proportional delay DiffServ queue model will be discussed in relation to the situation where the DRAM-NFV algorithm is not in use. For this analysis the DWFQ algorithm is used, and the traffic condition in the service class queues is underutilisation. The discussion covers the following issues:

- i. configuration of the service class queues;
- ii. average service class queue delays;
- iii. service class packets drop mechanism.

The proportional delay DiffServ domain is represented by the simulated queue model application (RDWQueue), the traffic model application (TosApp) and the network topology model. The (TosApp) application generates four different standard traffic flow types; these are VoIP, Video traffic, FTP and database request messages. The simulated DiffServ domains are designed to provide three service class queues ( $SC_1$ ,  $SC_2$  and  $SC_3$ ), differentiated by the way that their scheduling rates are specified, as illustrated in section 4.3. (Chin-Chang Li et al. 2000) set the delay configuration parameters ( $\delta$ ) for service classes ( $SC_1:SC_2:SC_3$ ), which are configured by the domain administrator, to (1:2:4) such that  $SC_1$  is the highest service priority and  $SC_3$  is the lowest priority service class. In addition, (Chin-Chang Li et al. 2000) tested the performance of these parameters in achieving the PDD conditions which were explained in 2.2. Based on (Chin-Chang Li et al. 2000) assumption, the delay differentiation parameters for service classes in the simulated DiffServ domain are set as ( $SC_1:SC_2:SC_3$ ) (1:2:4) respectively such that the scheduling rate for the medium priority ( $SC_2$ ) queue is half that of the scheduling rate for the highest priority ( $SC_1$ ) queue and the scheduling rate of the lowest priority ( $SC_3$ ) queue is half that of the scheduling rate for the medium priority ( $SC_2$ ) queue as illustrated in Equation 4-7 and Equation 4-8.

In relation to the QoS requirements of the abovementioned traffic applications, VoIP traffic quality is significantly affected by packet loss and delay. It should be marked as DSCP EF (J. Babiarz, K. Chan & F. Baker 2006) which has the highest priority as compared to other the classes, according to the standard traffic differentiation. Video traffic should be marked as DSCP AF<sub>41</sub>, which is the second highest priority class after the EF class, based on the standard traffic differentiation. This traffic is also sensitive to packet delay and loss. FTP traffic is considered to be bulk data traffic. It is relatively insensitive to delay and packet loss, and should be marked as DSCP AF<sub>11</sub>, based on the standard traffic differentiation. Consequently, the service class ( $SC_1$ ) in the simulated DiffServ module is allocated to VoIP traffic, the service

class (SC<sub>2</sub>) is allocated to Video traffic and the service class (SC<sub>3</sub>) is allocated to FTP traffic. The data base request message traffic (SC<sub>4</sub>) traffic is considered a best effort traffic. It should be marked as DSCP<sub>0</sub>, based on the standard traffic differentiation. This kind of traffic can be used to change the network traffic conditions in the downstream DiffServ domain. The best effort traffic should occupy the remaining bandwidth so it is inserted in any defined service class queues (SC<sub>1</sub>, SC<sub>2</sub> and SC<sub>3</sub>), depending on the available resources in these classes.

The buffer sizes of these abovementioned service class queues can be expressed in terms of power of 2 numbers of packets: i.e., (64, 128, 256, 512) packets (Wendell Odom 2005). These buffer sizes can also be expressed in terms of bytes by multiplying the number of packets that are required to be accommodated in a queue by the average packet size (in bytes) in that queue. The buffer size of each of the abovementioned service class queues (SC<sub>1</sub>, SC<sub>2</sub>, SC<sub>3</sub>) in the simulated DiffServ module (upstream and downstream domains) is configured to accommodate 128 packets respectively. The (WRED) algorithm is used as a queue management technique intended to avoid congestion in the queues. It prevents a queue from ever filling to its buffer size. The threshold parameters and drop probabilities for (WRED) are also configurable parameters and can be set by the administrator of a DiffServ domain. The threshold parameters and drop probabilities for each defined service class queue in the simulated DiffServ module (upstream and downstream domains) are the same. The WRED configuration of the Cisco<sup>TM</sup><sup>5</sup> 10000 series QoS routers (Cisco 2013) is used in this simulation, as illustrated in Table 6-2. The threshold values for the congestion level of all the service class queues at the ingress router of the downstream domain are the same. These values are also configured by the domain administrator and are chosen such that the lower congestion threshold value is set to the minimum queue threshold value as indicated in Table 6-2 and the higher threshold congestion value is set to 0.9 to get a wide and linear range for the scheduling rate update factor ( $\beta$ ). The relation between the congestion level ( $\alpha$ ) and the scheduling rate update factor ( $\beta$ ) was described in section 4.3.

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<sup>5</sup> Cisco<sup>TM</sup> is a trade mark of Cisco Systems, Inc. and its affiliates.

Table 6-2, configuration of the simulated WRED

	Minimum Threshold (times the queue size)	Maximum Threshold (times the queue size)	Drop Probability
<b>Highest Priority (SC<sub>1</sub>)</b>	15/32	½	1/10
<b>Medium Priority (SC<sub>2</sub>)</b>	7/16	½	1/10
<b>Lowest Priority (SC<sub>3</sub>)</b>	13/32	½	1/10

As the service class (SC<sub>1</sub>) is the highest priority service class in relation to the other classes (SC<sub>2</sub>) and (SC<sub>3</sub>), the performance of this class, in terms of average queue delay, should be better than that of both the SC<sub>2</sub> and the SC<sub>3</sub> queues. It is expected that the SC<sub>1</sub> queue should suffer the least average queuing delay because this queue occupies all the required bandwidth for its traffic. The performance in the service class (SC<sub>2</sub>) queue should have an asymptotic behaviour in relation to the service class (SC<sub>1</sub>) queue. Its performance should not be better or much worse than that of the SC<sub>1</sub> queue. In contrast, the SC<sub>3</sub> queue should have a markedly different performance to the SC<sub>1</sub> and SC<sub>2</sub> queues; this queue will witness variations in its average queuing delay because it occupies the least bandwidth compared to the other queues. Figure 6-4 and Figure 6-5 illustrate the average queue delays at the ingress and egress routers of the simulated DiffServ domain for the whole period of simulation.

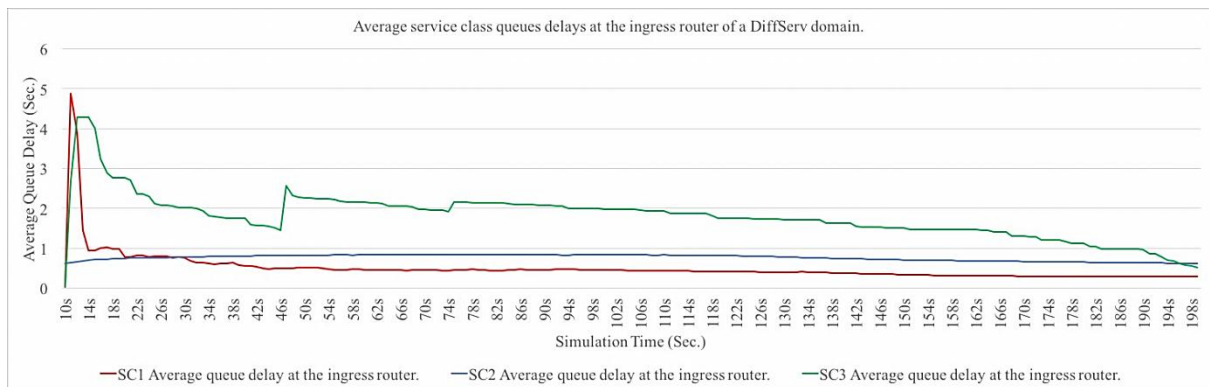


Figure 6-4, Average service class queue delays at the ingress router of the downstream DiffServ domain

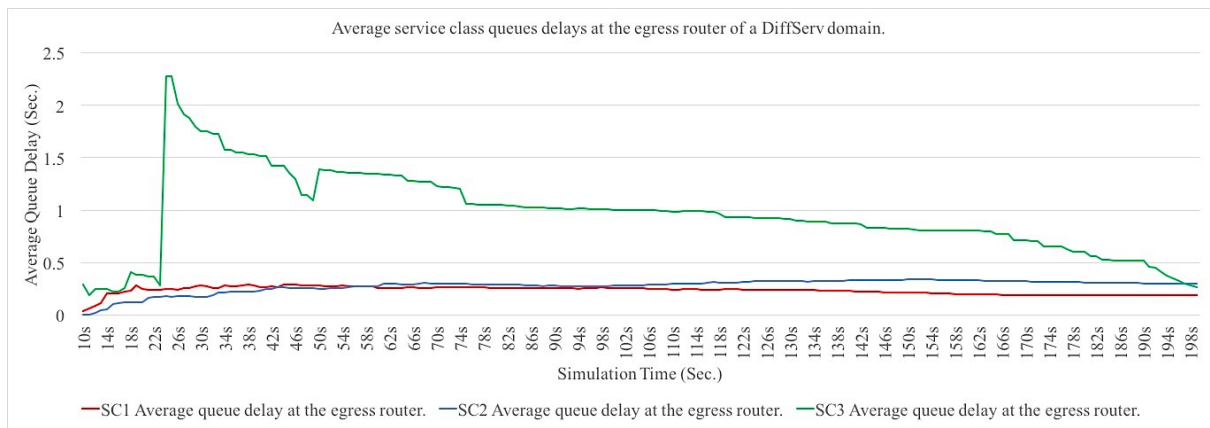


Figure 6-5, Average service class queue delays at the egress router of an upstream DiffServ domain

The ingress and egress routers of the simulated DiffServ domains have their service class scheduling rates adjusted dynamically within an interval  $k$ . Adjusting the scheduling rates of service classes dynamically maintains the delay differentiation between classes. Table 6-3 illustrates the average service class queue delay ratios and the delay differentiated parameter ratios for the whole period of simulation.

Table 6-3, service classes' average queue delay ratios and their delay differentiated parameter ratios for whole period of simulation – at the ingress router of a downstream DiffServ domain.

	SC1 class	SC2 class	SC3 class
Service class mean delay for the whole period of simulation (sec.)	0.501	1.076	2.365
Service class delay differentiated parameter ( $\delta$ )	1	2	4

	Service classes mean delays ratio	Service classes delay differentiated parameters ratio ( $\delta$ )
Class 1: class 2 ratio	0.465	0.5
Class 2: class 3 ratio	0.455	0.5
Class 1: class 3 ratio	0.212	0.25

## 6.6 Test Scenario 1 Analysis:

In test scenario 1, the upstream and downstream domains both consist of single core routers, as shown in Figure 6-1. The simulation diagram of the test scenario 1 topology is illustrated in appendix A-5.1- Figure 9-8. The configuration parameters of test scenario 1 is shown in Table 6-4. The best effort traffic (SC<sub>4</sub>) is injected into the downstream domain through the core routers to change the traffic conditions within that domain. This traffic can be injected into any defined service class queue depending on the available resources in those queues.

Table 6-4, Configuration parameters of test scenario 1.

Parameter	Configuration value	Notes
Link capacities of the upstream domain	2 Mbits	To ensure that more DiffServ traffic is injected into the downstream domain from the upstream domain.
Link capacity that connects the upstream and downstream domains.	4 Mbits	
link capacities of the downstream domain	2 Mbits	
Link capacity that connects the downstream domain and destination.	4 Mbits	
Both the upstream and downstream domains provide three service class queues	(SC <sub>1</sub> , SC <sub>2</sub> , SC <sub>3</sub> )	
The delay configuration parameters ( $\delta$ ) for service classes (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) at the edge DiffServ routers.	(1:2:4)	(Chin-Chang Li et al. 2000).
The service class markings in the upstream and downstream domains.	Identical.	
Service class queue buffer size in the upstream and downstream domains.	128 packets	(Wendell Odom 2005).
Minimum (lower) Service class queue threshold values (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the upstream and downstream domains.	(15/32:7/16:13/32) times the queue size.	(Cisco 2013)
Service class queue dropping probabilities (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the upstream and downstream domains.	(1/2:1/2:1/2) times the queue size.	(Cisco 2013)
Traffic Distribution in the upstream and downstream domains.	VoIP (SC <sub>1</sub> ) – Highest Priority, Video Traffic (SC <sub>2</sub> ), FTP (SC <sub>3</sub> ) – Lowest Priority, and Data Base Request message (Best Effort).	
Lower threshold congestion values for (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the downstream domain	(15/32:7/16:13/32)	(Cisco 2013)
Higher threshold congestion values for (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the downstream domain	(0.9:0.9:0.9)	To get a linear scale of resources management
Maximum Scheduling rate update factor ( $\beta_{max}$ ) for (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the downstream domain.	(0.9:0.9:0.9)	To damp the throughput at upstream domain.



The edge routers in the upstream and downstream domains use dynamic weights to forward their traffic while the core routers use pre-configured fixed weights; they forward packets from their queues based on this sequence ( $SC_1:SC_2:SC_3$ ) (5:4:3) packets respectively (Dovrolis 2000); it illustrated in an experiment how the weights of WFQ are selected and their effect in achieving controllable delay differentiation between classes.

As explained in section 6.3, to test the performance of the DRAM-NFV algorithm in managing resources within and across different DiffServ domains in test scenario (1), in the event of the traffic of certain service class queues are increased individually and the service class congestion levels at the ingress router of the downstream DiffServ domain is monitored by applying the DWFQ algorithm. In a proportional delay DiffServ domain, all the service class queues share one out link to forward their traffic. The highest priority service class queue is always occupying its required bandwidth for its traffic in the event of congestion while the other classes are competing between them to occupy the remaining bandwidth. This competition is based on the delay differentiated parameters ( $\delta$ ), as explained in chapter 4. Increasing the traffic in any of the non-highest or highest priority service classes can cause congestion in other classes because when the traffic of a service class queue increases, its service classes scheduling rate increases as the scheduling rate is directly proportional to the service class queue length. Consequently, the distribution of all service class weights, which represent the service classes' shares in the out link capacity, can be affected by granting the highest weight for the service class with the highest scheduling rate and the lowest weight for the service class with the least scheduling rate.

In test scenario (1), three case studies are undertaken to test the performance of the DRAM-NFV algorithm. These are as follows:

#### **6.6.1 Case Study 1- Test Scenario 1 Analysis:**

In this case study, The traffic of the medium priority service class ( $SC_2$ ) at the upstream domain of Figure 6-1 is increased - i.e., more  $SC_2$  traffic sources are connected to the DiffServ traffic sources node ( $n_0$ ) of the simulated test scenario (1) topology, appendix A-5.1- Figure 9-8. The measured congestion levels ( $\alpha$ ) in the service class queues ( $Q_{168}$ ) at the ingress router of the downstream DiffServ domain when resources are not managed across the DiffServ domains – i.e., by applying only the DWFQ algorithm – are shown in Figure 6-6, Figure 6-7 and Figure 6-8 (dashed curves) respectively. It is noticeable that the  $SC_1$  and  $SC_2$  queues suffer from different levels of congestion because of the increase in  $SC_2$  traffic, while the  $SC_3$  queue does not. The buffer resources of the  $SC_1$  and  $SC_3$  queues are not well utilized during the

periods of congestion in the  $SC_2$  queue at the ingress router of the downstream domain. This represents the research problem that needs to be solved through the managing of the resources across DiffServ domains.

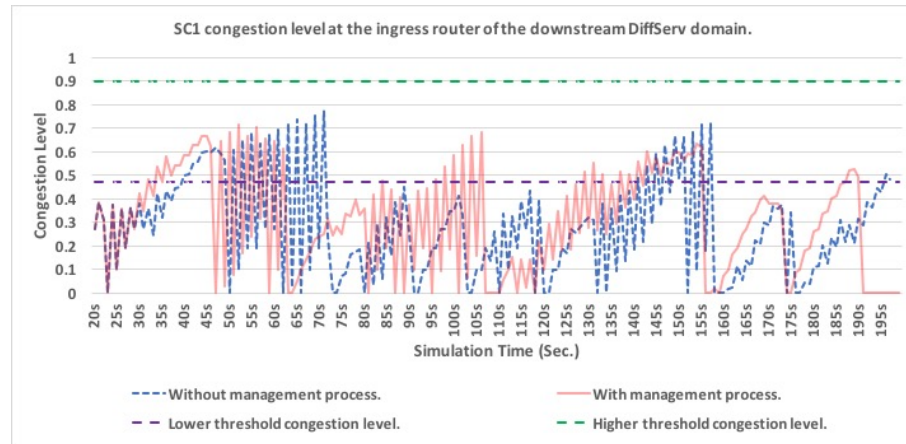


Figure 6-6,  $SC_1$  congestion level - case study 1, test scenario 1

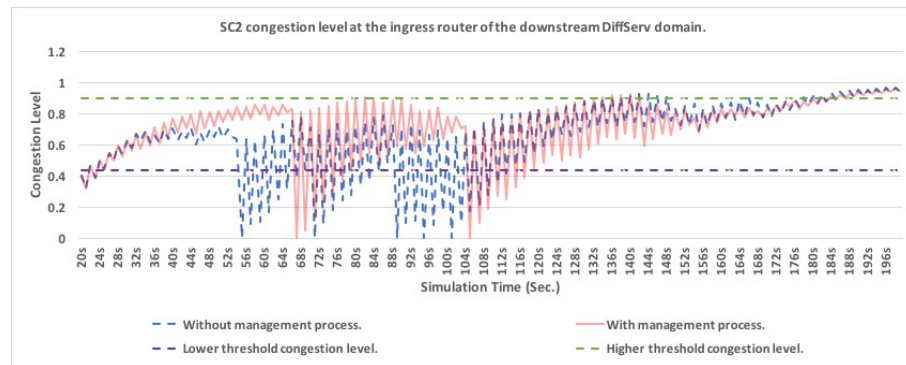


Figure 6-7,  $SC_2$  congestion level - case study 1, test scenario 1

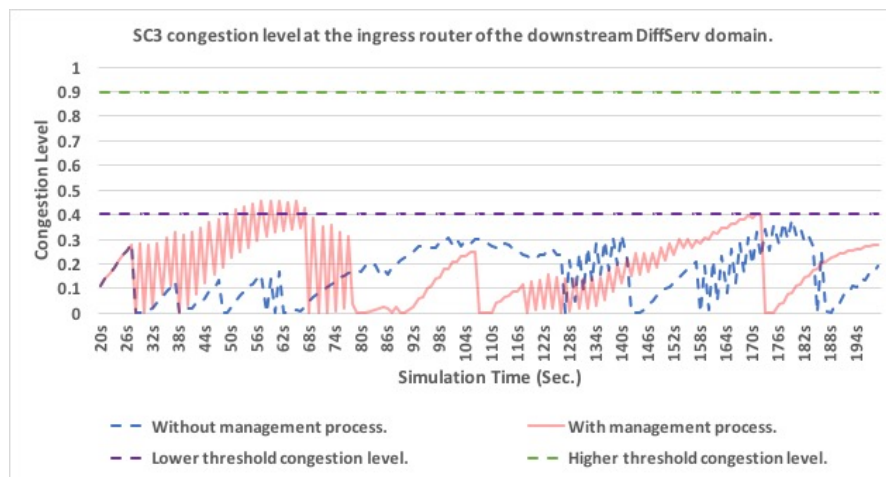


Figure 6-8,  $SC_3$  congestion level - case study 1, test scenario 1

When the DRAM-NFV algorithm is applied to manage resources across the DiffServ domains, the scheduling rates for the service class queues that, before, suffered from congestion at the egress router of the upstream DiffServ domain are reduced. Also the resources of the link that connects the DiffServ domains is redistributed in a different way such that the utilizations of service classes traffic at that link are organised based on the traffic conditions in the ingress router of the downstream domain and the traffic conditions at the egress router of the upstream domain.

In the event of congestion, the DRAM-NFV prevents any class that has a certain level of congestion above the lower threshold congestion level from occupying all the bandwidth it demands and grants some extra resources to the other classes. Because of this process, the link utilization for the highest priority service class ( $SC_1$ ) is reduced compared to the situation which pertains when only the DWFQ algorithm is applied, as shown in Figure 6-9, while, as shown in Figure 6-10 and Figure 6-11, the utilization of the medium and the lowest priorities service classes ( $SC_2$  and  $SC_3$ ) respectively are increased. The average percentage utilizations of the service classes at the link that connects the DiffServ domains, in relation to both the DWFQ and the DRAM-NFV algorithms, are shown in Table 6-5 for the whole period of simulation.

Table 6-5, Average percentage utilization for the service classes at the link that connects DiffServ domains - Test scenario 1, case study 1.

Service Class	Domains Link utilization with DWFQ algorithm	Domains Link utilization with DRAM-NFV algorithm
SC1	6.2%	4.72%
SC2	36.98%	38.42%
SC3	0.48%	0.53%

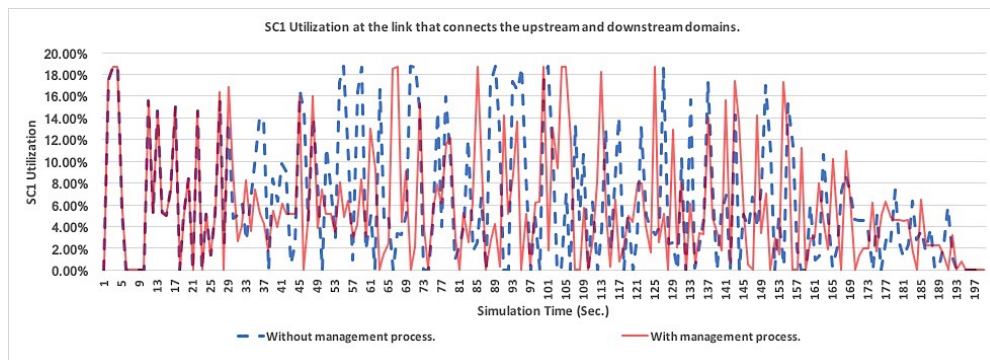


Figure 6-9,  $SC_1$  utilization at domains link - case study 1, test scenario 1.

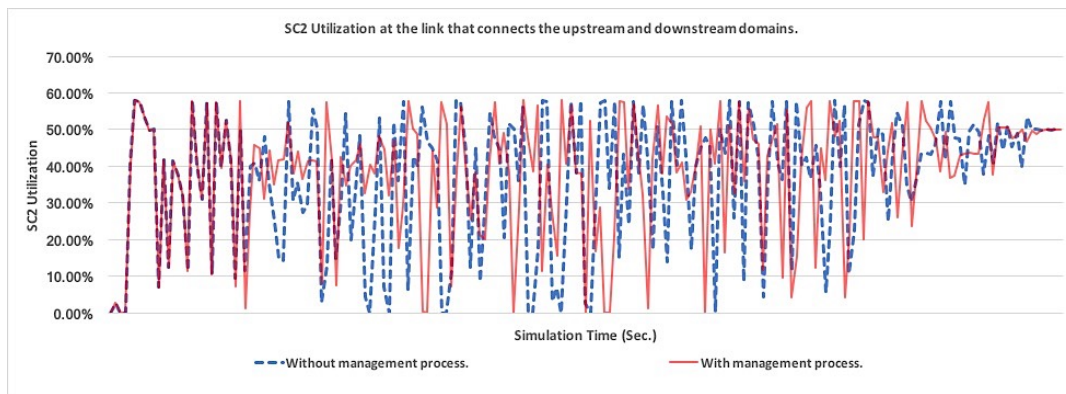


Figure 6-10, SC<sub>2</sub> utilization at domains link - case study 1, test scenario 1.

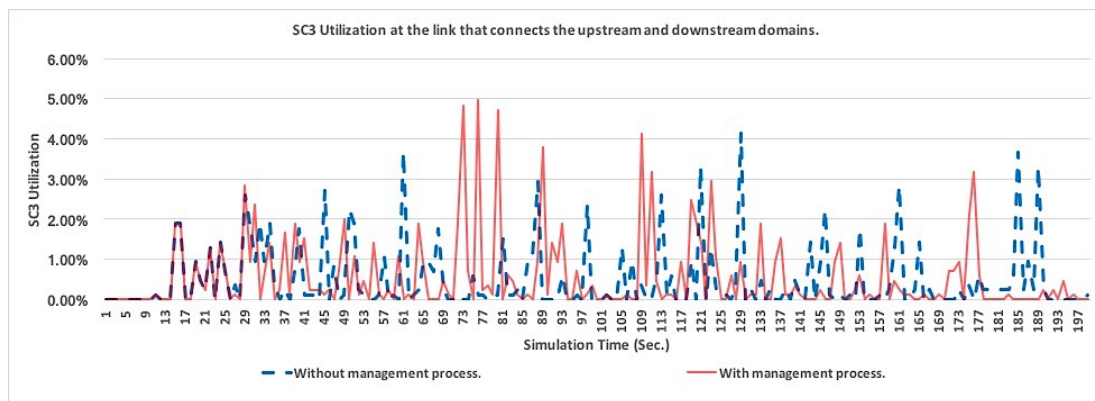


Figure 6-11, SC<sub>3</sub> utilization at domains link - case study 1, test scenario 1.

This improvement in that link's utilization creates also an improvement in the service classes' traffic utilization at the destination as illustrated in Table 6-6. These differences in service classes' traffic utilization values could be due to the injection of best effort traffic at the downstream domain which disturbs the traffic state at that domain.

Table 6-6, Average percentage utilization for the service classes at the destination link - Test scenario 1, case study 1.

Service Class	Destination Link utilization with DWFQ algorithm	Destination Link utilization with DRAM-NFV algorithm
SC1	1.55%	1.46%
SC2	16.26%	16.49%
SC3	0.428%	0.463%

The average End to End Delay for service classes traffic in the upstream and downstream DiffServ domains is illustrated in Table 6-7 for the whole period of simulation.

*Table 6-7, Average End-to-End Delay, test scenario 1 - case study 1.*

<b>Service Class</b>	<b>Average End-to-End delay using DWFQ algorithm</b>	<b>Average End-to-End delay using DRAM-NFV algorithm</b>	<b>Increased/ Decreased</b>
<b>SC1</b>	<b>4.01 sec</b>	<b>4.34 sec</b>	<b>Increased by 0.33 sec</b>
<b>SC2</b>	<b>4.29 sec</b>	<b>4.34 sec</b>	<b>Increased by 0.05 sec</b>
<b>SC3</b>	<b>105.77 sec</b>	<b>101.924 sec</b>	<b>Decreased by 3.85 sec</b>
<b>Overall traffic</b>	<b>9.361 sec</b>	<b>8.66 sec</b>	<b>Decreased by 0.7 sec</b>

When comparing the congestion levels of the service class queues at the ingress router of the downstream DiffServ domain Figure 6-6, Figure 6-7 and Figure 6-8. it is noticeable that DRAM-NFV increases the congestion level in these queues at some intervals during the simulation. This means that the DRAM-NFV algorithm participates in improving the service class buffer utilizations at the ingress router of the downstream domain during the periods of congestion in the SC<sub>1</sub> and SC<sub>2</sub> queues.

From Table 6-5, Table 6-6 and Table 6-7; it is noticeable that the DRAM-NFV algorithm achieves better balance for DiffServ domains resources through monitoring the bandwidth hungry service class at the downstream domain and managing its resources at the upstream domain. As a consequence of this, the utilizations of some service class across DiffServ domains are improved and the average End to End Delay for overall traffic is also reduced. Improving the utilizations of service classes traffic and reducing the average End to End Delay for overall traffic means that the DRAM-NFV algorithm participates in improving the QoS of application traffic during the congestion periods.

### **6.6.2 Case Study 2- Test Scenario 1 Analysis:**

In the second case study, the traffic for the medium and lowest priority service classes (SC<sub>2</sub>) and (SC<sub>3</sub>) of Figure 6-1 respectively are increased; by creating more SC<sub>2</sub> and SC<sub>3</sub> traffic sources at the DiffServ traffic sources node (n<sub>0</sub>) of the simulated test scenario (1) topology diagram, appendix A-5.1- Figure 9-8. When applying the DWFQ algorithm only for managing resources within the edge routers of the DiffServ domains, it is noticeable that the SC<sub>2</sub> queue at the ingress router of the downstream domain (Q<sub>168</sub>) suffers from congestion more than the others, as shown in Figure 6-13 (dashed curve). The SC<sub>1</sub> and SC<sub>3</sub> queues suffer from different levels of congestion, as shown in Figure 6-12 and Figure 6-14 respectively (dashed curves).



When the DRAM-NFV algorithm is applied, it causes a reduction in the congestion levels in the service class queues throughout the conditions pertaining to the periods of congestion that occurred when the DWFQ algorithm was applied, but it increases the congestion level in some intervals that were not congested when the DWFQ algorithm was applied, as shown in Figure 6-12, Figure 6-13 and Figure 6-14. These figures illustrate the congestion levels in the service class queues at the ingress router of the downstream domain when the DWFQ and subsequently the DRAM-NFV algorithms are applied.

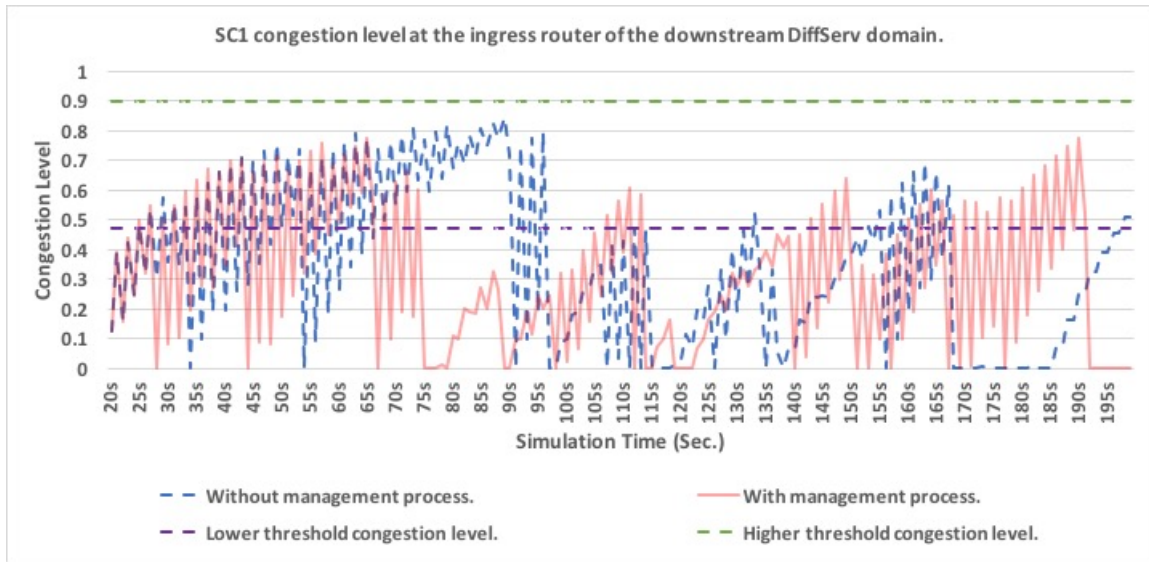


Figure 6-12, SC1 congestion levels in relation to the DWFQ and DRAM-NFV algorithms - case study 2, test scenario 1.

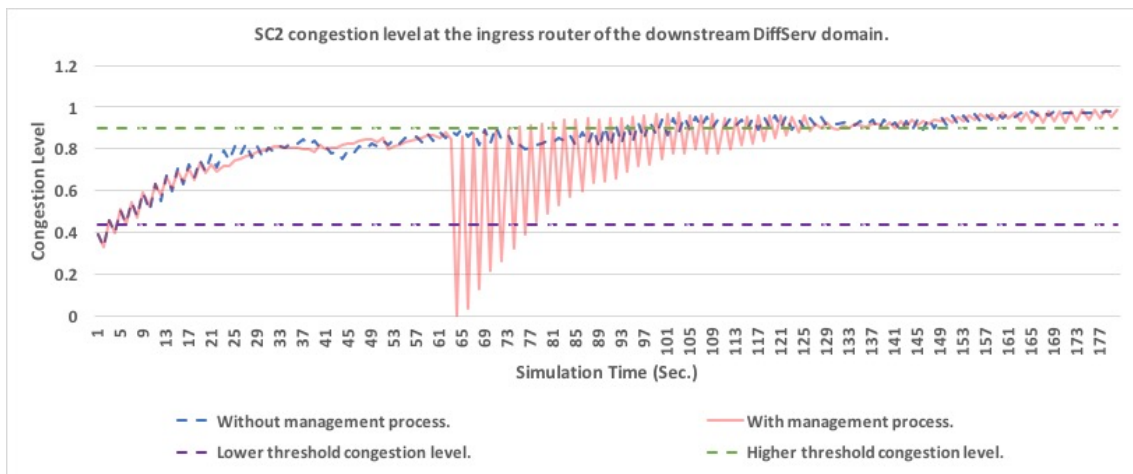


Figure 6-13, SC2 congestion levels in relation to the DWFQ and DRAM-NFV algorithms - case study 2, test scenario 1.

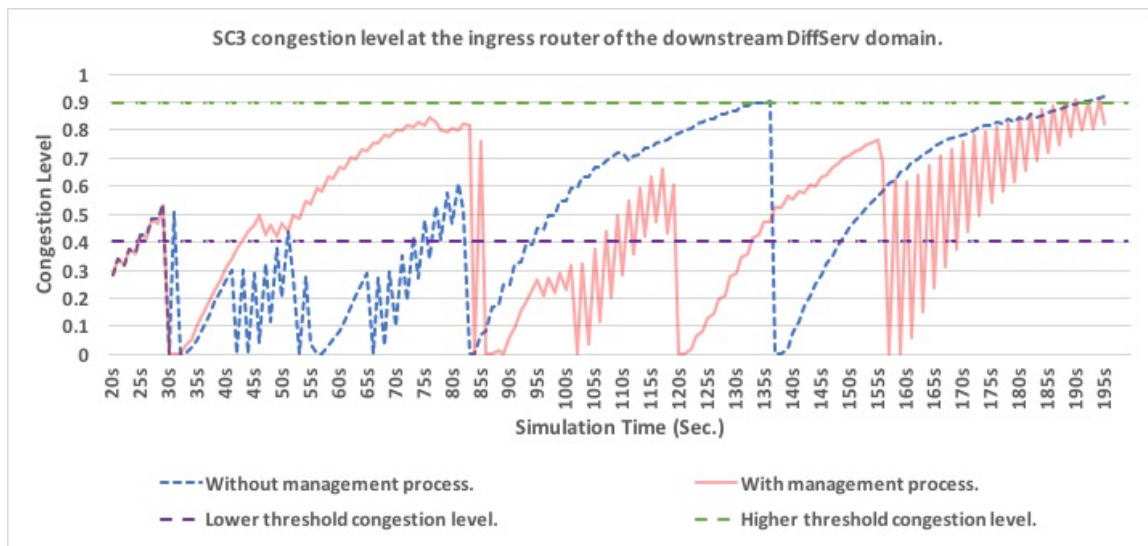


Figure 6-14, SC3 congestion levels in relation to the DWFQ and DRAM-NFV algorithms - case study 2, test scenario 1.

The DRAM- NFV algorithm reduces the scheduling rate for the corresponding service class queue at the egress router of the upstream domain in the event of congestion. This process will reduce the number of packets which are awaiting service in the corresponding service class queue at the ingress router of the downstream domain and thus, the average queue delay is also reduced. Figure 6-15, Figure 6-16 and Figure 6-17 illustrate the average queue delay at the ingress router of the downstream domain in relation to the DWFQ and DRAM-NFV algorithms. Moreover, the DRAM-NFV algorithm reallocate the resources of the link that connects the DiffServ domains, after reducing the scheduling rates, such that less resources are allocated at the egress router of the upstream domain for the congested service class queues and more resources are dedicated at the egress router of the upstream domain for the uncongested service class queues. This process of dedicating extra resources allows some extra traffic to be forwarded from the uncongested class at the egress router of the upstream domain towards the ingress router of the downstream domain.

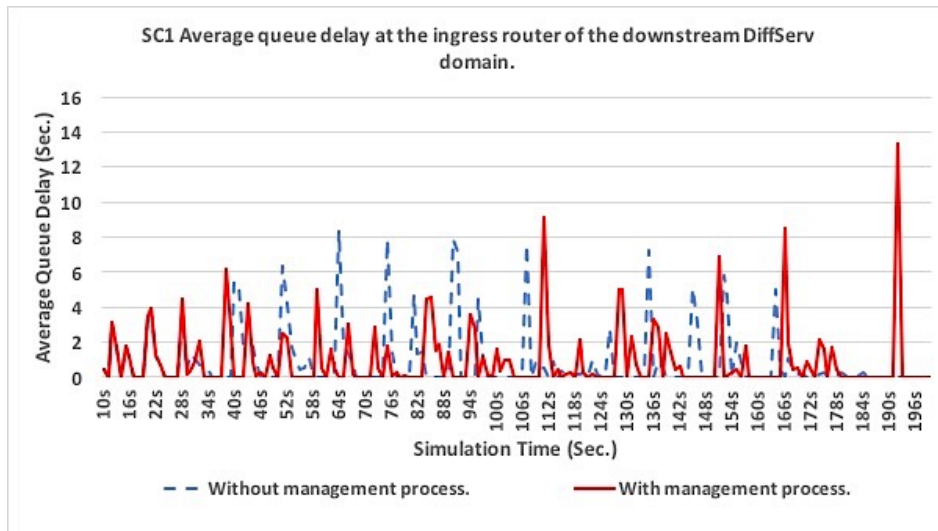


Figure 6-15, SC1 average queue delay - case study 2, test scenario 1.

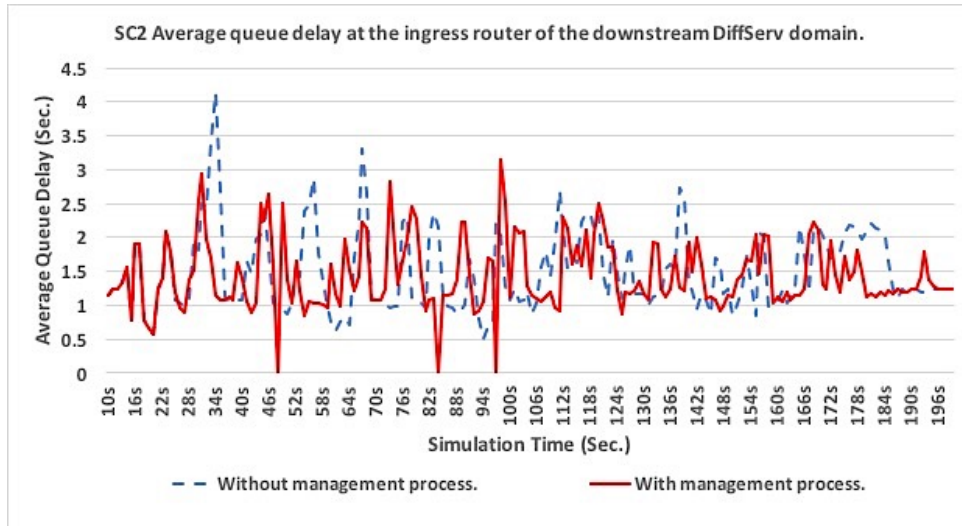


Figure 6-16, SC2 average queue delay - case study 2, test scenario 1

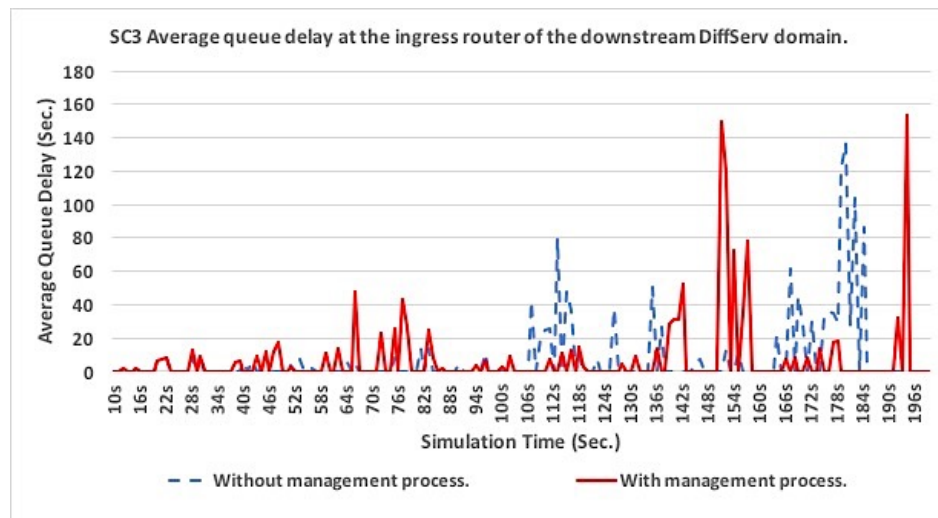


Figure 6-17, SC3 average queue delay - case study 2, test scenario 1



When the traffic of the medium and lowest priority service classes ( $SC_2$  and  $SC_3$ ) are increased, in case study 2, the utilization of the link that connects the DiffServ domains for  $SC_2$  and  $SC_3$  traffic is reduced and is organised according to the traffic conditions in the ingress router of the downstream domain and the traffic conditions in the egress router of the upstream domain. Table 6-8 illustrates the average percentage utilizations for the service classes traffic at the link that connects the upstream and downstream domains for the whole period of simulation. In terms of average End to End Delay, Table 6-9 illustrates the average End to End Delay for service classes traffic in the upstream and downstream DiffServ domains for the whole period of simulation.

Table 6-8, Average percentage utilization for the service classes at the link that connects DiffServ domains - Test scenario 1, case study 2.

<b>Service Class</b>	<b>Domains Link utilization with DWFQ algorithm</b>	<b>Domains Link utilization with DRAM-NFV algorithm</b>
<b>SC1</b>	<b>4.692%</b>	<b>4.799%</b>
<b>SC2</b>	<b>40.422%</b>	<b>40.166%</b>
<b>SC3</b>	<b>1.208%</b>	<b>1.172%</b>

Table 6-9, Average End-to-End Delay, test scenario 1 - case study 2.

<b>Service Class</b>	<b>Average End to End Delay using DWFQ algorithm</b>	<b>Average End to End Delay using DRAM-NFV algorithm</b>	<b>Increased/Decreased</b>
<b>SC1</b>	4.4226 sec	4.584 sec	Increased by 0.16 sec.
<b>SC2</b>	4.356 sec	4.454 sec	Increased by 0.1 sec.
<b>SC3</b>	84.353 sec	93.909 sec	Increased by 9.56 sec.
<b>Overall traffic</b>	10.937 sec	11.295 sec	Increased by 0.358 sec.

The use of DRAM-NFV causes some extra delays for the lowest priority service class ( $SC_3$ ). This increment in  $SC_3$  average End to End Delay does not have an effect on the QoS for the  $SC_3$  queue because this class is always dedicated to the traffic that can sustain delays and packet drops. The average End to End Delay for  $SC_1$  and  $SC_2$  traffic is increased slightly. Although the delay of  $SC_1$  traffic is increased slightly with the use of the DRAM-NFV algorithm, its traffic utilization across the DiffServ domains in the event of congestion is improved.

### 6.6.3 Case Study 3- Test Scenario 1 Analysis:

In the third case study for the test scenario 1- Figure 6-1, the traffic of all service classes is increased by creating more SC<sub>1</sub>, SC<sub>2</sub> and SC<sub>3</sub> traffic sources at the DiffServ traffic source node (n<sub>0</sub>) of the simulation test scenario (1) topology diagram , appendix A-5.1- Figure 9-8. First, as before the DWFQ algorithm is applied to the management of resources within the edge routers of the DiffServ domains. The service class queues at the ingress router of the downstream domain then suffer from different levels of congestion as shown in Figure 6-18, Figure 6-19 and Figure 6-20 (dashed curves).

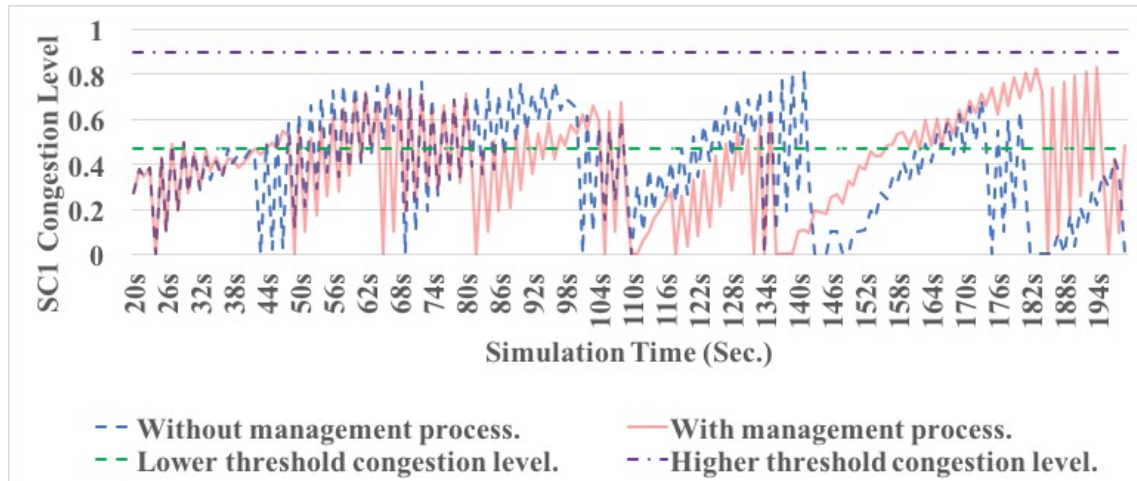


Figure 6-18, SC1 congestion level - case study 3, test scenario 1

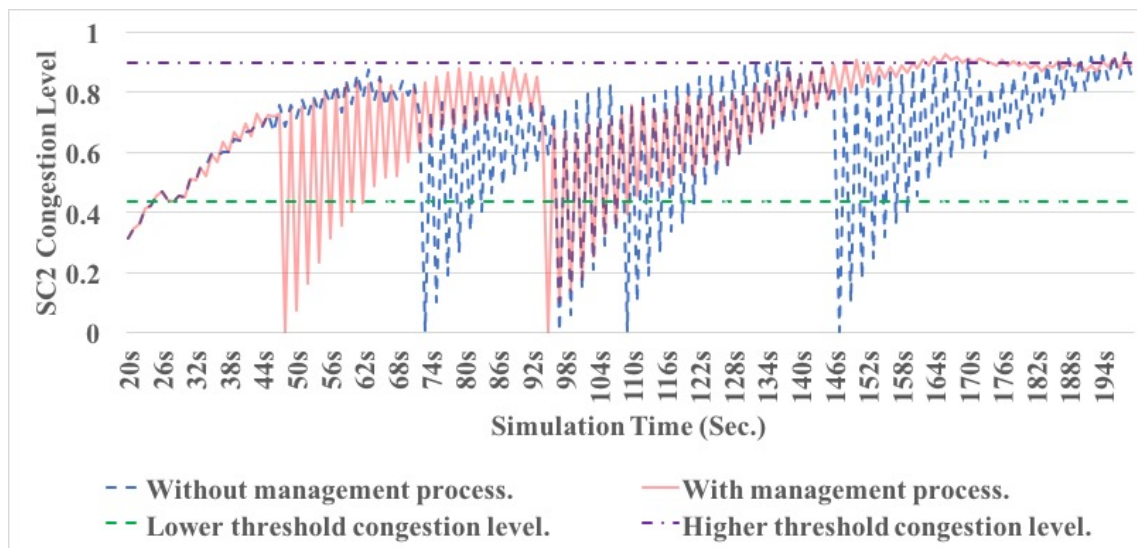


Figure 6-19, SC2 congestion level - case study 3, test scenario 1

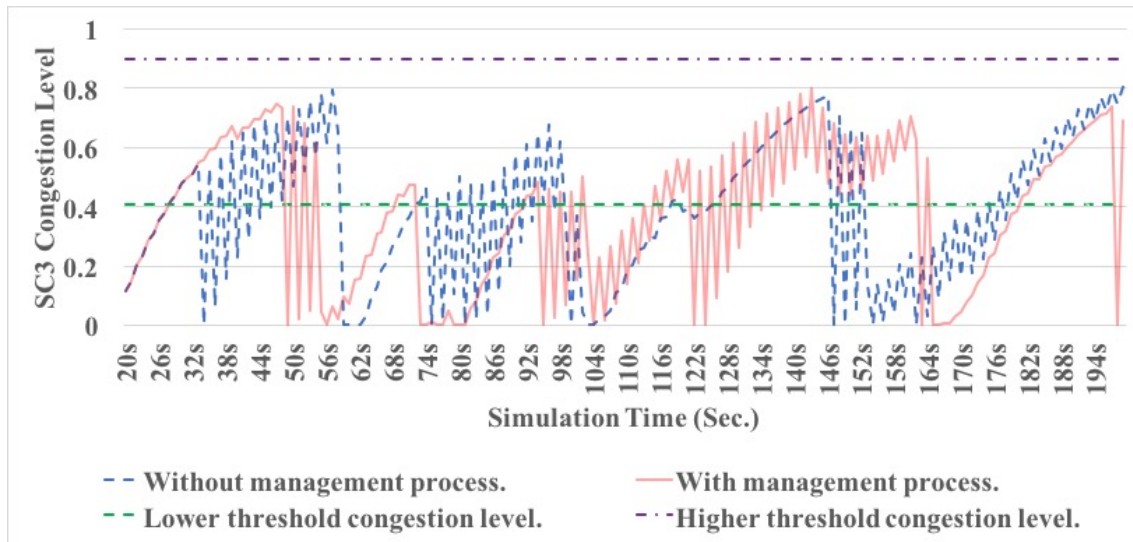


Figure 6-20, SC3 congestion level - case study 3, test scenario 1

When the DRAM-NFV algorithm is applied in place of the DWFQ algorithm, the utilizations of the link that connects the DiffServ domains for SC<sub>1</sub> and SC<sub>2</sub> traffic are increased while the SC<sub>3</sub> traffic utilization is decreased. The link's resources are organised according to the traffic conditions in the ingress router of the downstream domain and the traffic conditions in the egress router of the upstream domain. Table 6-10 illustrates the average percentage utilizations for the service classes traffic at the link that connects the upstream and downstream domains for the whole period of simulation. In DiffServ, it is impossible to improve the QoS for all service classes because all the service class queues use weights to share one out link in forwarding their traffic.

Table 6-10, Average percentage utilization for the service classes at the link that connects DiffServ domains - Test scenario 1, case study 3.

Service Class	Domains Link utilization with DWFQ algorithm	Domains Link utilization with DRAM-NFV algorithm
SC1	7.191%	7.251%
SC2	33.072%	33.167%
SC3	1.283%	1.091%

Although there is this improvement in the SC<sub>1</sub> and SC<sub>2</sub> traffic utilizations at the link that connects DiffServ domains which occurs when the DRAM-NFV algorithm is applied, the SC<sub>1</sub> utilization at the destination link is reduced because of the best effort traffic that is injected to change the network traffic conditions of the downstream domain through changing the traffic conditions in the egress router for the downstream domain. The SC<sub>3</sub> traffic utilization at the

destination link is also reduced while the SC<sub>2</sub> traffic utilization is increased. Table 6-11 illustrates the average percentage service classes' traffic utilizations at the destination for the whole period of simulation.

*Table 6-11, Average percentage utilization for the service classes at the destination link - Test scenario 1, case study 3.*

<b>Service Class</b>	<b>Destination Link utilization with DWFQ algorithm</b>	<b>Destination Link utilization with DRAM-NFV algorithm</b>
<b>SC1</b>	<b>1.80%</b>	<b>1.75%</b>
<b>SC2</b>	<b>14.575%</b>	<b>14.9%</b>
<b>SC3</b>	<b>0.62%</b>	<b>0.55 %</b>

In terms of average End to End Delay, the DRAM-NFV algorithm participates in reducing the average End to End Delay for overall traffic. The average End to End Delay for the highest and medium priority service classes are kept approximately the same. However, the average End to End Delay for the lowest priority service class SC<sub>3</sub> is decreased. Table 6-12 illustrates the average End to End Delay for service classes traffic in the upstream and downstream DiffServ domains for the whole period of simulation.

*Table 6-12, Average End-to-End Delay, test scenario 1 - case study 3.*

<b>Service Class</b>	<b>Average End-to-End delay using DWFQ algorithm</b>	<b>Average End-to-End delay using DRAM-NFV algorithm</b>	<b>Increased/ Decreased</b>
<b>SC1</b>	<b>4 sec</b>	<b>4 sec</b>	<b>-</b>
<b>SC2</b>	<b>4.6 sec</b>	<b>4.6 sec</b>	<b>-</b>
<b>SC3</b>	<b>107.388 sec</b>	<b>103.715 sec</b>	<b>Decreased by 3.67 sec</b>
<b>Overall traffic</b>	<b>11.214 sec</b>	<b>10.281</b>	<b>Decreased by 0.93 sec</b>

## 6.7 Test Scenario 2 Analysis:

In test scenario 2, the upstream domain consists of a single core router while the downstream domain consists of a number of multiple core routers, as shown in Figure 6-2. The simulation diagram of the test scenario 2 topology is illustrated in appendix A-5.2- Figure 9-9. The configuration parameters of test scenario 2 is shown in Table 6-13:

In the downstream domain, the packets are routed randomly within the domain. Best effort traffic ( $SC_4$ ) is injected into the downstream domain through the core routers to change the traffic conditions within that domain. This traffic can be injected into any of the service class queues, depending on the available resources in those queues. The edge routers in the upstream and downstream domains use dynamic weights to forward their traffic while the core routers use pre-configured fixed weights; they forward packets from their queues based on this sequence ( $SC_1:SC_2:SC_3$ ) (5:4:3) packets respectively as justified that in 6.6.

Each out-port of the ingress router for the downstream domain contains a queue for each defined service class. The scheduling rate for each class is configured by taking the maximum average service class queue length and the minimum average queue delay to maximize the throughput of service classes, as explained in section 4.4. The service class congestion level at these out-port queues is measured by taking the maximum average service class queue length at these out-port queues, as explained previously in section 4.4.

In order to test the performance of the DRAM-NFV algorithm in managing resources across the upstream and downstream domains of test scenario 2, first, the traffic of a selected service class is increased individually. This increment in the service class traffic causes congestion in the selected service class queue and also in some other queues because all service classes queues in DiffServ are competing between themselves to occupy the out link capacity of the DiffServ router. The performance of the DRAM-NFV algorithm is then compared with the performance of the DWFQ algorithm – this latter does not manage resources across DiffServ domains.

Table 6-13, Configuration parameters of test scenario 2.

Parameter	Configuration value	Notes
Link capacities of the upstream domain	5 Mbits	To ensure that more DiffServ traffic is injected into the downstream domain from the upstream domain.
Link capacity that connects the upstream and downstream domains.	10 Mbits	
link capacities of the downstream domain	1 Mbits	
Link capacity that connects the downstream domain and destination.	3 Mbits	
Both the upstream and downstream domains provide three service class queues	(SC <sub>1</sub> , SC <sub>2</sub> , SC <sub>3</sub> )	
The delay configuration parameters ( $\delta$ ) for service classes (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) at the edge DiffServ routers.	(1:2:4)	(Chin-Chang Li et al. 2000).
The service class markings in the upstream and downstream domains.	Identical.	
Service class queue buffer size in the upstream and downstream domains.	128 packets	(Wendell Odom 2005).
Minimum (lower) Service class queue threshold values (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the upstream and downstream domains.	(15/32:7/16:13/32) times the queue size.	(Cisco 2013)
Service class queue dropping probabilities (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the upstream and downstream domains.	(1/2:1/2:1/2) times the queue size.	(Cisco 2013)
Traffic Distribution in the upstream and downstream domains.	VoIP (SC <sub>1</sub> ) – Highest Priority, Video Traffic (SC <sub>2</sub> ), FTP (SC <sub>3</sub> ) – Lowest Priority, and Data Base Request message (Best Effort).	
Lower threshold congestion values for (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the downstream domain	(15/32:7/16:13/32)	(Cisco 2013)
Higher threshold congestion values for (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the downstream domain	(0.9:0.9:0.9)	To get a linear scale of resources management
Maximum Scheduling rate update factor ( $\beta_{max}$ ) for (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the downstream domain.	(0.9:0.9:0.9)	To damp the throughput at upstream domain.

In test scenario (2), two case studies are undertaken to test the performance of the DRAM-NFV algorithm. These are as follows:

### 6.7.1 Case Study 1- Test Scenario 2 Analysis:

In case study 1 of test scenario 2 - Figure 6-2, only the traffic of the lowest priority service class queue ( $SC_3$ ) increases through increasing the number of ( $SC_3$ ) sources that are created at the node ( $n_0$ ) of the test scenario 2 topology, appendix A-5.2- Figure 9-9. The service class queues at the out ports of the ingress router for the downstream domain ( $Q_{268}$ ,  $Q_{269}$  and  $Q_{2610}$ ) also suffer from levels of congestion. These levels are measured for the circumstance in which the DWFQ algorithm only is used for scheduling, as illustrated in Figure 6-21, Figure 6-22 and Figure 6-23 (dashed curves).

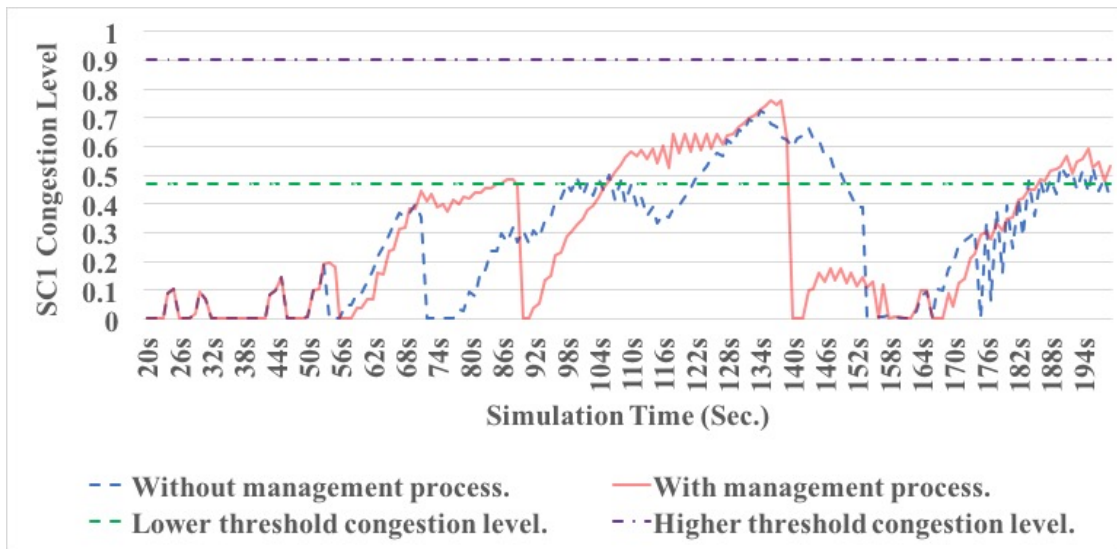


Figure 6-21, SC1 maximum congestion level using the DWFQ and DRAM-NFV algorithms – case study 1, test scenario 2

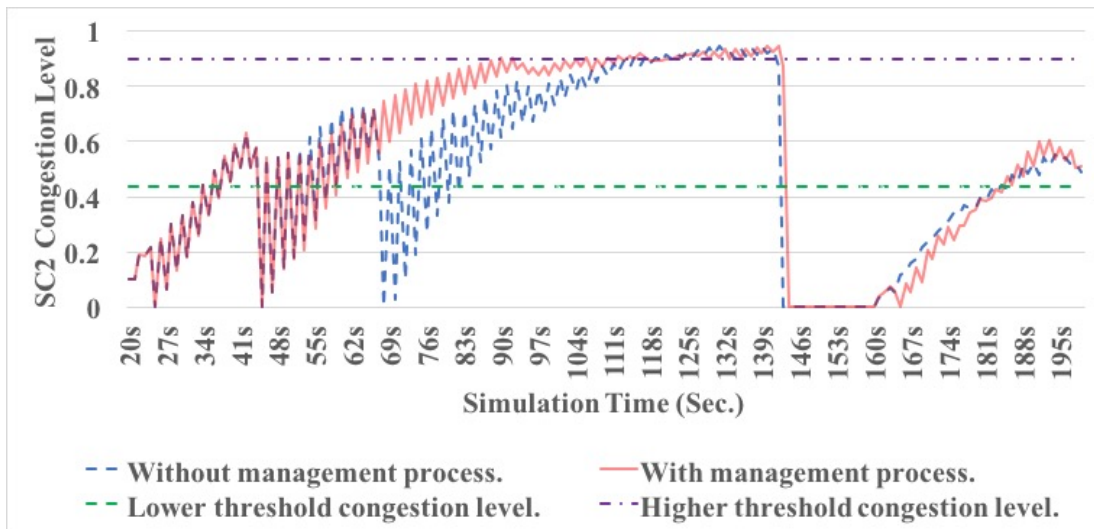


Figure 6-22, SC2 maximum congestion level using the DWFQ and DRAM-NFV algorithms - case study 1, test scenario 2



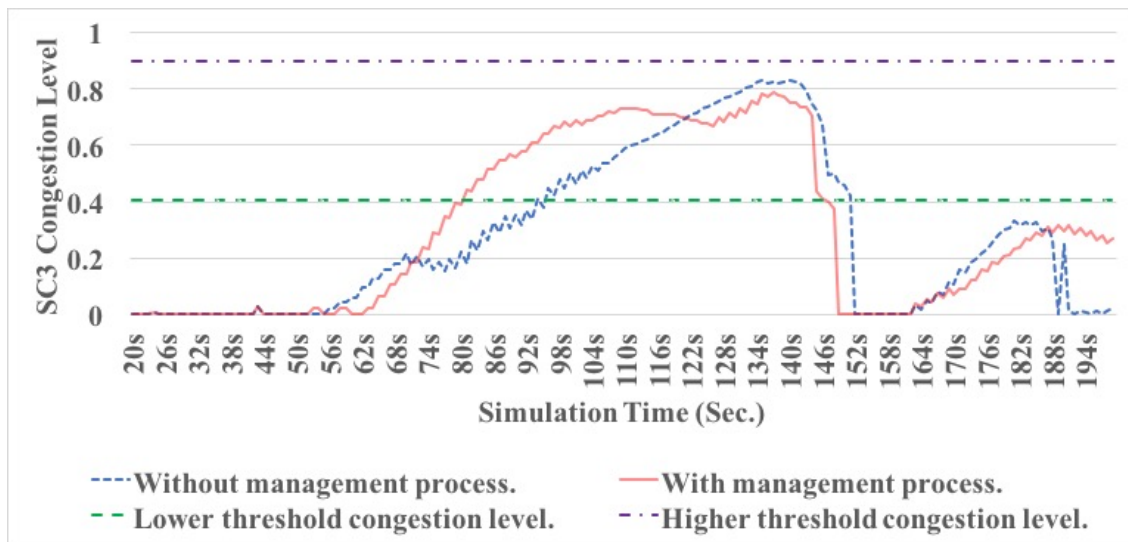


Figure 6-23, SC3 maximum congestion level using the DWFQ and DRAM-NFV algorithms - case study 1, test scenario 2

When managing the resources of the upstream and downstream domains using the DRAM-NFV algorithm, the utilization of the link that connects the DiffServ domains is organised based on the traffic conditions in the out port queues of the ingress router of the downstream domain and the traffic conditions in the egress router of the upstream domain.

By comparing the utilization of that link when using the DWFQ and then the DRAM-NFV algorithms in turn, it can be seen that the DRAM-NFV algorithm prevents the highest priority service class (SC<sub>1</sub>) from occupying the required bandwidth for its traffic at the egress router of the upstream domain whilst congestion is occurring. This means that the DRAM-NFV algorithm grants some extra resources to SC<sub>2</sub> traffic at the egress router of the upstream domain at the expense of SC<sub>1</sub> and the increasing SC<sub>3</sub> traffic. This extra resource causes an increment in the SC<sub>2</sub> congestion level at the out ports queues for the ingress router, as shown in Figure 6-22. Table 6-14 illustrates the average percentage utilizations for the service classes traffic at the link that connects the upstream and downstream domains for the whole period of simulation.

Table 6-14, Average percentage utilization for the service classes at the link that connects DiffServ domains - Test scenario 2, case study 1.

Service Class	Domains Link utilization with DWFQ algorithm	Domains Link utilization with DRAM-NFV algorithm
SC1	2.241%	2.09%
SC2	23.557%	24.215%
SC3	0.371%	0.298%



The average End to End Delay for all the service class traffic in the upstream and downstream DiffServ domains for the whole period of simulation is shown in Table 6-15. The average End to End Delays for SC<sub>1</sub> and SC<sub>2</sub> traffic are reduced slightly while the average End to End Delay for the lowest priority service class (SC<sub>3</sub>) is reduced dramatically.

Table 6-15, Average End-to-End Delay, test scenario 2 - case study 1.

Service Class	Average End to End Delay using DWFQ algorithm	Average End to End Delay using DRAM-NFV algorithm	Increased/Decreased
SC1	5.98 sec	5.85 sec	Decreased by 0.13 sec.
SC2	5.51 sec	5.46 sec	Decreased by 0.05 sec.
SC3	123.6 sec	105.74 sec	Decreased by 17.86 sec.
Overall traffic	11.3 sec	10.6 sec	Decreased by 0.7 sec.

### 6.7.2 Case Study 2- Test Scenario 2 Analysis:

In case study 2 of test scenario 2 - Figure 6-2, the traffic of all the service class queues is increased by increasing the number of service class traffic source applications at the DiffServ traffic sources node ( $n_0$ ) of the simulation scenario (2) topology diagram, appendix A-5.2- Figure 9-9. The congestion levels ( $\alpha$ ) for the service class (SC<sub>1</sub>, SC<sub>2</sub> and SC<sub>3</sub>) queues at the out-ports of the ingress router ( $Q_{268}$ ,  $Q_{269}$  and  $Q_{2610}$ ) of the downstream domain are then measured. This is carried out, first, in relation to the use of the DWFQ algorithm only which allocates resources within the edge routers of a DiffServ domain only. Figure 6-24, Figure 6-25 and Figure 6-26 (dashed curves) illustrate the measured maximum congestion levels in the queues of the service classes (SC<sub>1</sub>, SC<sub>2</sub> and SC<sub>3</sub>) at the out-ports for the ingress router of the downstream domain. It is noticeable that all service class queues at these out-ports suffer from various levels of congestions when the DWFQ algorithm is applied. However, the (SC<sub>2</sub>) queue is considerably more congested than the other classes.

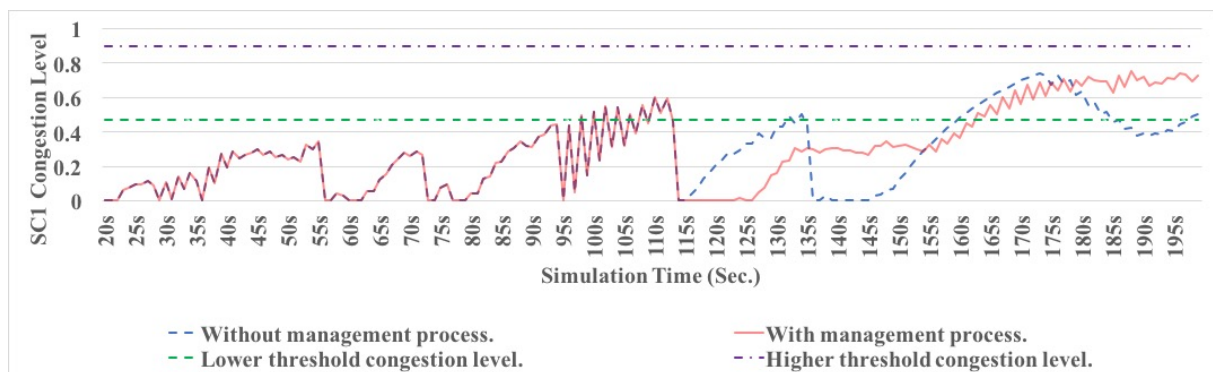


Figure 6-24, SC1 maximum congestion level using the DWFQ and DRAM-NFV algorithms – case study 2, test scenario 2

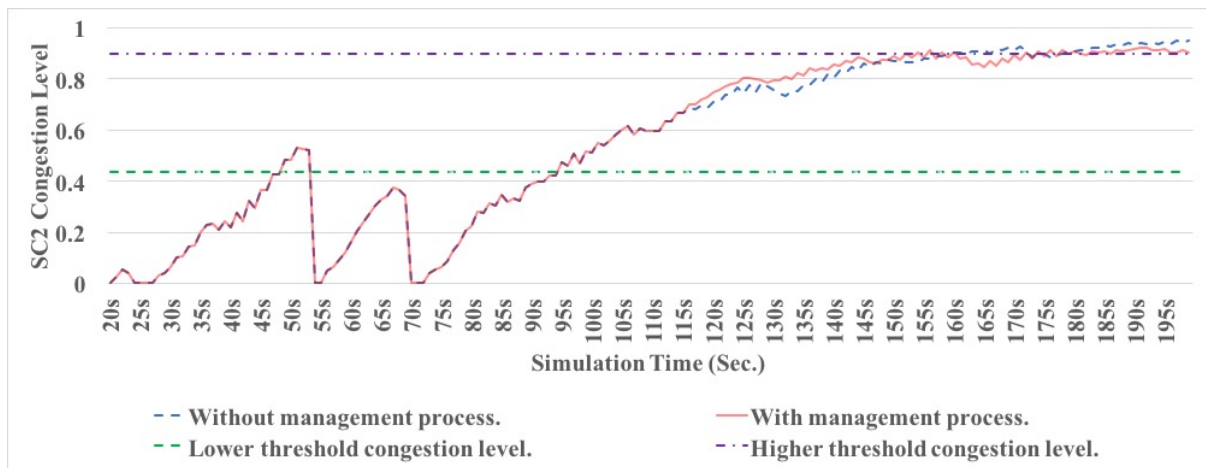


Figure 6-25, SC2 maximum congestion level using the DWFQ and DRAM-NFV algorithms – case study 2, test scenario 2.

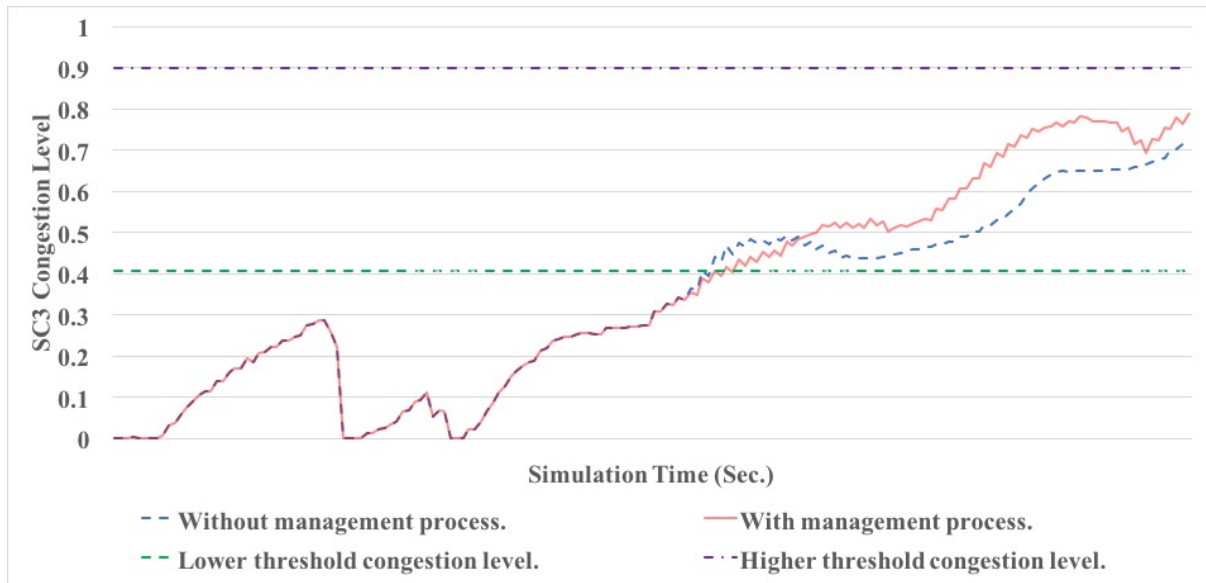


Figure 6-26, SC3 maximum congestion level using the DWFQ and DRAM-NFV algorithms – case study 2, test scenario 2.

When the DRAM-NFV algorithm is applied to manage resources across the different DiffServ domains (and within the edge routers of DiffServ domains), in the event of congestion, the utilization of the link that connects the upstream and downstream domains for SC<sub>1</sub> and SC<sub>3</sub> traffic is increased, whilst congestion occurs in the SC<sub>2</sub> queues because the DRAM-NFV algorithm works by reducing the scheduling rates of the congested service class queue at the egress router of the upstream domain and reallocates the resources of the link that connects the DiffServ domains. Table 6-16 illustrates the average percentage utilizations for the service class traffic at the link that connects the upstream and downstream domains for the whole period of simulation.

Table 6-16, Average percentage utilization for the service classes at the link that connects DiffServ domains - Test scenario 2, case study 2.

<b>Service Class</b>	<b>Domains Link utilization with DWFQ algorithm</b>	<b>Domains Link utilization with DRAM-NFV algorithm</b>
<b>SC1</b>	<b>2.2%</b>	<b>2.25%</b>
<b>SC2</b>	<b>25.1%</b>	<b>24.7%</b>
<b>SC3</b>	<b>0.335%</b>	<b>0.458%</b>

In addition to improving the utilization of the link that connects the DiffServ domains, the utilizations at the destination of the SC<sub>1</sub> and SC<sub>3</sub> traffic are also increased while the SC<sub>2</sub> traffic utilization at that destination is decreased. Table 6-17 illustrates the average percentage utilizations for the service classes traffic at the destination link for the whole period of simulation.

Table 6-17, Average percentage utilization for the service classes at the destination link - Test scenario 2, case study 2.

<b>Service Class</b>	<b>Destination Link utilization with DWFQ algorithm</b>	<b>Destination Link utilization with DRAM-NFV algorithm</b>
<b>SC1</b>	<b>1.358%</b>	<b>1.762%</b>
<b>SC2</b>	<b>23.606%</b>	<b>20.7783%</b>
<b>SC3</b>	<b>0.479%</b>	<b>0.8325%</b>

when resources are managed using the DRAM-NFV algorithm, the average End to End Delay for the highest priority service class traffic (SC<sub>1</sub>) is decreased while the average End to End Delay for the congested service class queue (SC<sub>2</sub>) and for the lowest priority service class traffic (SC<sub>3</sub>) are increased. Table 6-18 shows the average End-to-End Delay of service classes traffic in the upstream and downstream domains for the whole period of simulation.

Table 6-18, Average End-to-End Delay, test scenario 2 - case study 2.

<b>Service Class</b>	<b>Average End to End Delay using DWFQ algorithm</b>	<b>Average End to End Delay using DRAM-NFV algorithm</b>	<b>Increased/Decreased</b>
<b>SC1</b>	6.7 sec	6.4 sec	Decreased by 0.3 sec.
<b>SC2</b>	5.47 sec	5.8 sec	Increased by 0.33 sec.
<b>SC3</b>	85.097 sec	116.07 sec	Increased by 31 sec.
<b>Overall traffic</b>	7.79 sec	11.99 sec	Increased by 4.2 sec.

The DRAM-NFV algorithm improves the utilization of the highest priority service class traffic ( $SC_1$ ) and reduces its average End to End Delay throughout the time that the ( $SC_2$ ) queue is congested. Moreover, it improves the utilization of the lowest priority service class ( $SC_3$ ) whereas it causes some extra delay to its traffic. This increment in  $SC_3$  average End to End Delay does not effect the QoS for the ( $SC_3$ ) queue because this class is dedicated to the traffic that can most sustain delays and packet drops, while reducing the End to End Delay and increasing the utilization (throughput) of ( $SC_1$ ) are considered important factors in regard to improving the QoS of ( $SC_1$ ) when congestion occurs in  $SC_2$  the queue because the traffic of  $SC_1$ , which is VoIP traffic as detailed in section 6.2, is very sensitive to delay and packet drop.

### **6.8 Test Scenario 3 Analysis:**

In test scenario 3, there are two upstream DiffServ domains that forward their traffic to one downstream DiffServ domain, as shown in Figure 6-3. One of the upstream domains (Domain 1) is configured to attempt to manage resources across the domains while the other domain (Domain 3) only manages its own egress router resources and does not take into consideration the traffic conditions in the downstream domain. The simulation diagram of the test scenario 3 topology is illustrated in appendix A-5.3, Figure 9-10. The upstream domains consist of single core routers while the downstream domain consists of two ingress routers with multiple out ports connected to a multi-core router arrangement which sends packets to one egress router with a single out port, as illustrated in Figure 6-3. The configuration parameters of test scenario 3 is shown in Table 6-19.

In the downstream domain, packets are routed randomly within the domain. Best effort traffic ( $SC_4$ ) is not injected into the downstream domain. The edge routers in the upstream and downstream domains use dynamic weights to forward their traffic while the core routers use pre-configured fixed weights; they forward packets from their queues based on this sequence ( $SC_1:SC_2:SC_3$ ) (5:4:3) packets respectively as justified that in 6.6.

The scheduling rates and the congestion levels of each service class queue at each out-port of the ingress routers for the downstream domain are determined by taking the maximum average queue length and minimum average queue delay at these out ports, as explained in section 4.4. The service class scheduling rate at all the out-ports is the same and the process of calculating the congestion level in a specific service class is based on the maximum average service class queue length across all the out-port queues of the ingress routers.

Three case studies are examined in order to evaluate the performance of the DRAM-NFV algorithm in managing resources across the upstream and downstream domains of test scenario

3 whilst congestion is occurring. This performance is compared with that of the DWFQ algorithm which does not manage resources across different DiffServ domains. These cases are as follows:

Table 6-19, Configuration parameters of test scenario 3.

Parameter	Configuration value	Notes
Link capacities of the upstream domain	5 Mbits	To ensure that more DiffServ traffic is injected into the downstream domain from the upstream domain.
Link capacity that connects the upstream and downstream domains.	6 Mbits	
link capacities of the downstream domain	1 Mbits	
Link capacity that connects the downstream domain and destination.	3 Mbits	
Both the upstream and downstream domains provide three service class queues	(SC <sub>1</sub> , SC <sub>2</sub> , SC <sub>3</sub> )	
The delay configuration parameters ( $\delta$ ) for service classes (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) at the edge DiffServ routers.	(1:2:4)	(Chin-Chang Li et al. 2000).
The service class markings in the upstream and downstream domains.	Identical.	
Service class queue buffer size in the upstream and downstream domains.	128 packets	(Wendell Odom 2005).
Minimum (lower) Service class queue threshold values (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the upstream and downstream domains.	(15/32:7/16:13/32) times the queue size.	(Cisco 2013)
Service class queue dropping probabilities (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the upstream and downstream domains.	(1/2:1/2:1/2) times the queue size.	(Cisco 2013)
Traffic Distribution in the upstream and downstream domains.	VoIP (SC <sub>1</sub> ) – Highest Priority, Video Traffic (SC <sub>2</sub> ), FTP (SC <sub>3</sub> ) – Lowest Priority, and Data Base Request message (Best Effort).	
Lower threshold congestion values for (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the downstream domain	(15/32:7/16:13/32)	(Cisco 2013)
Higher threshold congestion values for (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the downstream domain	(0.9:0.9:0.9)	To get a linear scale of resources management
Maximum Scheduling rate update factor ( $\beta_{max}$ ) for (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the downstream domain.	(0.9:0.9:0.9)	To damp the throughput at upstream domain.

### 6.8.1 Case Study 1- Test Scenario 3 Analysis:

In case study 1 of test scenario 3 - Figure 6-3, the traffic of the medium priority service class ( $SC_2$ ) at the upstream domain (Domain 3) is only increased such that the numbers of the service class traffic sources at ( $n_0$ ), appendix A-5.3 - Figure 9-10, of the upstream domain (Domain 1) are (90:90:90) sources for ( $SC_1:SC_2:SC_3$ ) respectively and the numbers of service class traffic sources at ( $n_1$ ) of (Domain 3) are (90:120:90) sources for ( $SC_1:SC_2:SC_3$ ) respectively. The maximum congestion level of the queues that belong to a specific service class at the out ports of the ingress routers for the downstream domain is measured, first, using the DWFQ algorithm. Figure 6-27, Figure 6-28 and Figure 6-29 show comparisons of maximum congestion levels in service class queues when using the DRAM-NFV algorithm as compared to the situation which pertains when using DWFQ algorithm only. It is noticeable that the ( $SC_1$ ) and ( $SC_2$ ) queues suffer from different levels of congestion with DWFQ algorithm, Figure 6-27 and Figure 6-28 (dashed curves). The  $SC_2$  queues at the ingress routers are considerably more congested than the ( $SC_1$ ) queues while the ( $SC_3$ ) queues are the least congested queues, Figure 6-29 (dashed curves).

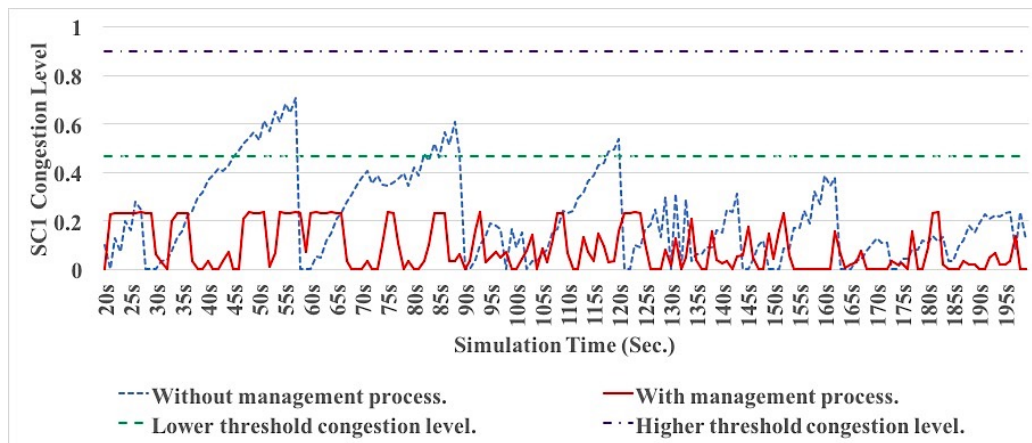


Figure 6-27 SC1 maximum congestion levels using the DWFQ and DRAM-NFV algorithms - case study 1, test scenario 3.

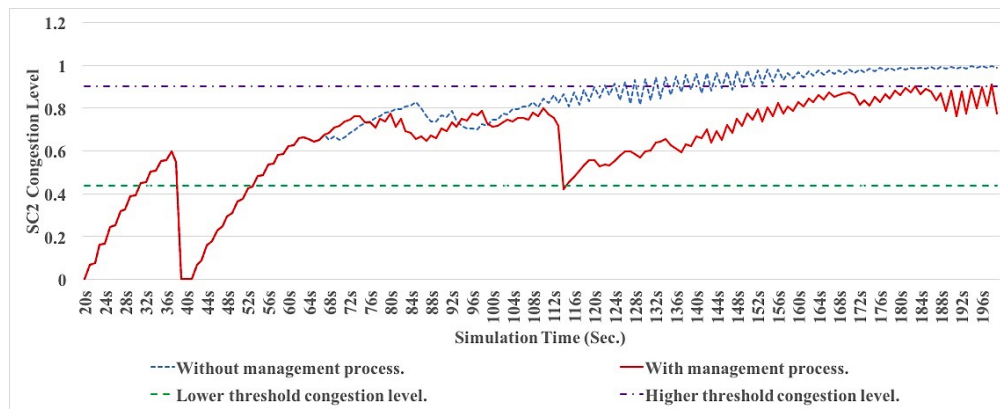


Figure 6-28, SC2 maximum congestion levels using the DWFQ and DRAM-NFV algorithms – case study 1, test scenario 3.

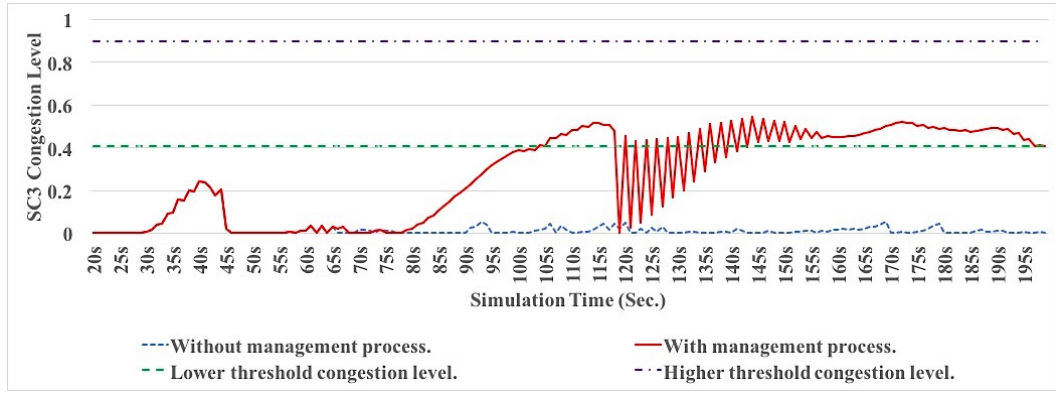


Figure 6-29, SC3 maximum congestion levels using the DWFQ and DRAM-NFV algorithms - case study 1, test scenario 3.

When the DRAM-NFV algorithm is applied, the congestion level in a service class queue can be reduced throughout periods of congestion but are increased during periods in which there is no congestion. As has been stated previously, the algorithm reduces the scheduling rate of the equivalent service class queue at the egress router of the upstream domain and reallocates the resource of the link that connects the upstream and downstream domains after that, thus this will reduce the number of packets that are waiting for service in the congested class and increase the number of packets in the uncongested class at the ingress router of the downstream domain. When the average service class queue length is reduced, the average service class queue delay is also reduced because the average service class queue delay is directly proportional to the average service class queue length.

Through the use of the DRAM-NFV algorithm, the resources of the link that connects the upstream domain (Domain 1) with the downstream domain, see Figure 6-3 – are reallocated since the service classes' scheduling rates take into account the traffic conditions in the ingress routers of the downstream domain and the traffic conditions at the egress router of the upstream domain (Domain 1). The utilizations of some service classes' traffic at the abovementioned link are improved such that the highest and the lowest priority service classes ( $SC_1$ ) and ( $SC_3$ ) traffic utilizations are increased throughout the presence of congestion in the ( $SC_2$ ) queue at the ingress routers of the downstream domain. In contrast, the average percentage utilization for the medium priority service class ( $SC_2$ ), which is considerably more congested than the other queues, is reduced. Table 6-20 illustrates the average percentage utilizations for the service classes traffic at the link that connects the upstream (Domain 1) and downstream domains for the whole period of simulation.



Table 6-20, Average percentage utilization for the service classes at the link that connects the upstream (Domain 1) and downstream DiffServ domains - Test scenario 3, case study 1.

<b>Service Class</b>	<b>Domains Link utilization with DWFQ algorithm</b>	<b>Domains Link utilization with DRAM-NFV algorithm</b>
<b>SC1</b>	<b>6.706%</b>	<b>8.223%</b>
<b>SC2</b>	<b>44.674 %</b>	<b>40.437%</b>
<b>SC3</b>	<b>0.47%</b>	<b>0.52 %</b>

It can be seen that the utilization of the (SC<sub>1</sub>) traffic at the link that connects the upstream domain (Domain 1) to the downstream domain is improved. However, (SC<sub>1</sub>) utilization at the destination link of the downstream domain is reduced because of the increasing (SC<sub>2</sub>) traffic which is forwarded from the unmanaged upstream domain (Domain 3) towards the downstream domain. In contrast, the utilizations for the SC<sub>2</sub> and SC<sub>3</sub> traffic at the destination link increase. This encourages the study of the circumstance whereby the resources of all upstream domains are managed (across domains) in preference to the situation where a single upstream domain is managed in this way, while other upstream domains are not. Table 6-21 illustrates the average percentage utilizations for the service classes traffic at the destination link when managing the upstream domain (Domain 1) only for the whole period of simulation.

Table 6-21, Average percentage utilization for the service classes at the destination link when managing the upstream domain (Domain 1) only - Test scenario 3, case study 1.

<b>Service Class</b>	<b>Destination Link utilization with DWFQ algorithm</b>	<b>Destination Link utilization with DRAM-NFV algorithm</b>
<b>SC1</b>	<b>3.379%</b>	<b>2.359%</b>
<b>SC2</b>	<b>15.22%</b>	<b>19.646 %</b>
<b>SC3</b>	<b>0.319%</b>	<b>0.35%</b>

In terms of service classes' average End to End Delays, the use of the DRAM-NFV algorithm increases the average End to End Delay for SC<sub>1</sub> traffic while the average End to End Delays for SC<sub>2</sub> and SC<sub>3</sub> traffic are reduced. Table 6-22 shows the average End to End Delay for all the service class traffic in the upstream and downstream DiffServ domains for the whole period of simulation.



Table 6-22, Average End-to-End Delay when managing the upstream domain (Domain 1) only, test scenario 3 - case study 1.

Service Class	Average End to End Delay using DWFQ algorithm	Average End to End Delay using DRAM-NFV algorithm	Increased/Decreased
SC1	4.196 sec	5.556 sec	Increased by 1.36 sec.
SC2	8.263 sec	7.206 sec	Decreased by 1.057 sec.
SC3	120.388 sec	107.535 sec	Decreased by 12.853 sec.
Overall traffic	7.8 sec	8.25 sec	Increased by 0.45 sec.

On applying the DRAM-NFV algorithm to this scenario, the throughput of the highest priority service class (SC<sub>1</sub>) traffic at the destination link of the downstream domain is decreased and its average End to End Delay increases whilst there is congestion in the SC<sub>2</sub> queues. This does damage the QoS for the highest priority service class traffic – which is highly sensitive to delay and packets drop.

In the second part of this case study, both the upstream domains (Domain 1) and (Domain 3) are managed based on the service class congestion levels at the ingress routers of the downstream domain. The congestion levels in the service class queues tend to be reduced during periods of congestion and increased during periods that when there is no congestion (relative to the situation where the DWFQ and DRAM-NFV algorithms are in use separately), as shown in Figure 6-30, Figure 6-31 and Figure 6-32. It is noticeable that the SC<sub>1</sub>, SC<sub>2</sub> and SC<sub>3</sub> queues suffer from different levels of congestion, thus their scheduling rates at the egress routers of the upstream domains (Domain 1 and Domain 3 of Figure 6-3) are reduced based on the determined values for the scheduling rates update factor ( $\beta$ ).

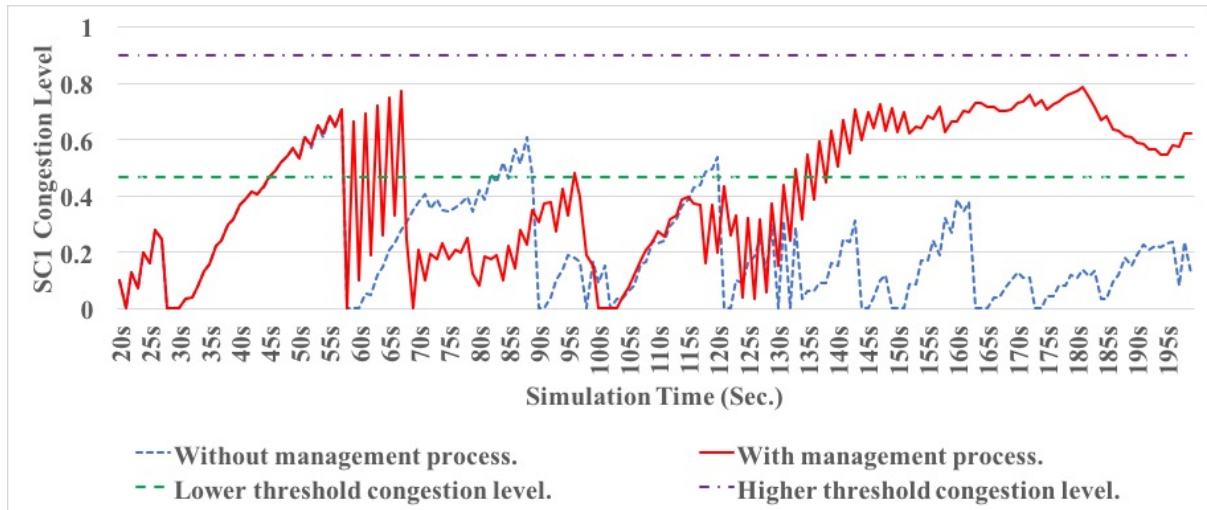


Figure 6-30, SC1 maximum congestion level when using a mix of the DWFQ and DRAM-NFV algorithms to manage the upstream domains - case study 1, test scenario 3.

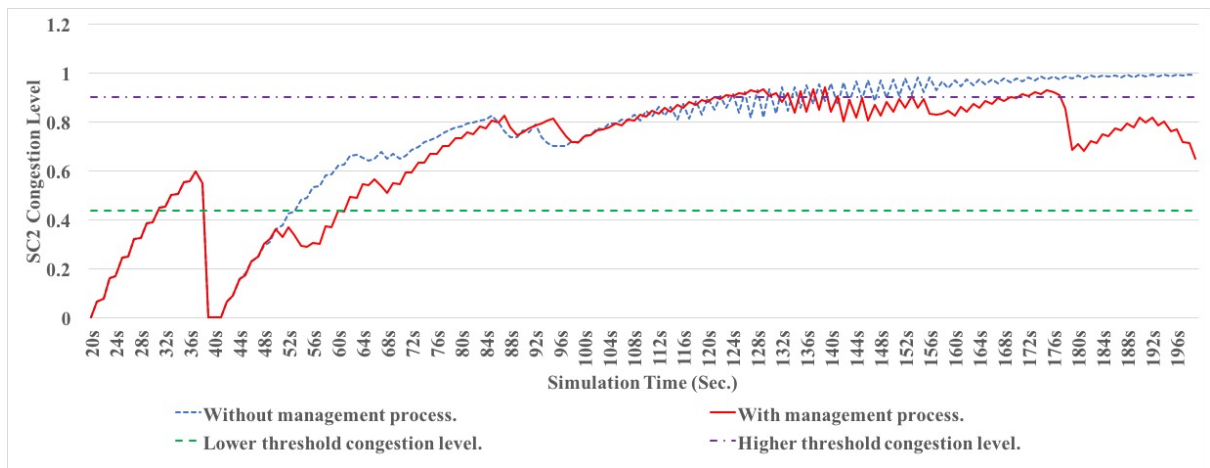


Figure 6-31, SC2 maximum congestion levels when using DWFQ and DRAM-NFV algorithms to manage both upstream domains - case study 1, test scenario 3.

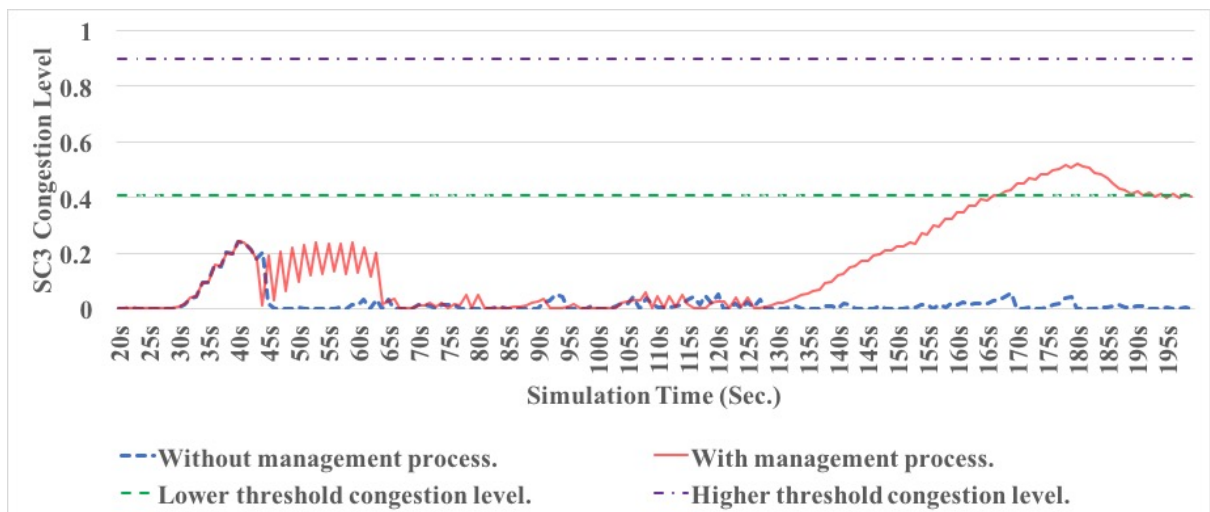


Figure 6-32, SC3 maximum congestion level using DWFQ and DRAM-NFV algorithms with managing both upstream domains - Study case 1, test scenario 3.

The SC<sub>1</sub> traffic utilizations at the (Domain 1- downstream) and (Domain 3- downstream) links are increased while The SC<sub>2</sub> traffic utilizations are reduced. The SC<sub>3</sub> traffic utilizations at these links stay approximately the same. Table 6-23 summarizes the service classes utilizations when both the upstream domain out links are managed by DRAM-NFV.

Table 6-23, Service classes utilizations when managing both the upstream domains out links - case study 1, test scenario 3.

Upstream domain out link (Domain 1- Downstream)	DWFQ Algorithm	DRAM-NFV Algorithm
SC1 Traffic	6.706%	7.632%
SC2 Traffic	44.674%	42.174%
SC3 Traffic	0.47%	0.445%

Upstream domain out link (Domain 3- Downstream)	DWFQ Algorithm	DRAM-NFV Algorithm
SC1 Traffic	7.99%	9.133%
SC2 Traffic	39.146%	36.038%
SC3 Traffic	0.406%	0.411%

The SC<sub>1</sub> traffic utilization at the destination link of the downstream domain is better than its utilization in the case of only one upstream domain (Domain 1) being managed. The SC<sub>2</sub> traffic utilization at that link is also increased but it is less than its utilization in the case where only (Domain 1) is managed. The SC<sub>3</sub> utilization is approximately the same in both cases. Table 6-24 summarizes the service classes' utilizations when managing both the upstream domains out links and when managing a single upstream domain out link (Domain 1).

Table 6-24, service classes utilizations at the destination link when managing both the upstream domains out links and when managing only a single upstream domain out link - case study 1, test scenario 3.

Utilization at the Destination link.	When managing the out link of a single upstream domain (Domain 1)		When managing the out links of both upstream domains (Domain 1 and 3)	
	DWFQ Algorithm	DRAM-NFV algorithm	DWFQ Algorithm	DRAM-NFV algorithm
SC1	3.379%	2.359%	3.379%	3.021%
SC2	15.22%	19.646%	15.22%	16.733%
SC3	0.319%	0.35%	0.319%	0.342%

In terms of service classes' average End to End Delays, the DRAM-NFV algorithm increases the average End to End Delay for the SC<sub>1</sub> traffic but this delay is less than that recorded when only one upstream domain (Domain 1) is managed. The average End to End Delays of the SC<sub>2</sub> and SC<sub>3</sub> traffic are reduced. Table 6-25 summarizes the service classes' average End to End Delays, first, when managing both the upstream domains out links and, second, when managing only a single upstream domain out link.

Table 6-25, service classes average End to End Delays when managing both the upstream domains out links and when managing only a single upstream domain out link - case study 1, test scenario 3.

Average End to End Delay	When managing the out link of both upstream domains (Domain 1 and 3)		When managing the out link of a single upstream domain (Domain 1)	
	DWFQ Algorithm	DRAM-NFV algorithm	DWFQ Algorithm	DRAM-NFV algorithm
SC1	4.196 sec	4.543 sec	4.196 sec	5.556 sec
SC2	8.263 sec	7.654 sec	8.263 sec	7.206 sec
SC3	120.388 sec	105.401 sec	120.388 sec	107.535 sec
Overall traffic	7.808 sec	7.865 sec	7.808 sec	8.251 sec

## 6.8.2 Case Study 2- Test Scenario 3 Analysis:

In case study 2 of test scenario 3 - Figure 6-3, the traffic of the lowest priority service class (SC<sub>3</sub>) is increased such that the number of (SC<sub>3</sub>) traffic sources is set to 180 at both the nodes (n<sub>0</sub>) and (n<sub>1</sub>), representing at the upstream domains - appendix A-5.3- Figure 9-10, while the numbers of SC<sub>1</sub> and SC<sub>2</sub> traffic sources are reduced to 60 sources at both (n<sub>0</sub>) and (n<sub>1</sub>). The maximum congestion level of the queues that belong to a specific service class at the out ports of the ingress routers for the downstream domain is measured, first, using the DWFQ algorithm. Figure 6-33, Figure 6-34 and Figure 6-35 show comparisons of maximum congestion levels in

service class queues when using the DRAM-NFV algorithm as compared to the situation which pertains when using the DWFQ algorithm. When using DWFQ algorithm, it is noticeable that all the classes suffer from congestion, at different levels. The SC<sub>2</sub> and SC<sub>3</sub> queues are considerably more congested than the SC<sub>1</sub> queue, Figure 6-33, Figure 6-34 and Figure 6-35 (dashed curves).

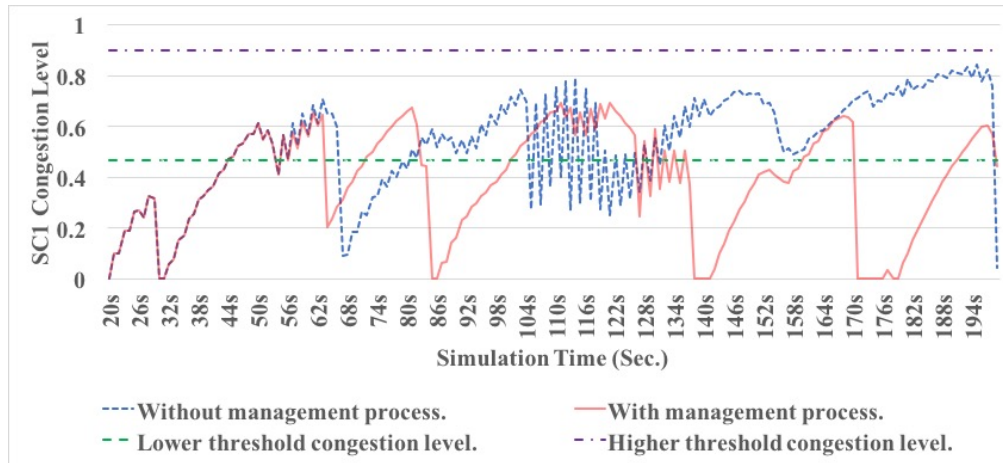


Figure 6-33, SC<sub>1</sub> maximum congestion level using the DWFQ and DRAM-NFV algorithms - case study 2, test scenario 3.

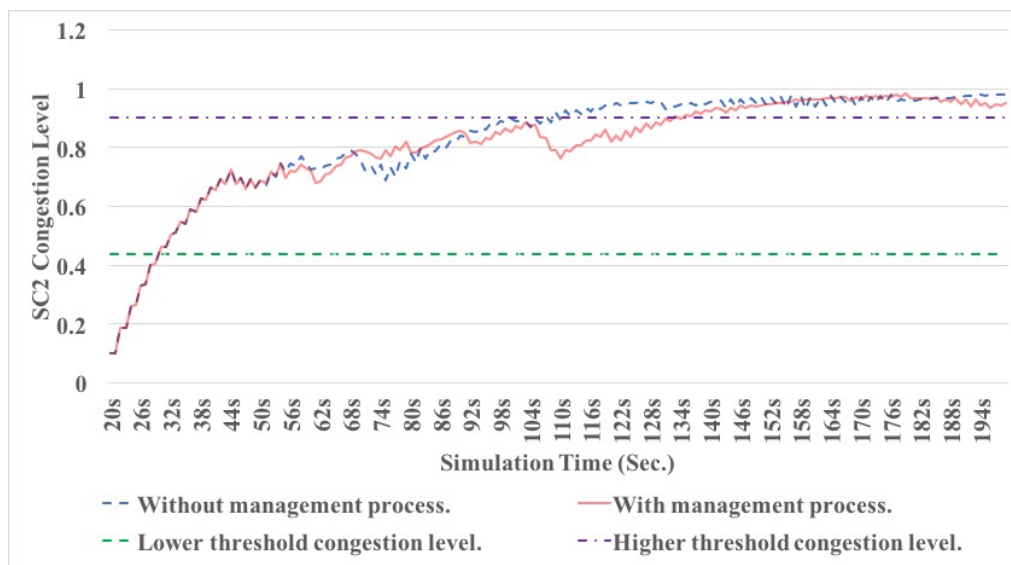


Figure 6-34, SC<sub>2</sub> maximum congestion level using the DWFQ and DRAM-NFV algorithms - case study 2, test scenario 3.

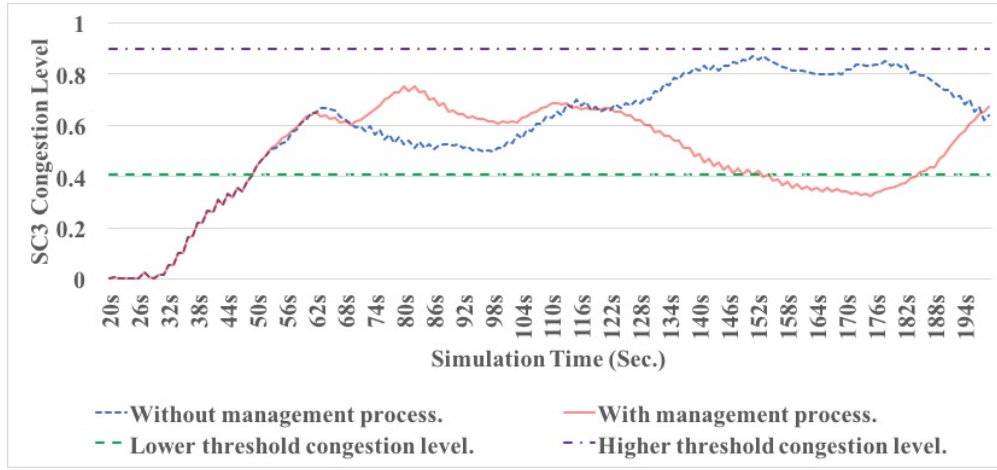


Figure 6-35, SC3 maximum congestion level using the DWFQ and DRAM-NFV algorithms - case study 2, test scenario 3.

When the DRAM-NFV algorithm is applied, the congestion level in the service class queues is reduced, generally, during periods of congestions and increased during periods that do not experience congestion, Figure 6-33, Figure 6-34 and Figure 6-35, as explained for the previous case studies. The congestion level represents the ratio of the average service class queue length to the service class buffer size so reducing the congestion level means reducing the average service class queue length. Thus, the average service class queue delay is also reduced because the average service class queue delay is directly proportion to the average service class queue length.

The utilization of the link that connects the upstream domain (Domain 1) with the downstream domain is also organized via the use of the DRAM-NFV algorithm. This organizational effort is based on the traffic conditions in the ingress routers of the downstream domain and the traffic conditions at the egress router of the upstream domain (Domain 1). The use of DRAM-NFV improves the highest priority service class (SC1) traffic utilization at that link when congestion occurs in the SC2 and SC3 queues at the ingress routers of the downstream domain while the average percentage utilizations for the medium and lowest priorities service class queues (SC<sub>2</sub>) and (SC<sub>3</sub>) respectively, which are considerably more congested than the SC1 queue, are reduced. Table 6-26 summarizes the average percentage utilizations for the service classes traffic at the link that connects the upstream (Domain 1) and downstream domains for the whole period of simulation.

Table 6-26, Average percentage utilization for the service classes at the link that connects the upstream (Domain 1) and downstream DiffServ domains - Test scenario 3, case study 2.

Service Class	Domains Link utilization with DWFQ algorithm	Domains Link utilization with DRAM-NFV algorithm
SC1	4.181%	5.127%
SC2	50.493 %	48%
SC3	0.604%	0.536 %

Comparing the service classes utilizations of the (Domain 1- downstream) link with the service classes utilizations of the (Domain 3 – downstream) link when the DRAM-NFV algorithm is in use, it can be seen that the SC<sub>1</sub> and SC<sub>2</sub> traffic utilizations at the (Domain 1- downstream) link are better than the corresponding traffic utilizations at the (Domain 3 – downstream) link although both upstream domains have the same configurations as regards service class queues and there are the same numbers of service class traffic sources at (n<sub>0</sub>) and (n<sub>1</sub>). Table 6-27 summarizes the average percentage utilizations for the service classes traffic at the links that connect the upstream and downstream domains when managing the upstream (Domain 1) only using the DRAM-NFV algorithm for the whole period of simulation.

Table 6-27, Average percentage utilization for the service classes at the links that connect the upstream and downstream DiffServ domains - Test scenario 3, case study 2.

Upstream (Domain 1) – Downstream Link Utilization			Upstream (Domain 3) – Downstream Link Utilization	
Service Class	with DWFQ algorithm	with DRAM-NFV algorithm	with DWFQ algorithm	(Domain 3) – Downstream Link is managed using DWFQ algorithm while (Domain 1) – Downstream Link is managed using DRAM-NFV algorithm
SC1	4.181%	5.127%	4.476%	4.458%
SC2	50.493%	48%	47.175%	47.468%
SC3	0.604%	0.536%	0.691%	0.528%

The utilization of (SC<sub>1</sub>) traffic at the destination link of the downstream domain is also improved with the use of the DRAM-NFV algorithm while the average utilizations for the (SC<sub>2</sub>) and (SC<sub>3</sub>) traffic are reduced. Table 6-28 illustrates the average percentage utilizations for the service classes traffic at the destination link for the whole period of simulation.



Table 6-28, Average percentage utilization for the service classes at the destination link when mangning the upstream domain (Domain 1) only - Test scenario 3, case study 2.

Service Class	Destination Link utilization with DWFQ algorithm	Destination Link utilization with DRAM-NFV algorithm
SC1	1.277%	1.452%
SC2	23.88 %	23.5%
SC3	0.46%	0.336 %

In terms of service classes' average End to End Delays, the DRAM-NFV algorithm reduces the average End to End Delay for the SC<sub>1</sub> and SC<sub>2</sub> traffic slightly and causes a little extra delay for the SC<sub>3</sub> traffic as explained in Table 6-29 . The throughput and average End to End Delay for the highest priority service class (SC<sub>1</sub>) are improved when the DRAM-NFV algorithm is applied to manage the link between the upstream (Domain 1) and downstream domains and when the SC<sub>2</sub> and SC<sub>3</sub> queues at the ingress routers of the downstream domain are suffering from congestion.

Table 6-29, Average End-to-End Delay when mangning the upstream domain (Domain 1) only, test scenario 3 - case study 2.

Service Class	Average End to End Delay using DWFQ algorithm	Average End to End Delay using DRAM-NFV algorithm	Increased/Decreased
SC1	9.394 sec	9.045 sec	Decreased by 0.35 sec.
SC2	6.743 sec	6.644 sec	Decreased by 0.1 sec.
SC3	102.475 sec	102.977 sec	Increased by 0.5 sec.
Overall traffic	9.7 sec	8.9 sec	Decreased by 0.8 sec.

### 6.8.3 Case Study 3- Test Scenario 3 Analysis:

In case study 3 of test scenario 3 - Figure 6-3, the number of service class traffic sources that are created at the (DiffServ traffic sources) nodes ( $n_0$ ) and ( $n_1$ ) of the simulated test scenario 3 topology diagram, appendix A-5.3 - Figure 9-10, is 90 sources for each (SC<sub>1</sub>, SC<sub>2</sub>, SC<sub>3</sub>). This creates some congestion states in the service class queues at the downstream domain. The maximum congestion level of the queues that belong to a specific service class at the out ports of the ingress routers for the downstream domain is measured using the DWFQ algorithm first. Figure 6-36, Figure 6-37 and Figure 6-38 illustrate comparisons of maximum congestion levels in service class queues when using the DRAM-NFV algorithm as compared to the situation which pertains when using the DWFQ algorithm. When using DWFQ algorithm, it is noticeable that the service classes suffer from different levels of congestion. The SC<sub>2</sub> queue is considerably more congested than the SC<sub>1</sub> and SC<sub>3</sub> queues, Figure 6-36, Figure 6-37 and Figure 6-38 (dashed curves).

When the DRAM-NFV algorithm is applied to manage resources across and within the upstream and downstream domains, the congestion level in the service class queues is reduced throughout the periods of congestion, but increases during the periods that do not experience congestion, as explained in the section 6.8.1.

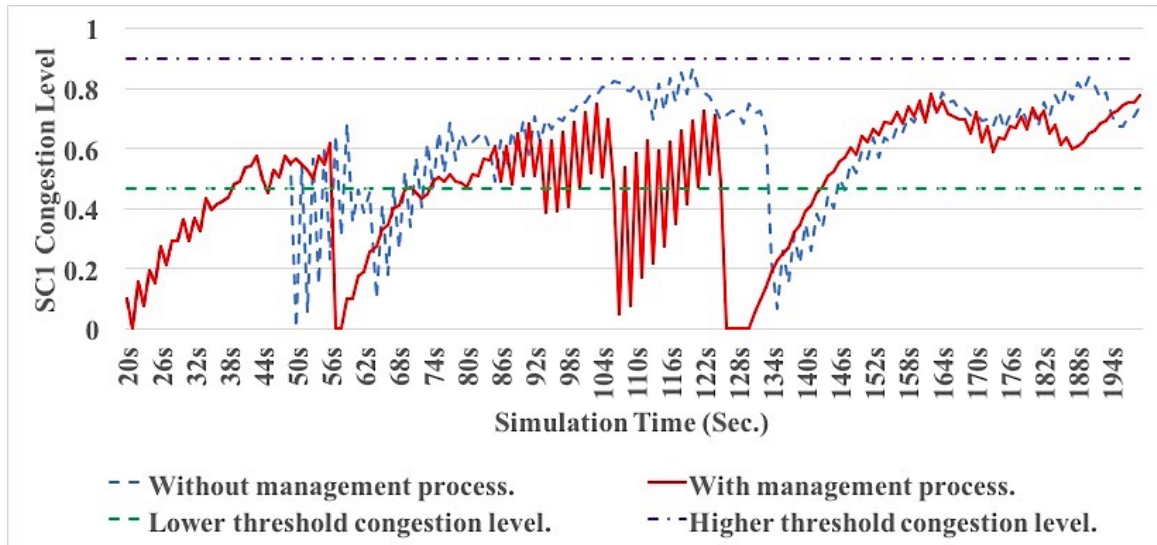


Figure 6-36, SC1 maximum congestion levels for both the DWFQ and the DRAM-NFV algorithms - case study 3, test scenario 3.

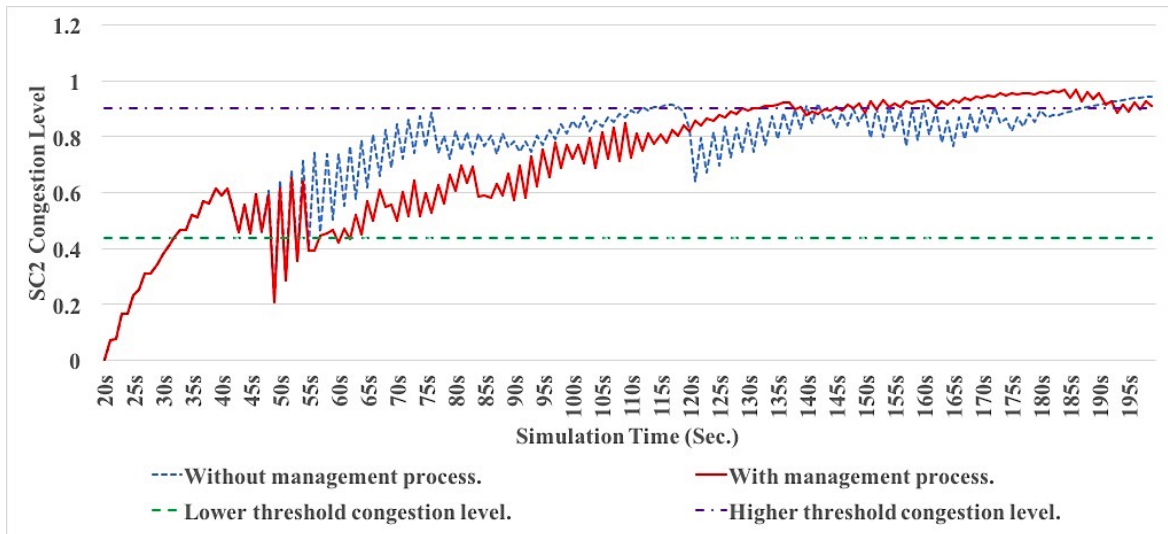


Figure 6-37, SC2 maximum congestion levels for both the DWFQ and the DRAM-NFV algorithms - case study 3, test scenario 3.



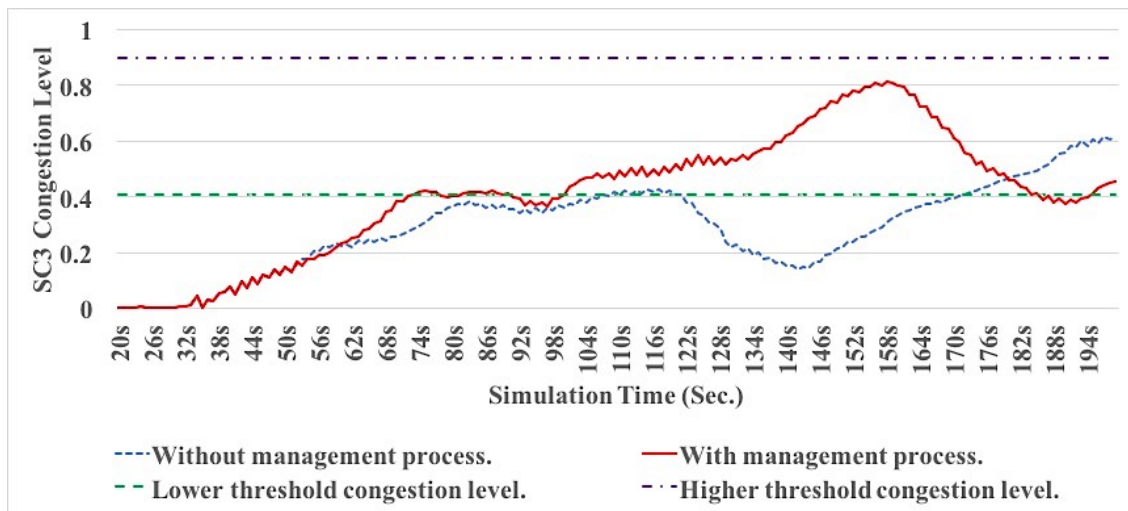


Figure 6-38, SC3 maximum congestion levels for both the DWFQ and the DRAM-NFV algorithms - case study 3, test scenario 3.

The DRAM-NFV algorithm organizes the utilization of the link that connects the upstream domain (Domain 1) with the downstream domain on the basis of the traffic conditions in the ingress routers of the downstream domain and the traffic conditions at the egress router of the upstream domain (Domain 1). It does improve some of the service class traffic utilization at that link when congestion occurs. The average percentage utilization for the highest and the lowest priority service classes (SC<sub>1</sub>) and (SC<sub>3</sub>) at that link are increased while the average percentage utilization for the medium priority service class (SC<sub>2</sub>), which is considerably more congested than the other queues, is reduced. Table 6-30 illustrates the average percentage utilizations for the service classes traffic at the link that connects the upstream (Domain 1) and downstream domains for the whole period of simulation.

Table 6-30, Average percentage utilization for the service classes at the link that connects the upstream (Domain 1) and downstream DiffServ domains - Test scenario 3, case study 3.

Service Class	Domains Link utilization with DWFQ algorithm	Domains Link utilization with DRAM-NFV algorithm
SC1	6.563%	7.281%
SC2	45.141%	42.872%
SC3	0.4237%	0.618%

The upstream domain (Domain 3) in Figure 6-3 is not included in the process of resources management across domains as was said in the beginning of section 6.3.3. The egress router of (Domain 3) allocates its out-port resources for the service classes according to the service classes differentiation pattern and also according to the traffic conditions at its router without taking into consideration the traffic conditions at the ingress routers of the downstream domain. When comparing the service classes utilizations of the (Domain 1 – downstream) link with the service classes utilizations of the (Domain 3 – downstream) link (which uses the DWFQ algorithm only) the SC<sub>1</sub> and SC<sub>2</sub> traffic utilizations at the (Domain 1- downstream) link are better than the traffic utilizations for these service classes at the (Domain 3 – downstream) link although both upstream domains have the same configurations of service class queues and the same quantities of service classes traffic sources at (n<sub>0</sub>) and (n<sub>1</sub>) respectively. Table 6-31 summarizes the average percentage utilizations for the service classes traffic at the links that connect the upstream and downstream domains when managing the upstream (Domain 1) only using the DRAM-NFV algorithm for the whole period of simulation.

Table 6-31, Average percentage utilization for the service classes at the links that connect the upstream and downstream DiffServ domains - Test scenario 3, case study 3.

Upstream (Domain 1) – Downstream Link Utilization			Upstream (Domain 3) – Downstream Link Utilization	
Service Class	with DWFQ algorithm	with DRAM-NFV algorithm	with DWFQ algorithm	(Domain 3) – Downstream Link is managed using DWFQ algorithm while (Domain 1) – Downstream Link is managed using DRAM-NFV algorithm
SC1	6.563%	7.281%	7.861%	6.756%
SC2	45.141%	42.281%	39.229%	41.972%
SC3	0.423%	0.618%	0.431%	0.619%

The utilizations of some of the service class traffic at the destination link of the downstream domain are also improved with the use of the DRAM-NFV algorithm. The utilizations for the SC<sub>1</sub> and SC<sub>3</sub> traffic are increased while the utilization for the SC<sub>2</sub> traffic decreases as illustrated in Table 6-32 .

Table 6-32, Average percentage utilization for the service classes at the destination link when managing the upstream domain (Domain 1) only - Test scenario 3, case study 3.

Service Class	Destination Link utilization with DWFQ algorithm	Destination Link utilization with DRAM-NFV algorithm
SC1	2.02%	2.08%
SC2	20.955%	20.147%
SC3	0.407%	0.693%

In terms of service class average End to End Delays, the DRAM-NFV algorithm reduces the average End to End Delay for the SC<sub>1</sub> and SC<sub>3</sub> traffic and causes a little extra delay for the SC<sub>2</sub> traffic as shown in Table 6-33. The throughputs and average End to End Delays for the highest and lowest priority service classes (SC<sub>1</sub>) and (SC<sub>3</sub>) are improved when congestion occurs in the SC<sub>2</sub> queues at the ingress routers of the downstream domain by applying the DRAM-NFV algorithm.

Table 6-33, Average End-to-End Delay when managing the upstream domain (Domain 1) only, test scenario 3 - case study 3.

Service Class	Average End to End Delay using DWFQ algorithm	Average End to End Delay using DRAM-NFV algorithm	Increased/Decreased
SC1	6.112 sec	6.073 sec	Decreased by 0.04 sec.
SC2	6.895 sec	7.148 sec	Increased by 0.25 sec.
SC3	116.537 sec	94.987 sec	Decreased by 21.55 sec.
Overall traffic	8.734 sec	9.561 sec	Increased by 0.827 sec.

## 6.9 Chapter Summary.

This chapter presents the performance of the DRAM-NFV algorithm in managing resources across the edge routers of DiffServ domains. The DiffServ domain is represented by the simulated queue model application (RDWQueue) - section 5.3 , the traffic model application (TosApp) - section 6.2 and the network topology model – appendix A-5. The behaviour of the Proportional Delay DiffServ domain in term of classification the generated traffic into classes, service class queues configurations, average service class queue delays and service classes weights configurations is discussed in section 6.5. Moreover, the performance of the DRAM-NFV algorithm in managing resources across different DiffServ domains in the event of congestion is also tested and discussed in this chapter. This is done by applying this algorithm in the test scenarios which have been implemented using a simulation environment called “network simulator NS3” and measuring: i) the utilization of the link that connects the DiffServ domains, ii) the utilization of the link that connects the downstream domain to the destination and iii) measuring the average End to End Delay for service classes’ traffic. The management is based on two factors. Firstly, reducing the scheduling rates of the congested service class queues at the egress router of the upstream domain based on the corresponding service class queues congestion levels at the ingress router of the downstream domain. Secondly, reallocating the resources of the link that connects the DiffServ domains based on the updated scheduling rates.

The algorithm is particularly effective when both the DiffServ domains (upstream and downstream) are underutilized but some of the service class queues at the ingress router of the downstream domain are either congested or suffering from various levels of congestion, while

other classes are not congested. Consequently, several case studies are presented for each test scenario; each case study includes increasing the traffic of (a) particular service class(es) at the upstream domain to create congestion states in the service class queues at the ingress routers of the downstream domain.

The results show that with dynamic management of resources across different DiffServ domains, the congestion levels in the service class queues at the ingress router of the downstream domain can be reduced. In addition, the algorithm prevents any congested service class queue at the egress router of the upstream domain from occupying the required bandwidth for its congested traffic and provides extra resources to other, uncongested, classes when such congestion occurs. By reallocating the resources of the link that connects DiffServ domains, the utilization of that link and the link utilization at the destination for some service class queues when congestion is present can be improved. Moreover, the average End to End Delay for some service class queues can also be improved. In contrast, the classes that suffered from congestion will reduce their traffic utilizations at these abovementioned links and increase their average End to End Delays.

# Chapter Seven

## Conclusions and Recommendations

### 7.1 Introduction:

In this research, a new resources allocation algorithm (DRAM-NFV) is proposed to allocate service class resources within proportional delay DiffServ domains. The DRAM-NFV algorithm manages the resources among service classes within the edge routers of the DiffServ domains dynamically according to their traffic conditions and manages these resources across different DiffServ domains, particularly in the event of congestion, based on the corresponding traffic conditions at the egress routers of the upstream domain and the ingress routers of the downstream domains. In this chapter, conclusions concerning this study along with a discussion of possible future research is presented.

### 7.2 Conclusions:

The importance of Quality of Service (QoS) technologies has increased rapidly with the rapid development of multimedia applications over the Internet such as distance learning, Video traffic, the voice over Internet protocol, IP TV and so on. There are different types of QoS models, and Differentiated Services (DiffServ) is one of them. It aims to achieve differentiation among service classes in the network through a sequence of operations such as classification, marking, queuing or dropping and scheduling based on the QoS requirements of the Internet applications. In addition, it has Per Hop Behaviour (PHB), which means that each domain or router manages its resources locally depending on its traffic condition and in a different way from other domains. The problem that we have in DiffServ is that it is static. This means that each domain allocates its resources separately according to domain configuration parameters. Once the domain administrator sets the configuration parameters of a domain, the parameters will not be changed. The traffic condition within a DiffServ domain can be changed continuously and randomly and therefore, there will be a situation where the network favours a certain amount of traffic while other network traffic will suffer despite the priority levels that we have made for service classes in a DiffServ domain so it needs to adjust the parameters at one domain to rebalance the resources at another domain. This research aims to achieve dynamic resource management for resources across and within DiffServ domains through using

the SDN and NFV technologies rather than the current static approach in allocating its resources separately per each domain in order to orchestrate resources between DiffServ domains.

The DiffServ is PHB and uses a static approach to allocating resources separately per each domain such that the domain administrator sets the parameters for service classes in a fixed manner and did not change it. However, it would be better if the DiffServ domains were managing resources dynamically rather than statically because the network operation (traffic condition) within a DiffServ domain could be changed continuously and therefore there would be a situation whereby the network favours a certain amount of network traffic. As a consequence, other network traffic will suffer. Instead of having a static DiffServ, we can monitor traffic condition in one domain and make changes to managing resources of other domains so we can now calibrate between DiffServ domains in managing resources. This calibration can be accomplished by programming remotely and automatically the OpenFlow switches of DiffServ domains, which are SDN devices, and by extracting one of the DiffServ network functions, which is resources management function, and implementing it virtually using the concept of Network Function Virtualisation NFV in order to deploy resources management across DiffServ domains dynamically when the traffic condition in a DiffServ domain is unbalanced; i.e.: when there is traffic congestion in a service class queue of a DiffServ domain. The management will still be local if the traffic condition in a DiffServ domain is balanced or normal.

The intended contribution of this research is to introduce a new scheduling algorithm called “Dynamic Resource Allocation Management - Network Function Virtualization (DRAM-NFV)” to manage the service class resources within and across the DiffServ domains in a dynamic and consolidated manner through using the SDN and NFV technologies. This new algorithm enables the Internet Service Providers (Administrators of DiffServ domains) to manage the resources of their Wide Area Networks (WANs) during the congestion periods in their networks.

The presented algorithm, which is run on the NFV server and uses SDN technology, takes into consideration all the possibilities of congestion occurring in a service class queue, and as we have SDN networks, we can monitor and manage the queues in a DiffServ domain remotely. The network statistics of a DiffServ domain like the average queue delay and average queue length need to be sent to the NFV server. After that, the NFV server calculates the congestion level in each service class queue at the ingress router of the downstream domain. If there is no congestion, then the DiffServ domain controller sets the service class weights and these weights are set based on the domain administrator configuration. If there is congestion in one of the

service classes, then NFV server calculates the scheduling rate update factor  $\beta$  and updates the scheduling rate of the equivalent service class at the egress router of the upstream domain. The condition that I decide if the service class queue is congested or not depends on the depth or size of the service class queue. The parameters that we are using in managing resources between DiffServ domains are the threshold values of service class queues, which are used to define the lower and higher threshold congestion levels. Average service class queue size is used to measure the congestion level  $\alpha$  in a service class queue as a ratio of average queue size to service class buffer size and the scheduling rate update factor  $\beta$ . The job of the NFV server is to decide how much to reduce the service class scheduling rate between DiffServ domains. If the calculated congestion level is less than the low threshold value, then there is no need to reduce the scheduling rate of the equivalent service class at the egress router of the upstream domain; its rate is set based on the domain administrator configuration. If it is between the low and high threshold values then the scheduling rate is reduced by  $\beta$ . The relation between  $\beta$  and congestion level is linear such that the value of  $\beta$  is directly proportional to the value of the congestion level. If the service class is very congested, more so than the high threshold value, then  $\beta$  is set to the maximum value in order to dampen the service class throughput at the egress router of the upstream domain. A number of test scenarios are presented in this research in order to test the performance of DRAM-NFV algorithm. These scenarios simulate real world networks. The Network Simulator NS3 is used to simulate these scenarios. In order to evaluate the DRAM-NFV algorithm, the profile of VoIP, video traffic, FTP and Database request messages are considered when generating traffic in NS3. The algorithm is particularly effective when the DiffServ domains are underutilised but some of the service class queues at the ingress router of the downstream domain are either congested or suffering from different levels of congestion while other classes are not congested. The performance of the DRAM-NFV algorithm in managing resources across DiffServ domains is compared with the performance of the DWFQ algorithm, which manages resources within the edge routers of a DiffServ domain but cannot manage resources across DiffServ domains through looking at the utilization of the link that connects DiffServ domains, the utilization of the link that connects the downstream domain to the destination and the average End to End Delay for service classes traffic.

Managing resources dynamically across DiffServ domains instead of the current static approach to managing resources per each domain separately achieves better balance for DiffServ domains resources through monitoring the bandwidth hungry service class at one domain and managing its resources at another domain. As a consequence of this, the utilizations

of some service classes in the simulated test scenarios are improved. The second achievement when managing resources across DiffServ domains is that the average End to End Delay for overall traffic in the simulated test scenarios are reduced.

In test scenario 1- Case Study 1, the medium priority service class traffic SC2 is increased at the upstream domain. As a result, the SC1 and SC2 queues at ingress router of the downstream domain suffer from different levels of congestion. When managing resources dynamically using DRAM-NFV algorithm between the upstream and downstream domains, the average utilization for the SC2 and SC3 for the whole period of simulation at the destination end are increased by 0.23% and 0.035% respectively. The average End to End Delay for overall traffic is reduced by 700 msec.

In test scenario 2- Case Study 1, the lowest priority service class traffic SC3 is increased at the upstream domain. As a result, all queues at the ingress router of the downstream domain suffer from different levels of congestion. When managing resources dynamically using DRAM-NFV algorithm between the upstream and downstream domains, the average utilizations for the SC1 and SC2 for the whole period of simulation at the destination end are increased by 0.1% and 0.02% respectively. The average End to End Delay for overall traffic is also reduced by 700 msec.

While in test scenario 3- Case Study 2, the lowest priority service class traffic SC3 is increased at the upstream domain. As a result, the SC2 and SC3 queues at ingress router of the downstream domain are more congested than SC1 queue. When managing resources dynamically using DRAM-NFV algorithm between the upstream and downstream domains, the average utilization for the SC1 for the whole period of simulation at the destination end is increased by 0.175% and the average End to End Delay for overall traffic is also reduced by 800 msec.

As a result of reducing the average End to End Delay for overall traffic and improving the utilizations of service classes traffic, the QoS of applications traffic can be improved by dynamically managing the resources across DiffServ domains.

There are some limitations that were not taken into consideration when implementing a prototype of the DRAM-NFV algorithm using Network Simulator, NS3, and these will now be mentioned. This study did not consider the latency times involved in sending and receiving the management information to and from the Cloud infrastructure. Managing the resources across DiffServ domains can cause a few milliseconds delay because of sending the information related to traffic state from OpenFlow switches to NFV server for processing and sending back the configuration information to configure the queues of OpenFlow switches by the domain



controller. However, this delay can be trivial if the NFV server virtual resources, which are allocated for the SDN controllers, are enough for processing this information and configuring the queues quickly. The other limitation is that the marking strategies in all the simulated DiffServ domains are made to be identical, for simplicity. Considering different marking schemes within the DiffServ domains would increase the complexity of the Network Simulator NS3 programming and the analysis.

Throughout this thesis, we have made some recommendations for aspects of the research that would benefit from further attention and study. For instance, we could take the effect of a different parameter such as packet dropping in the equation of managing resources across DiffServ domains in order to improve the dynamic resource management process across DiffServ domains. The second recommendation is that the concept of this algorithm could be applied to other QoS models such as IntServ model or provide IntServ over DiffServ model. IntServ model needs to configure every router along a path and across domains from source to destination in order to reserve resources per each flow.

### **7.3 Review of Objectives:**

During this research, several objectives have been achieved. These objectives are as follows:

1. The identification of the research problem relating to a common difficulty encountered by proportional delay DiffServ domains. Section 1.3 of this thesis described the research problem.
2. The reviewing of the algorithms that are used in allocating the resources within the edge routers of a proportional delay DiffServ domain, explaining the use of SDN to achieve better QoS, and the use of NFV. These topics were discussed in sections 3.2 and 3.3 respectively.
3. The presenting of a new algorithm for the dynamic and consolidated management of the resources within and across proportional delay DiffServ domains when traffic congestion occurs. This algorithm is called “Dynamic Resource Allocation Management – Network Function Virtualization (DRAM-NFV)”. The principle of the DRAM-NFV algorithm and the conditions which enable the DRAM-NFV algorithm to manage resources across DiffServ domains are identified in section 4.2 of this thesis.
4. The presenting of a mathematical model for the DRAM-NFV algorithm as illustrated in section 4.4 of this thesis.

5. The explaining of the simulated queueing model which is used to implement the proportional delay DiffServ and DRAM-NFV algorithm as illustrated in section 5.3 of this thesis.
6. The clarifying of the experimental design procedures which were used when evaluating and validating the DRAM-NFV algorithm. Section 6.3 of this thesis presents the test scenarios, and their case studies, which were used for this evaluation.
7. The discussion of the simulation results of the test scenarios presented, as exhibited in sections 6.6, 6.7 and 6.8.

#### **7.4 Chapter Summary:**

In this chapter, a comprehensive and precise overview of the research undertaken for this thesis has been presented. In addition, some specific issues have been listed in this chapter which deserves further research. This further research would complete this study and develop a cutting-edge infrastructure for the future Internet.

## 8. References

1. Al-Quzweeni, A. et al., 2016. A framework for energy efficient NFV in 5G networks. In *2016 18th International Conference on Transparent Optical Networks (ICTON)*. IEEE, pp. 1–4. Available at: <http://ieeexplore.ieee.org/document/7550698/>.
2. Alipio, M.I. et al., 2016. Demonstration of Quality of Service mechanism in an OpenFlow testbed. In *2016 IEEE Advanced Information Management, Communicates, Electronic and Automation Control Conference (IMCEC)*. IEEE, pp. 443–447. Available at: <http://ieeexplore.ieee.org/document/7867251/>.
3. An, N. et al., 2016. Dynamic priority-adjustment for real-time flows in software-defined networks. In *2016 17th International Telecommunications Network Strategy and Planning Symposium (Networks)*. IEEE, pp. 144–149. Available at: <http://ieeexplore.ieee.org/document/7751167/>.
4. Armbrust, M. et al., 2009. “Above the Clouds : A Berkeley View of Cloud Computing”, technical report, University of California,
5. Ashon, S.A. & Ilyas, M., 2011. *Cloud Computing and Software Services Theory and Techniques*,
6. Batalle, J. et al., 2013. On the Implementation of NFV over an OpenFlow Infrastructure: Routing Function Virtualization. In *2013 IEEE SDN for Future Networks and Services (SDN4FNS)*. IEEE, pp. 1–6. Available at: [http://ieeexplore.ieee.org/xpls/abs\\_all.jsp?arnumber=6702546](http://ieeexplore.ieee.org/xpls/abs_all.jsp?arnumber=6702546) [Accessed June 5, 2014].
7. Bennett, J.C.R. & Zhang, H., 1997. Hierarchical packet fair queueing algorithms. *IEEE/ACM Transactions on Networking*, 5(5), pp.675–689.
8. Blake, S. et al., 1998. *An architecture for differentiated services*, Available at: <http://scholar.google.com/scholar?hl=en&btnG=Search&q=intitle:No+Title#0> [Accessed July 2, 2014].
9. Bodin, U. & Scheln, O., 2001. Drop Strategies and Loss-rate Differentiation. , pp.146–154.
10. Buyya, R., Vecchiola, C. & Selvi, S.T., 2013. *Mastering Cloud Computing Foundations and Applications Programming*, Elsevier.
11. Carpenter, B. & Faucheur, F. Le, 2000. “Per Hop Behavior Identification Codes”, RFC 2836,
12. Chen, N. et al., 2015. Self-Organizing Scheme Based on NFV and SDN Architecture for Future Heterogeneous Networks. *Mobile Networks and Applications*, 20(4), pp.466–472. Available at: <http://link.springer.com/10.1007/s11036-015-0630-3>.
13. Chin-Chang Li et al., 2000. Proportional delay differentiation service based on weighted fair queuing. In *Proceedings Ninth International Conference on Computer Communications and Networks (Cat.No.00EX440)*. IEEE, pp. 418–423. Available at: <http://ieeexplore.ieee.org/lpdocs/epic03/wrapper.htm?arnumber=885523>.
14. Chiosi, M. et al., 2012. *Network Functions Virtualisation - Introductory White Paper*,
15. Chowdhury, N.M.M.K. & Boutaba, R., 2009. Network virtualization: state of the art and research challenges. *IEEE Communications Magazine*, 47(7), pp.20–26. Available at: <http://ieeexplore.ieee.org/document/5183468/>.
16. Cisco, 2013. WRED and Queue Limit. Available at: <http://www.cisco.com/c/en/us/td/docs/routers/10000/10008/configuration/guides/qos/qoscf/10queue.html#wp1041848>.
17. Cisco Systems, I., 2006. *IPv6 Extension Headers Review and Considerations.*, Available at:

- [http://www.cisco.com/en/US/technologies/tk648/tk872/technologies\\_white\\_paper0900aecd8054d37d.html](http://www.cisco.com/en/US/technologies/tk648/tk872/technologies_white_paper0900aecd8054d37d.html).
18. Clark, D.D. & Fang, W., 1998. Explicit allocation of best-effort packet delivery service. *IEEE/ACM Transactions on Networking*, 6(4), pp.362–373. Available at: <http://ieeexplore.ieee.org/lpdocs/epic03/wrapper.htm?arnumber=720870> [Accessed June 25, 2014].
  19. Demers, a., Keshav, S. & Shenker, S., 1989. Analysis and simulation of a fair queueing algorithm. *ACM SIGCOMM Computer Communication Review*, 19(4), pp.1–12.
  20. Dong Xu, 2010. Cloud Computing: An emerging technology. In *2010 International Conference On Computer Design and Applications*. IEEE, pp. V1-100-V1-104. Available at: <http://ieeexplore.ieee.org/lpdocs/epic03/wrapper.htm?arnumber=5541105> [Accessed July 6, 2014].
  21. Dovrolis, C. & Ramanathan, P., 1999. A case for relative differentiated services and the proportional differentiation model. *IEEE Network*, 13(5), pp.26–34. Available at: <http://ieeexplore.ieee.org/lpdocs/epic03/wrapper.htm?arnumber=793688>.
  22. Dovrolis, C., Stiliadis, D. & Ramanathan, P., 2002. Proportional differentiated services: Delay differentiation and packet scheduling. *IEEE/ACM Transactions on Networking*, 10(1), pp.12–26. Available at: <http://ieeexplore.ieee.org/lpdocs/epic03/wrapper.htm?arnumber=986503>.
  23. Dovrolis, C., Stiliadis, D. & Ramanathan, P., 1999. Proportional differentiated services: Delay Differentiation and Packet scheduling. *ACM SIGCOMM Computer Communication Review*, 29(4), pp.109–120. Available at: <http://portal.acm.org/citation.cfm?doid=316194.316211>.
  24. Dovrolis, K., 2000. *Proportional differentiated services for the Internet*.
  25. Duan, Q., Yan, Y. & Vasilakos, A. V., 2012. A survey on service-oriented network virtualization toward convergence of networking and cloud computing. *IEEE Transactions on Network and Service Management*.
  26. ETSI, ETSI NFV Technology. *ETSI*. Available at: [www.etsi.org/technologies-clusters/technologies/nfv](http://www.etsi.org/technologies-clusters/technologies/nfv).
  27. Floodlight Project, 2013. Floodlight Project. Available at: <http://www.projectfloodlight.org/floodlight/>.
  28. Flowgrammable Research Team, 2013. Message layer in OpenFlow. Available at: [http://flowgrammable.org/sdn/openflow/message-layer/#tab\\_ofp\\_1\\_4](http://flowgrammable.org/sdn/openflow/message-layer/#tab_ofp_1_4).
  29. Floyd, S. & Jacobson, V., 1995. Link-sharing and resource management models for packet networks. *IEEE/ACM Transactions on Networking*, 3(4).
  30. Foster, I. et al., 2008. Cloud Computing and Grid Computing 360-degree compared. In *Grid Computing Environments Workshop, GCE 2008*.
  31. Giordano, S. et al., 2003. Advances QoS provisioning in IP networks: The European premium IP projects. *IEEE Communications Magazine*, 41(1), pp.30–36.
  32. Guck, J.W. & Kellerer, W., 2014. Achieving end-to-end real-time Quality of Service with Software Defined Networking. In *2014 IEEE 3rd International Conference on Cloud Networking, CloudNet 2014*.
  33. Han, B. et al., 2015. Network function virtualization: Challenges and opportunities for innovations. *IEEE Communications Magazine*, 53(2), pp.90–97.
  34. Han, Y. et al., 2016. An intent-based network virtualization platform for SDN. In *2016 12th International Conference on Network and Service Management (CNSM)*. IEEE, pp. 353–358. Available at: <http://ieeexplore.ieee.org/document/7818446/>.
  35. Hawilo, H. et al., 2014. NFV: state of the art, challenges, and implementation in next generation mobile networks (vEPC). *IEEE Network*, 28(6), pp.18–26. Available at: <http://ieeexplore.ieee.org/lpdocs/epic03/wrapper.htm?arnumber=6963800>.

36. Heinanen, J. et al., 1999. "Assured Forwarding PHB Group", RFC 2597,
37. Hill, R. et al., 2013. *Guide to cloud computing: principles and practice*, Springer.
38. Hu, C., Wang, Q. & Dai, X., 2015. SDN over IP: Enabling Internet to Provide Better QoS Guarantee. In *Proceedings - 2015 9th International Conference on Frontier of Computer Science and Technology, FCST 2015*.
39. IETF, 2013. RFC7047,
40. J. Babiarz, K. Chan, F. Baker, Nortel Networks, C.S., 2006. RFC 4594 - *Configuration Guidelines for DiffServ Service Classes*,
41. J. Virtamo, 2005. Queuing theory - priority queues. In *Queuing Theory*. Available at: [https://www.netlab.tkk.fi/opetus/s383143/kalvot/E\\_priority.pdf](https://www.netlab.tkk.fi/opetus/s383143/kalvot/E_priority.pdf).
42. Jamjoom, H., Williams, D. & Sharma, U., 2014. Don't call them middleboxes, call them middlepipes. In *Proceedings of the third workshop on Hot topics in software defined networking - HotSDN '14*. New York, New York, USA: ACM Press, pp. 19–24. Available at: <http://dl.acm.org/citation.cfm?doid=2620728.2620760>.
43. Karakus, M. & Durrezi, A., 2017. Quality of Service (QoS) in Software Defined Networking (SDN): A survey. Available at: [www.elsevier.com/locate/jnca](http://www.elsevier.com/locate/jnca).
44. Karaman, M.A. et al., 2015. Quality of service control and resource prioritization with Software Defined Networking. In *1st IEEE Conference on Network Softwarization: Software-Defined Infrastructures for Networks, Clouds, IoT and Services, NETSOFT 2015*.
45. Kate Greene, 2009. MIT Tech Review 10 Breakthrough Technologies: Software-defined Networking. Available at: <http://www2.technologyreview.com/>.
46. Kim, H. & Feamster, N., 2013. Improving network management with software defined networking. *IEEE Communications Magazine*, 51(2), pp.114–119. Available at: <http://ieeexplore.ieee.org/lpdocs/epic03/wrapper.htm?arnumber=6461195>.
47. Kreutz, D. et al., 2015. Software-Defined Networking: A Comprehensive Survey. *Proceedings of the IEEE*, 103(1), pp.14–76. Available at: <http://ieeexplore.ieee.org/lpdocs/epic03/wrapper.htm?arnumber=6994333>.
48. Kun I. Park, 2005. *QoS in Packet Networks*, Springer.
49. Kurose, J.F. & Ross, K.W., 2013. *COMPUTER NETWORKING A Top-Down Approach Sixth.*, PEARSON.
50. Kusmirek, E. et al., 2002. An integrated network resource and QoS management framework. In *IEEE Workshop on IP Operations and Management*. IEEE, pp. 68–72. Available at: <http://ieeexplore.ieee.org/document/1045758/>.
51. Language, C. programming, 2016. C++ online learning programming language. Available at: <http://www.cplusplus.com>.
52. Lee, C., Shin, S. & Chung, J.-M., 2016. Enhanced LTE handover scheme using NFV for LTE handover delay reduction. In *2016 IEEE International Conference on Consumer Electronics-Asia (ICCE-Asia)*.
53. Lin, P. et al., 2015. A west-east bridge based SDN inter-domain testbed. *IEEE Communications Magazine*, 53(2), pp.190–197.
54. Luo, S. et al., 2016. Improving Energy Efficiency in Industrial Wireless Sensor Networks Using SDN and NFV. In *2016 IEEE 83rd Vehicular Technology Conference (VTC Spring)*. IEEE, pp. 1–5. Available at: <http://ieeexplore.ieee.org/document/7504281/>.
55. M.P.V. Manthena, 2015. *Network-as-a-Service Architecture with SDN and NFV*.
56. McKeown, N. et al., 2008. OpenFlow. *ACM SIGCOMM Computer Communication Review*, 38(2), p.69. Available at: <http://doi.acm.org/10.1145/1355734.1355746%5Cnhttp://portal.acm.org/citation.cfm?id=1355746>.
57. Mell, P. & Grance, T., 2011. The NIST Definition of Cloud Computing

- Recommendations of the National Institute of Standards and Technology. *Nist Special Publication*, 145, p.7. Available at: <http://www.mendeley.com/research/the-nist-definition-about-cloud-computing/>.
58. Moret, Yan, and S.F., 1998. A proportional queue control mechanism to provide differentiated services. In *Proceedings of the International Symposium on Computer and Information Systems (ISCIS)*.
  59. Nichols, H. et al., 1998. "Definition of the Differentiated Services Field (DiffServ Field) in the IPv4 and IPv6 Headers", RFC 2474,
  60. ns-2 lists contributors., 1995. The Network Simulator - ns-2. Available at: <http://www.isi.edu/nsnam/ns/>.
  61. NS3 Doxygen, 2015. ns-3 Direct Code Execution Documentation (Doxygen). Available at: <https://www.nsnam.org/docs/release/3.23/doxygen/index.html>.
  62. NS3 Manual, 2015. ns-3 Manual.
  63. NS3 Model library, 2015. ns-3 Model Library.
  64. ns3 project, 2010. Procedure of adding new module to ns3 packetge. Available at: <https://www.nsnam.org/docs/manual/html/new-modules.html>.
  65. NS3 software users group., NS3 Users Forum. Available at: <https://groups.google.com/forum/#!forum/ns-3-users>.
  66. NS3 Tutorial, 2015. ns-3 Tutorial.
  67. ONOS Project, 2015. Open Source Network Operating System. Available at: <http://onosproject.org/>.
  68. Open Networking Foundation (ONF), 2014a. *of-config-1.2 OpenFlow Management and Configuration Protocol*, Available at: <https://www.opennetworking.org/images/stories/downloads/sdn-resources/onf-specifications/openflow-config/of-config-1.2.pdf>.
  69. Open Networking Foundation (ONF), 2011. *OpenFlow Switch Specification version 1.2*,
  70. Open Networking Foundation (ONF), 2012. *OpenFlow Switch Specification version 1.3*,
  71. Open Networking Foundation (ONF), 2013. *OpenFlow Switch Specification version 1.4*,
  72. Open Networking Foundation (ONF), 2014b. *SDN architecture*, Available at: [https://www.opennetworking.org/images/stories/downloads/sdn-resources/technical-reports/TR\\_SDN\\_ARCH\\_1.0\\_06062014.pdf](https://www.opennetworking.org/images/stories/downloads/sdn-resources/technical-reports/TR_SDN_ARCH_1.0_06062014.pdf).
  73. Open Networking Foundation (ONF), 2014. *OpenFlow Switch Specification Version 1.5*,
  74. OpenDaylight Project, 2013. OpenDaylight Project. *OpenDaylight Project*. Available at: <https://www.opendaylight.org>.
  75. Palma, D. et al., 2014. The QueuePusher: Enabling queue management in OpenFlow. In *Proceedings - 2014 3rd European Workshop on Software-Defined Networks, EWSDN 2014*.
  76. Panza, G., Grazioli, M. & Sidoti, F., 2006. Design and analysis of a dynamic Weighted Fair Queuing ( WFQ ) scheduler. *IST Mobile and Wireless Communication Summit '05*.
  77. Pfaff Ben, Bob Lantz, B.H., 2011. *OpenFlow Switch Specification version 1.1*, Available at: <http://archive.openflow.org/documents/openflow-spec-v1.1.0.pdf>.
  78. Pfaff Ben, Brandon Heller, D.T., 2009. *OpenFlow Switch Specification version 1.0*, Available at: <http://archive.openflow.org/documents/openflow-spec-v1.0.0.pdf>.
  79. Raghavan, B. et al., 2012. Software-defined internet architecture. In *Proceedings of the 11th ACM Workshop on Hot Topics in Networks - HotNets-XI*. New York, New York, USA: ACM Press, pp. 43–48. Available at: <http://dl.acm.org/citation.cfm?id=2390239>.
  80. Rao, S.K.N., 2014. *SDN AND ITS USE - CASES - NV AND NFV A State-of-the-Art Survey*,
  81. Reichmeyer, F. & Networks, N., 1998. *A Two-Tier Resource Management Model for Differentiated Services Networks I Introduction : a High-Level Model of QoS Control*,

82. Shaikh, F.A. et al., 2002. End-to-end testing of IP QoS mechanisms. *Computer*, 35(5), pp.80–87. Available at: <http://ieeexplore.ieee.org/lpdocs/epic03/wrapper.htm?arnumber=999779>.
83. Sharma, R., Kumar, N. & Talabattula, S., 2014. Performance of new dynamic benefit-weighted scheduling scheme in DiffServ networks. In *2014 International Conference on Advances in Computing, Communications and Informatics (ICACCI)*. IEEE, pp. 2578–2583. Available at: <http://ieeexplore.ieee.org/lpdocs/epic03/wrapper.htm?arnumber=6968380>.
84. Soares, J. et al., 2014. Cloud4NFV: A platform for Virtual Network Functions. In *2014 IEEE 3rd International Conference on Cloud Networking (CloudNet)*. IEEE, pp. 288–293. Available at: <http://ieeexplore.ieee.org/lpdocs/epic03/wrapper.htm?arnumber=6969010>.
85. Srinivasan, V., Suri, S. & Varghese, G., 1999. Packet classification using tuple space search. *ACM SIGCOMM Computer Communication Review*, 29(4), pp.135–146. Available at: <http://portal.acm.org/citation.cfm?doid=316194.316216>.
86. Suter, B. et al., 1999. Design considerations for supporting TCP with per-flow queueing. In *Proceedings. IEEE INFOCOM '98, the Conference on Computer Communications. Seventeenth Annual Joint Conference of the IEEE Computer and Communications Societies. Gateway to the 21st Century (Cat. No.98CH36169)*. IEEE, pp. 299–306. Available at: <http://ieeexplore.ieee.org/lpdocs/epic03/wrapper.htm?arnumber=659666>.
87. Tanenbaum, A.S. & Wetherall, D.J., 2011. *Computer Networks Fifth.*, Pearson. Available at: <http://scholar.google.com/scholar?hl=en&btnG=Search&q=intitle:No+Title#0>.
88. Theophilus Benson, A.A. and D.M., 2009. *Unraveling the Complexity of Network Management*, Berkeley,CA,USA. Available at: [https://www.usenix.org/legacy/events/nsdi09/tech/full\\_papers/benson/benson\\_html/](https://www.usenix.org/legacy/events/nsdi09/tech/full_papers/benson/benson_html/).
89. Tim Szigeti, and C.H., 2005. *End-to-End QoS Network Design: Quality of Service in LANs, WANs, and VPNs.*, Cisco Press.
90. Urgun, Y. & Kavak, A., 2016. SDN/NFV-based QoS mechanism for Cellular Core Networks. In *2016 24th Telecommunications Forum (TELFOR)*. IEEE, pp. 1–4. Available at: <http://ieeexplore.ieee.org/document/7818733/>.
91. Vaughan-Nichols, S.J., 2011. TECHNOLOGY NEWS OpenFlow: The Next Generation of the Network?
92. Volvach, I. & Globa, L., 2016. Mobile networks disaster recovery using SDN-NFV. In *2016 International Conference Radio Electronics & Info Communications (UkrMiCo)*. IEEE, pp. 1–3. Available at: <http://ieeexplore.ieee.org/document/7739648/>.
93. Wallner, R. & Cannistra, R., 2013. An SDN Approach: Quality of Service using Big Switch's Floodlight Open-source Controller. *Proceedings of the Asia-Pacific Advanced Network*, 35, pp.14–19. Available at: <http://dx.doi.org/10.7125/APAN.35.2>.
94. Wang, H.W.H., Shen, C.S.C. & Shin, K.G., 2001. Adaptive-weighted packet scheduling for premium service. *ICC 2001. IEEE International Conference on Communications. Conference Record (Cat. No.01CH37240)*, 6, pp.1846–1850.
95. WANG, Z., 2001. *Internet QoS Architectures and Mechanisms for Quality of Services.*, Elsevier.
96. Wendell Odom, and M.J.C., 2005. *Cisco QoS Exam Certification Guide*, Cisco Press.
97. Wibowo, F.X.A. et al., 2017. Multi-domain Software Defined Networking: Research status and challenges. Available at: [www.elsevier.com/locate/jnca](http://www.elsevier.com/locate/jnca).

## 9. Appendixes

### A-1 Research Problem Experiment:

In order to present the research problem, an experiment has been carried out using NS3 simulator using the scenario shown in Figure 9-1. In this experiment, we call the upstream (in terms of direction of traffic) domain, Domain 1, and the downstream domain, Domain 2. Both are relative DiffServ domains. The marking strategy for both domains is assumed to be identical for simplicity; both provide three types of service classes according to their priorities from the highest to lowest priorities: SC<sub>1</sub>, SC<sub>2</sub> and SC<sub>3</sub> such that SC<sub>1</sub> is dedicated for the VoIP traffic, SC<sub>2</sub> is dedicated for video traffic and SC<sub>3</sub> is dedicated for FTP traffic. The core router of the downstream domain connects with multiple LANs and these networks are injected with best effort traffic in order to modify the network traffic conditions of the downstream domain. In this simulation, best effort traffic can be inserted into any defined service class queue at the downstream domain, depending on the available buffer resources at these queues. All the queues which belong to a specific service class in both the upstream and downstream DiffServ domains in this experiment are assumed to have the same queue configuration in terms of queue buffer size, threshold parameters and drop probabilities for the packet drop algorithm (WRED) as shown in Table 9-1. The buffer sizes of these abovementioned service class queues can be expressed in terms of power of 2 numbers of packets: i.e., (64, 128, 256, 512) packets (Wendell Odom 2005) so the buffer sizes of these classes are configured to 128 packets in both domains and the WRED configuration of the Cisco<sup>TM</sup> 10000 series QoS routers (Cisco 2013) is used in this experiment to configure the threshold values and drop probabilities for these service classes. Table 9-2 illustrates the configuration parameters of the research problem experiment.

Table 9-1, Explanation of research problem - service classes configuration parameters.

	Buffer Size (in term of number of packets)	Minimum Threshold (times the queue size)	Maximum Threshold (times the queue size)	Drop Probability
Highest Priority (SC <sub>1</sub> )	128	15/32	½	1/10
Medium Priority (SC <sub>2</sub> )	128	7/16	½	1/10
Lowest Priority (SC <sub>3</sub> )	128	13/32	½	1/10

<sup>6</sup> Cisco<sup>TM</sup> is a trade mark of Cisco Systems, Inc. and its affiliates.



Table 9-2, Configuration parameters of the research problem experiment.

Parameter	Configuration value	Notes
Link capacities of the upstream domain	2 Mbits	To ensure that more DiffServ traffic is injected into the downstream domain from the upstream domain.
Link capacity that connects the upstream and downstream domains.	4 Mbits	
link capacities of the downstream domain	2 Mbits	
Link capacity that connects the downstream domain and destination.	4 Mbits	
Both the upstream and downstream domains provide three service class queues	(SC <sub>1</sub> , SC <sub>2</sub> , SC <sub>3</sub> )	
The delay configuration parameters ( $\delta$ ) for service classes (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) at the edge DiffServ routers.	(1:2:4)	(Chin-Chang Li et al. 2000).
The service class markings in the upstream and downstream domains.	Identical.	
Service class queue buffer size in the upstream and downstream domains.	128 packets	(Wendell Odom 2005).
Minimum (lower) Service class queue threshold values (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the upstream and downstream domains.	(15/32:7/16:13/32) times the queue size.	(Cisco 2013)
Service class queue dropping probabilities (SC <sub>1</sub> :SC <sub>2</sub> :SC <sub>3</sub> ) in the upstream and downstream domains.	(1/2:1/2:1/2) times the queue size.	(Cisco 2013)
Traffic Distribution in the upstream and downstream domains.	VoIP (SC <sub>1</sub> ) – Highest Priority, Video Traffic (SC <sub>2</sub> ), FTP (SC <sub>3</sub> ) – Lowest Priority, and Data Base Request message (Best Effort).	

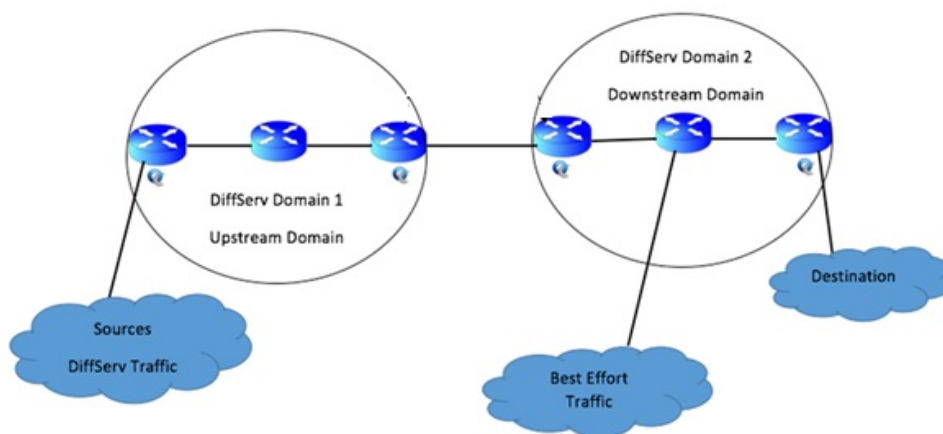


Figure 9-1, Research problem simulation scenario.

In this experiment, we increase the traffic of one of the service classes traffic at the upstream domain, the traffic of SC<sub>2</sub> (Video Traffic) for example. When the SC<sub>2</sub> traffic at the upstream domain of Figure 9-1 increases, the resources of the link that connects the upstream and downstream domains will (currently) be configured based only on the traffic conditions of the egress upstream router and may become mostly occupied by the increasing SC<sub>2</sub> traffic – plus the SC<sub>1</sub> and SC<sub>3</sub> traffic as shown in Figure 9-2, Figure 9-3 and Figure 9-4. In addition, the resources of the downstream domain may also become mostly occupied by the increasing SC<sub>2</sub> traffic (plus the SC<sub>1</sub> and SC<sub>3</sub> traffic). The average utilization of SC<sub>2</sub> traffic at the link that connects both domains is about 60% of the link capacity while the average utilization for the highest priority service class SC<sub>1</sub> is about 18% of the link capacity and for the lowest priority service class SC<sub>3</sub> is about 2.5% of the link capacity.

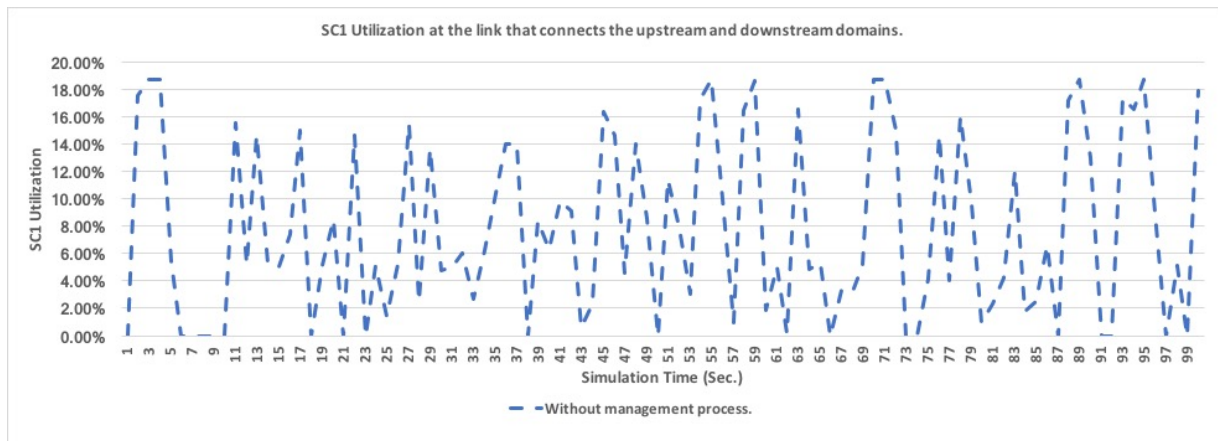


Figure 9-2, Explanation of research problem -SC1 utilization at the link that connects the upstream and downstream DiffServ domains.

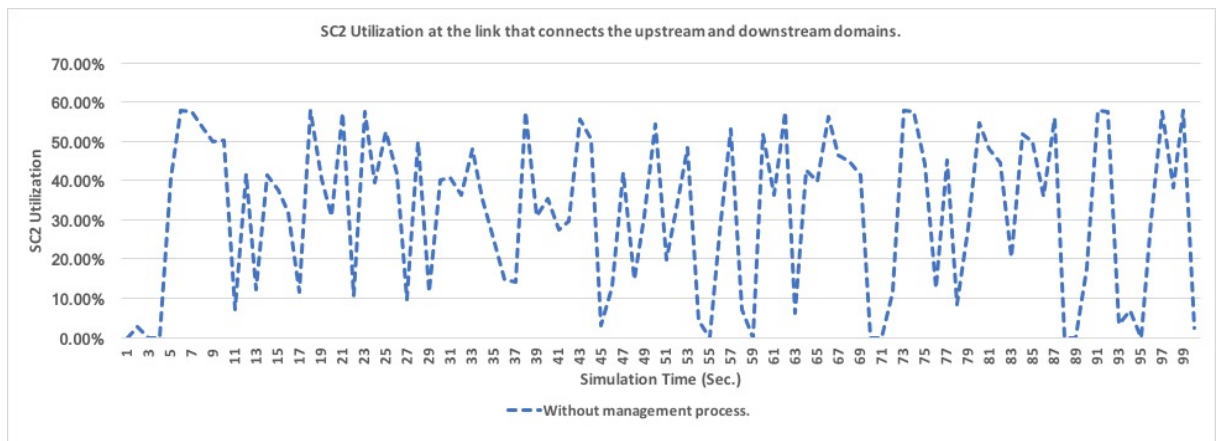


Figure 9-3, Explanation of research problem -SC2 utilization at the link that connects the upstream and downstream DiffServ domains.

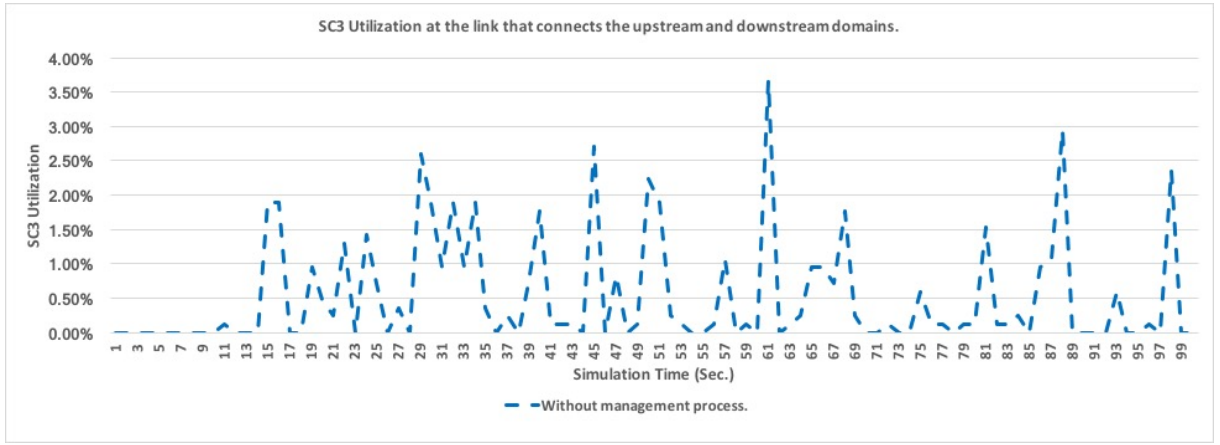


Figure 9-4, Explanation of research problem- SC3 utilization at the link that connects the upstream and downstream DiffServ domains.

In addition, there are still available resources not utilized well for  $SC_1$  and  $SC_3$  queues at the ingress router of the downstream domain while  $SC_2$  queue resource is completely utilized. This means that the downstream domain favoring the medium priority traffic  $SC_2$  while the traffic of  $SC_1$  and  $SC_3$  are suffering. Figure 9-5, Figure 9-6 and Figure 9-7 represent the congestion level in each service class queue at the ingress router of the downstream domain. The congestion level can be expressed as the ratio of the average service class queue size or length to the allocated service class buffer size. It is noticeable that  $SC_2$  queue is congested at the ingress router of the downstream domain, its congestion level is about 0.8 of its buffer size for the whole period of simulation while the congestion level for the highest priority service class is between 0.4 and 0.7 of  $SC_1$  buffer size for the whole period of simulation and for the lowest priority service class  $SC_3$  do not exceed 0.3 of its buffer size for the whole period of simulation.

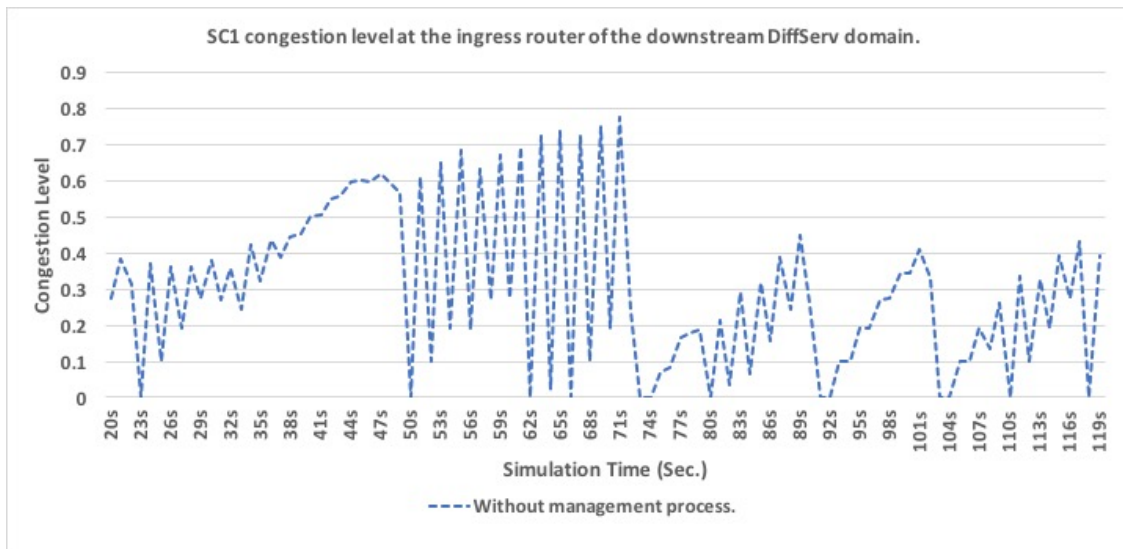


Figure 9-5, Explanation of research problem -SC1 congestion level at the ingress router of the downstream domain.

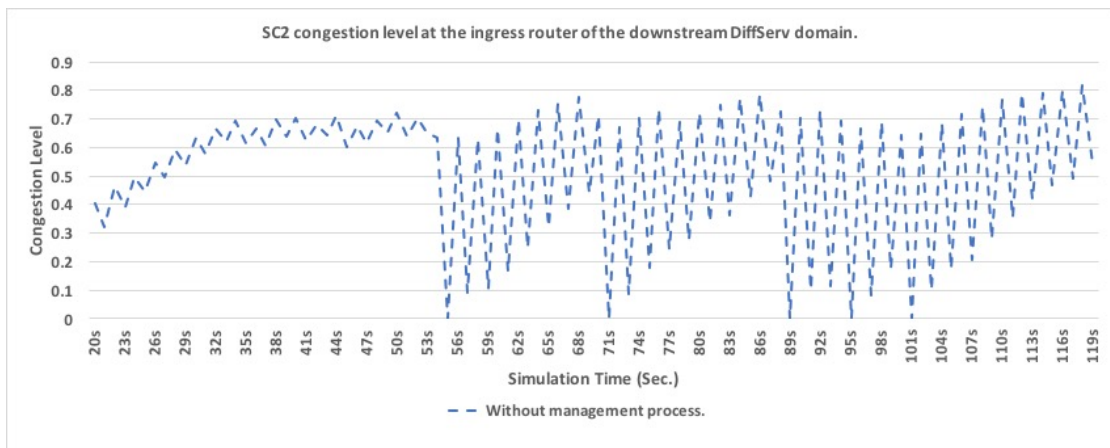


Figure 9-6, Explanation of research problem- SC2 congestion level at the ingress router of the downstream domain.

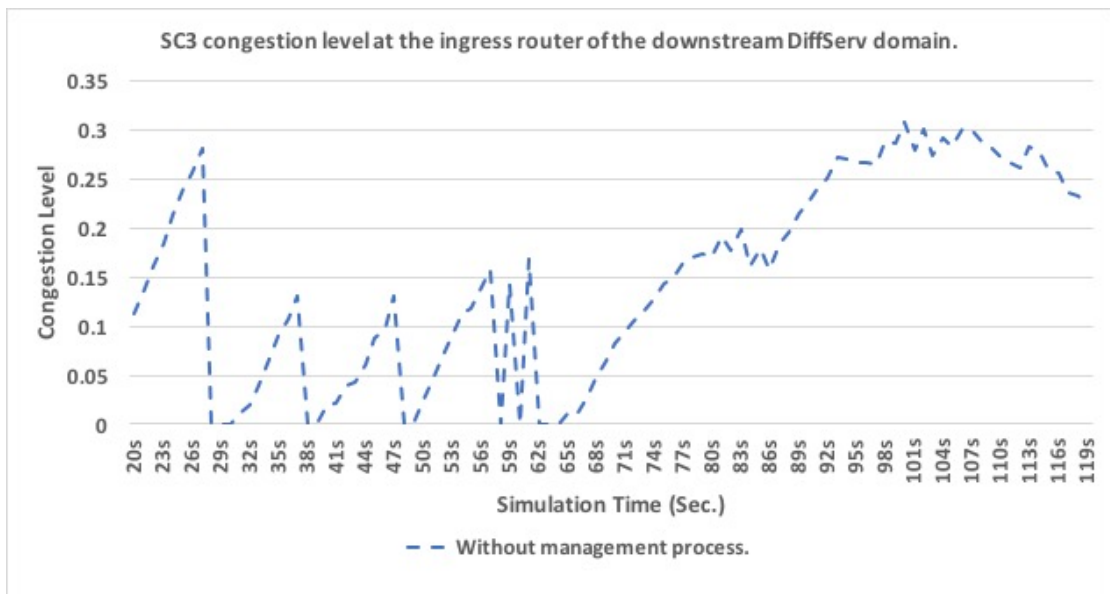


Figure 9-7, Explanation of research problem -SC3 congestion level at the ingress router of the downstream domain.

## A-2: Use of NS3 Class References in the Simulated Queue and Traffic Model:

### A-2.1: In RDWQueue Model:

Table 9-3, NS3 class references which are used to build the RDWQueue model and their purposes.

NS3 class reference	Purpose
NS3::Queue class reference	To build the RDWQueue model of a DiffServ.
NS3::Object model	To instantiate, identify and set the attributes of the instantiated RDWQueue model.
NS3::Simulator Class Reference	To schedule the events of RDWQueue.
NS3::Exponential Random Distribution Class Reference	To generate random value from exponential random distribution.
NS3::QoSTag Class Reference	To provide a means whereby applications which generate traffic can specify the application tag and define traffic flow

### A-2.2: In TosApp Model:

Table 9-4, NS3 class references which are used to build the TosApp model and their purposes.

NS3 class reference	Purpose
NS3::Socket Class Reference	To create one or more TCP or UDP socket(s) for the source addresses that are to be connected to destinations for packet sending.
NS3::Address Class Reference	To specify the addresses of destinations.
NS3::Packet Class Reference	To create packets with the required packet size in bytes, add a packet tag (ToS) to the created packets and send the packets to these destinations.
NS3::QoS Tag Class Reference	To set-up the ToS tag for the packet to be sent.
NS3::Exponential Random Variable Class Reference	To obtain random inter-arrival times which are used in scheduling the events of sending packets and to obtain random but bounded packet sizes
NS3::Constant Random Variable Class Reference	To obtain constant inter-arrival times for scheduling the events of sending packets.
NS3::Data Rate Class Reference	To construct a data rate for the generated traffic in bits/sec which is used in scheduling the events of sending packets.
NS3::Simulator Class Reference	To schedule the events of sending the generated packets of TosApp.

## A-3: Queue Model NS3 Simulation Code:

```
1  #ifndef RDWQUEUE_H
2  #define RDWQUEUE_H
3
4  #include <queue>
5  #include <deque>
6  #include <map>
7  #include <list>
8  #include <cmath>
9
10
11 #include "ns3/packet.h"
12 #include "ns3/queue.h"
13 #include "ns3/nstime.h"
14 #include "ns3/boolean.h"
15 #include "ns3/data-rate.h"
16 #include "ns3/nstime.h"
17 #include "ns3/ipv4-header.h"
18 #include "ns3/core-module.h"
19 #include "ns3/network-module.h"
20 #include "ns3/internet-module.h"
21 #include "ns3/point-to-point-module.h"
22 #include "ns3/applications-module.h"
23 #include "ns3/csma-module.h"
24
25 using namespace ns3;
26
27 class RDWQueue : public Queue
28 {
29 public:
30     static TypeId GetTypeId(void);
31     RDWQueue();
32     RDWQueue(int id);
33     virtual ~RDWQueue();
34     // common status
35     uint32_t linkID;           //!< which link this queue was installed
36     DataRate m_linkBandwidth;  //!< Link bandwidth
37     Time m_linkDelay;          //!< Link delay
38     uint32_t BYTEmode;
39     uint32_t SC1Packets;
40     uint32_t SC2Packets;
41     uint32_t SC3Packets;
42     uint32_t SC1DepartPackets;
43     uint32_t SC2DepartPackets;
44     uint32_t SC3DepartPackets;
45     uint32_t SC4PKTsSC1;
46     uint32_t SC4PKTsSC2;
47     uint32_t SC4PKTsSC3;
48     double_t usc1; // edit 29/09
49     double_t usc2; // edit 29/09
50     double_t usc3; // edit 29/09
51     double_t u; // edit 29/09
52     double_t UtiliSC1;
53     double_t UtiliSC2;
54     double_t UtiliSC3;
55
56     uint32_t band;
57     uint32_t Pk_ID;
58
59     int currSecond;
60     int NumberOfPktsPerSec;
61     int NumberOfPktsPerK;
```



```

62
63 // customized status
64 void setPKTBUFFER(uint32_t sc1, uint32_t sc2, uint32_t sc3);
65 uint32_t SC1PKTBUFFER;
66 uint32_t SC2PKTBUFFER;
67 uint32_t SC3PKTBUFFER;
68 void setBYTEBUFFER(uint32_t sc1, uint32_t sc2, uint32_t sc3);
69 uint32_t SC1BYTEBUFFER;
70 uint32_t SC2BYTEBUFFER;
71 uint32_t SC3BYTEBUFFER;
72 void setREDTHIGH(double sc1, double sc2, double sc3);
73 double REDSC1THIGH;
74 double REDSC2THIGH;
75 double REDSC3THIGH;
76 void setREDTHIGHPKT(double sc1, double sc2, double sc3);
77 double REDSC1THIGHPKT;
78 double REDSC2THIGHPKT;
79 double REDSC3THIGHPKT;
80
81 void setREDHDROP(double sc1, double sc2, double sc3);
82 double REDSC1HDROP;
83 double REDSC2HDROP;
84 double REDSC3HDROP;
85 void setREDTLOW(double sc1, double sc2, double sc3);
86 void setREDTLOWPKT(double sc1, double sc2, double sc3);
87
88 void setALPHALPKT(float sc1, float sc2, float sc3);
89 void setALPHAHPKT(float sc1, float sc2, float sc3);
90
91 double REDSC1TLOW;
92 double REDSC2TLOW;
93 double REDSC3TLOW;
94 double REDSC1TLOWPKT;
95 double REDSC2TLOWPKT;
96 double REDSC3TLOWPKT;
97
98 void setREDLDROP(double sc1, double sc2, double sc3);
99 double REDSC1LDROP;
100 double REDSC2LDROP;
101 double REDSC3LDROP;
102 void setALPHAL(float sc1, float sc2, float sc3);
103 float SC1CONGESTIONFATORALPHAL;
104 float SC2CONGESTIONFATORALPHAL;
105 float SC3CONGESTIONFATORALPHAL;
106 float PKTSC1CONGESTIONFATORALPHAL;
107 float PKTSC2CONGESTIONFATORALPHAL;
108 float PKTSC3CONGESTIONFATORALPHAL;
109
110 void setALPHAH(float sc1, float sc2, float sc3);
111 float SC1CONGESTIONFATORALPHAH;
112 float SC2CONGESTIONFATORALPHAH;
113 float SC3CONGESTIONFATORALPHAH;
114 float PKTSC1CONGESTIONFATORALPHAH;
115 float PKTSC2CONGESTIONFATORALPHAH;
116 float PKTSC3CONGESTIONFATORALPHAH;
117
118 void setBELTA(float sc1, float sc2, float sc3);
119 float CONGESTIONFATBELTA1;
120 float CONGESTIONFATBELTA2;
121 float CONGESTIONFATBELTA3;
122

```

```

123 // queue mode is packet, for calculate the delay.
124
125 virtual bool DoEnqueue(Ptr<Packet> p);
126 virtual Ptr<Packet> DoDequeue(void);
127 int PktDeqScPromo;
128 virtual Ptr<const Packet> DoPeek(void) const;
129
130 uint32_t Classify(Ptr<const Packet> p); // check the tos
131 double GenRand(void);
132
133 float MeasureQueueLength(void); // measure the queue length every time interval, store in usagelist
134
135 std::list<double> SC1Usage;
136 std::list<double> SC2Usage;
137 std::list<double> SC3Usage;
138
139 // For average queue length calculation new method
140
141 std::vector<double> SC1PrevUsage;
142 std::vector<double> SC2PrevUsage;
143 std::vector<double> SC3PrevUsage;
144
145 std::vector<double> SC1CurreUsage;
146 std::vector<double> SC2CurreUsage;
147 std::vector<double> SC3CurreUsage;
148
149
150 std::vector<double> SC1CurreDelay;
151 std::vector<double> SC2CurreDelay;
152 std::vector<double> SC3CurreDelay;
153
154 double SC1CurrUsage;
155 double SC2CurrUsage;
156 double SC3CurrUsage;
157
158 float MeasureDelay(void); // measure the queue length every time interval, store in usageset
159
160 std::list<uint64_t> SC1Delay;
161 std::list<uint64_t> SC2Delay;
162 std::list<uint64_t> SC3Delay;
163
164 void CalculateSchedulingRate(void); // calculate everything in here.
165 int CalRatePktType;
166 float* CalculateSchedulingRateFixed(float rates[3]);
167 float* CalculateSchedulingRatePktType1(float rates[3]);
168 float* CalculateSchedulingRatePktType2(float rates[3]);
169 float* CalculateSchedulingRateByte(float rates[3]);
170
171 float SC1ScheRate;
172 float SC2ScheRate;
173 float SC3ScheRate;
174 bool CalculateBucket(float sc1, float sc2, float sc3);
175 int DeQMode1CurrCls;
176
177 double SC1ScheRateAvg;
178 double SC2ScheRateAvg;
179 double SC3ScheRateAvg;
180
181 uint32_t SC1BUCKETS;

```



```

182     uint32_t SC2BUCKETS;
183     uint32_t SC3BUCKETS;
184
185     uint32_t SC1BUCKETSCPY;
186     uint32_t SC2BUCKETSCPY;
187     uint32_t SC3BUCKETSCPY;
188     double Ran;
189
190     float GetSchedulingRate(int band);
191     bool SetSchedulingRate(int band, float SchRate); // for node 4,8
192     bool SetSchedulingRateByFactor(int band, float alpha); // for node 5
193     bool SetSchedulingRateByFactorPacketMode(int band, float alpha); // for node 5
194     bool SetSchedulingRateByFactorSC1;
195     bool SetSchedulingRateByFactorSC2;
196     bool SetSchedulingRateByFactorSC3;
197
198     bool CalculateWeight(void); // calculate the weight
199     float SC1Weight;
200     float SC2Weight;
201     float SC3Weight;
202     float GetWeight(int band);
203     bool SetWeight(int band, float Weight); // for node 4,8
204
205     std::deque<Ptr<Packet> > m_packets1; //!< the packets in band 0
206     std::deque<Ptr<Packet> > m_packets2; //!< the packets in band 1
207     std::deque<Ptr<Packet> > m_packets3; //!< the packets in band 2
208
209     std::map<uint64_t, Time> SC1EnqueueTime;
210     std::map<uint64_t, Time> SC2EnqueueTime;
211     std::map<uint64_t, Time> SC3EnqueueTime;
212     std::map<uint64_t, Time> SC1DequeueTime;
213     std::map<uint64_t, Time> SC2DequeueTime;
214     std::map<uint64_t, Time> SC3DequeueTime;
215
216     uint64_t sc1ByteLength; //!< Queue length in bytes(usage)
217     uint64_t sc2ByteLength;
218     uint64_t sc3ByteLength;
219
220     uint64_t sc1qavg; //!< Average Queue length in bytes(usage)
221     uint64_t sc2qavg;
222     uint64_t sc3qavg;
223
224     double REDSC1LD;
225     double REDSC2LD;
226     double REDSC3LD;
227
228     uint32_t sc1dropcount;
229     uint32_t sc2dropcount;
230     uint32_t sc3dropcount;
231     uint32_t sc4dropcount;
232
233     uint32_t sc1Hdropcount;
234     uint32_t sc1Ldropcount;
235     uint32_t sc1Bdropcount;
236
237     uint32_t sc2Hdropcount;
238     uint32_t sc2Ldropcount;
239     uint32_t sc2Bdropcount;
240
241     uint32_t sc3Hdropcount;
242     uint32_t sc3Ldropcount;

```

```

243     uint32_t sc3Bdropcount;
244
245     /**
246     * \brief Get the current value of the queue in bytes or packets.
247     *
248     * \returns The queue size in bytes or packets.
249     */
250     uint32_t GetQueueSizeBytes(uint32_t band);
251     uint32_t GetQueueSizePkts(uint32_t band);
252     void ShowStatisticReport(void);
253
254     double getAverageQueueLength(int band);
255     float getAverageQueueDelay(int band);
256     void CalculateSchedulingRateQ6(int band, double qlength, float delay);
257
258     uint32_t sc1pktsinsecond;
259     uint32_t sc2pktsinsecond;
260     uint32_t sc3pktsinsecond;
261
262     void UpdateLinkUtilization(uint32_t band, uint32_t pktsize);
263     void getLinkUtilization();
264
265     void UpdatePacketsPerSec(uint32_t band);
266     void getPktsPerSec();
267     uint32_t sc1pktsinsec;
268     uint32_t sc2pktsinsec;
269     uint32_t sc3pktsinsec;
270
271
272     //bool SetSchedulingRateByAverage(int band, float sclaveragelength, float  2
273     averagelength, float averagedelay);
274     bool SetSchedulingRateByAverage(int band, double sclaveragelength, double  2
275     sc2averagelength, double sc3averagelength, double sclaveragedelay, double  2
276     sc2averagedelay, double sc3averagedelay );
277
278     bool SetSchedulingRateByLinearFactor(int band, float avgqueueLength);
279     bool SetSchedulingRateByLinearFactorSC1;
280     bool SetSchedulingRateByLinearFactorSC2;
281     bool SetSchedulingRateByLinearFactorSC3;
282
283     bool SetSchedulingRateByAverageSC1;
284     bool SetSchedulingRateByAverageSC2;
285     bool SetSchedulingRateByAverageSC3;
286
287     void UpdateAverageQueueDelay(int band, double AverageQueueDelay);
288     double GetAverageQueueDelay(int band);
289     std::vector<double> SC1CurrAverageDelay;
290     std::vector<double> SC2CurrAverageDelay;
291     std::vector<double> SC3CurrAverageDelay;
292
293     void UpdateAverageQueueLength1 (int band, double AverageQueueLength);
294     double GetAverageQueueLength1(int band);
295     std::vector<double> SC1CurrAverageLength1;
296     std::vector<double> SC2CurrAverageLength1;
297     std::vector<double> SC3CurrAverageLength1;
298
299     void UpdateAverageQueueLength2 (int band, double AverageQueueLength);
300     double GetAverageQueueLength2(int band);
301     std::vector<double> SC1CurrAverageLength2;
302     std::vector<double> SC2CurrAverageLength2;
303     std::vector<double> SC3CurrAverageLength2;

```

```
301
302     bool SetAverageQueueDelay1;
303     bool SetAverageQueueDelay2;
304     bool SetAverageQueueDelay3;
305
306
307 };
308
309 #endif // RDWQUEUE_H
310
```

```

1  #include "RDWQueue.h"
2  #include "ns3/log.h"
3  #include "ns3/enum.h"
4  #include "ns3/uinteger.h"
5  #include "ns3/double.h"
6  #include "ns3/simulator.h"
7  #include "ns3/abort.h"
8  #include "ns3/random-variable-stream.h"
9  #include "Case1_conf.h"
10 #include "ns3/qos-tag.h"
11
12 using namespace ns3;
13
14 NS_LOG_COMPONENT_DEFINE("RDWQueue");
15 NS_OBJECT_ENSURE_REGISTERED(RDWQueue);
16
17 TypeId RDWQueue::GetTypeId(void)
18 {
19     static TypeId tid = TypeId("ns3::RDWQueue")
20         .SetParent<Queue>()
21         .SetGroupName("Network")
22         .AddConstructor<RDWQueue>()
23         .AddAttribute("LinkBandwidth",
24             "The RDWQueue link bandwidth",
25             DataRateValue(DataRate("1.5Mbps")),
26             MakeDataRateAccessor(&RDWQueue::m_linkBandwidth),
27             MakeDataRateChecker())
28         .AddAttribute("LinkDelay",
29             "The RDWQueue link delay",
30             TimeValue(MilliSeconds(20)),
31             MakeTimeAccessor(&RDWQueue::m_linkDelay),
32             MakeTimeChecker());
33
34     return tid;
35 }
36
37
38 // Constructor Function
39 RDWQueue::RDWQueue(int id) : Queue()
40 {
41     linkID = id;
42     // ctor
43     sc1dropcount = 0;
44     sc2dropcount = 0;
45     sc3dropcount = 0;
46     sc4dropcount = 0;
47
48     sc1Hdropcount = 0;
49     sc1Ldropcount = 0;
50     sc1Bdropcount = 0;
51
52     sc2Hdropcount = 0;
53     sc2Ldropcount = 0;
54     sc2Bdropcount = 0;
55
56     sc3Hdropcount = 0;
57     sc3Ldropcount = 0;
58     sc3Bdropcount = 0;
59

```

```

60     SC1Weight = 4;
61     SC2Weight = 2;
62     SC3Weight = 1;
63
64     SC1BUCKETS = 10;
65     SC2BUCKETS = 10;
66     SC3BUCKETS = 10;
67
68     sc1qavg = 100;
69     sc2qavg = 100;
70     sc3qavg = 100;
71
72     REDSC1LD = 0;
73     REDSC2LD = 0;
74     REDSC3LD = 0;
75
76     sc1ByteLength = 0;
77     sc2ByteLength = 0;
78     sc3ByteLength = 0;
79
80
81     SC1Packets = 0;
82     SC2Packets = 0;
83     SC3Packets = 0;
84
85     SC1DepartPackets = 0;
86     SC2DepartPackets = 0;
87     SC3DepartPackets = 0;
88
89     SC4PKTsSC1 = 0;
90     SC4PKTsSC2 = 0;
91     SC4PKTsSC3 = 0;
92
93     SC1PrevUsage.push_back(sc1ByteLength);
94     SC2PrevUsage.push_back(sc2ByteLength);
95     SC3PrevUsage.push_back(sc3ByteLength);
96
97     SC1CurreUsage.push_back(sc1ByteLength);
98     SC2CurreUsage.push_back(sc2ByteLength);
99     SC3CurreUsage.push_back(sc3ByteLength);
100
101     SC1CurreDelay.push_back(0);
102     SC2CurreDelay.push_back(0);
103     SC3CurreDelay.push_back(0);
104
105
106     band = 0;
107     BYTEmode = 1;
108     CalRatePktType = 1;
109     PktDeqScPromo = 1;//1
110     DeQModelCurrCls = 1;
111     currSecond = 0;
112     NumberOfPktsPerSec = 0;
113     NumberOfPktsPerK = 0;
114
115     for (double i = 10; i < Case1_conf::SIM_STOP_Sec; i +=1) {
116         Simulator::Schedule(Seconds(i), &RDWQueue::CalculateSchedulingRate, this);
117     }
118
119     for (double i = 10; i < Case1_conf::SIM_STOP_Sec; i += 1) {
120         Simulator::Schedule(Seconds(i), &RDWQueue::ShowStatisticReport, this);

```



```

121     }
122     for(double i = 1; i < Case1_conf::SIM_STOP_Sec; i += 1) {
123         Simulator::Schedule(Seconds(i), &RDWQueue::getLinkUtilization, this);
124         Simulator::Schedule(Seconds(i), &RDWQueue::getPktsPerSec, this);
125     }
126 }
127
128 }
129 // Destructor Function
130 RDWQueue::~RDWQueue()
131 {
132     // dtor
133 }
134 // DoEnqueue Function (Byte Mode)
135 bool RDWQueue::DoEnqueue(Ptr<Packet> p)
136 {
137     uint32_t band = Classify(p);
138     NS_LOG_FUNCTION(linkID << this << band);
139     double Ran;
140     if (BYTEmode) // byte mode
141     {
142         if (band == 1 && (p->GetUid() != Pk_ID || p->GetUid() == 0)) {
143             if (sc1ByteLength < SC1BYTEBUFFER) {
144                 if (sc1ByteLength > REDSC1THIGH) {
145                     Ran = GenRand();
146                     if (Ran < REDSC1HDROP) {
147                         Pk_ID = p->GetUid();
148                         Drop(p);
149                         sc1dropcount++;
150                         sc1Hdropcount++;
151                         NS_LOG_INFO(linkID << " "
152                                     << "SC1 Hthreshold Drop"
153                                     << " " << sc1Hdropcount);
154                     } else {
155                         Pk_ID = p->GetUid();
156                         m_packets1.push_back(p);
157                         sc1ByteLength += p->GetSize();
158                         SC1EnqueueTime[p->GetUid()] = Simulator::Now();
159                         SC1Packets++;
160                         NS_LOG_INFO("SC1Pkt_InsertTime"
161                                     << " " << linkID << " " << p->GetUid() << " " <<
162                                     << p->GetSize() << " " <<
163                                     << SC1EnqueueTime[p->GetUid()] << "tos" << band << " " << SC1Packets);
164                     }
165                 } else if (sc1ByteLength > REDSC1TLOW) {
166                     Ran = GenRand();
167                     if (Ran < REDSC1LDROP) {
168                         Pk_ID = p->GetUid();
169                         Drop(p);
170                         sc1dropcount++;
171                         sc1Ldropcount++;
172                         NS_LOG_INFO(linkID << " "
173                                     << "SC1 Lthreshold Drop"
174                                     << " " << sc1Ldropcount);
175                     } else {
176                         Pk_ID = p->GetUid();
177                         m_packets1.push_back(p);
178                         sc1ByteLength += p->GetSize();
179                         SC1EnqueueTime[p->GetUid()] = Simulator::Now();
180                         SC1Packets++;

```

```

180         NS_LOG_INFO("SC1Pkt_InsertTime"
181                     << " " << linkID << " " << p->GetUid() << " "
182                     << p->GetSize() << " "
183                     << SC1EnqueueTime[p->GetUid()] << " "
184                     << "tos" << band << " " << SC1Packets);
185     }
186     } else {
187         Pk_ID = p->GetUid();
188         m_packets1.push_back(p);
189         sc1ByteLength += p->GetSize();
190         SC1EnqueueTime[p->GetUid()] = Simulator::Now();
191         SC1Packets++;
192         NS_LOG_INFO("SC1Pkt_InsertTime"
193                     << " " << linkID << " " << p->GetUid() << " "
194                     << p->GetSize() << " "
195                     << SC1EnqueueTime[p->GetUid()] << " "
196                     << "tos" << band << " " << SC1Packets);
197     }
198     } else {
199         Pk_ID = p->GetUid();
200         Drop(p);
201         sc1dropcount++;
202         sc1Bdropcount++;
203         NS_LOG_INFO(linkID << " "
204                     << "SC1 Buffer Drop"
205                     << " " << sc1Bdropcount);
206     }
207     } else if (band == 2 && (p->GetUid() != Pk_ID || p->GetUid() == 0)) {
208         if (sc2ByteLength < SC2BYTEBUFFER) {
209             if (sc2ByteLength > REDSC2THIGH) {
210                 Ran = GenRand();
211                 if (Ran < REDSC2HDRDP) {
212                     Pk_ID = p->GetUid();
213                     Drop(p);
214                     sc2dropcount++;
215                     sc2Hdropcount++;
216                     NS_LOG_INFO(linkID << " "
217                                 << "SC2 Hthreshold Drop"
218                                 << " " << sc2Hdropcount);
219                 } else {
220                     Pk_ID = p->GetUid();
221                     m_packets2.push_back(p);
222                     sc2ByteLength += p->GetSize();
223                     SC2EnqueueTime[p->GetUid()] = Simulator::Now();
224                     SC2Packets++;
225                     NS_LOG_INFO("SC2Pkt_InsertTime"
226                                 << " " << linkID << " " << p->GetUid() << " "
227                                 << p->GetSize() << " "
228                                 << SC2EnqueueTime[p->GetUid()] << " "
229                                 << "tos" << band << " " << SC2Packets);
230                 }
231             } else if (sc2ByteLength > REDSC2TLOW) {
232                 Ran = GenRand();
233                 if (Ran < REDSC2LDRDP) {
234                     Pk_ID = p->GetUid();
235                     Drop(p);
236                     sc2dropcount++;
237                     sc2Ldropcount++;
238                     NS_LOG_INFO(linkID << " "
239                                 << "SC2 Lthreshold Drop"

```

```

238                                     << " " << sc2Ldropcount);
239     } else {
240         Pk_ID = p->GetUid();
241         m_packets2.push_back(p);
242         sc2ByteLength += p->GetSize();
243         SC2EnqueueTime[p->GetUid()] = Simulator::Now();
244         SC2Packets++;
245         NS_LOG_INFO("SC2Pkt_InsertTime"
246                     << " " << linkID << " " << p->GetUid() << " "
247                     << p->GetSize() << " "
248                     << SC2EnqueueTime[p->GetUid()] << " "
249                     << "tos" << band << " " << SC2Packets);
250     }
251     } else {
252         Pk_ID = p->GetUid();
253         m_packets2.push_back(p);
254         sc2ByteLength += p->GetSize();
255         SC2EnqueueTime[p->GetUid()] = Simulator::Now();
256         SC2Packets++;
257         NS_LOG_INFO("SC2Pkt_InsertTime"
258                     << " " << linkID << " " << p->GetUid() << " "
259                     << p->GetSize() << " "
260                     << SC2EnqueueTime[p->GetUid()] << " "
261                     << "tos" << band << " " << SC2Packets);
262     }
263     } else {
264         Pk_ID = p->GetUid();
265         Drop(p);
266         sc2dropcount++;
267         sc2Bdropcount++;
268         NS_LOG_INFO(linkID << " "
269                     << "SC2 Buffer Drop"
270                     << " " << sc2Bdropcount);
271     }
272     } else if (band == 3 && (p->GetUid() != Pk_ID || p->GetUid() == 0)) {
273         if (sc3ByteLength < SC3BYTEBUFFER) {
274             if (sc3ByteLength > REDSC3THIGH) {
275                 Ran = GenRand();
276                 if (Ran < REDSC3HDRDP) {
277                     Pk_ID = p->GetUid();
278                     Drop(p);
279                     sc3dropcount++;
280                     sc3Hdropcount++;
281                     NS_LOG_INFO(linkID << " "
282                                 << "SC3 Hthreshold Drop"
283                                 << " " << sc3Hdropcount);
284                 } else {
285                     Pk_ID = p->GetUid();
286                     m_packets3.push_back(p);
287                     sc3ByteLength += p->GetSize();
288                     SC3EnqueueTime[p->GetUid()] = Simulator::Now();
289                     SC3Packets++;
290                     NS_LOG_INFO("SC3Pkt_InsertTime"
291                                 << " " << linkID << " " << p->GetUid() << " "
292                                 << p->GetSize() << " "
293                                 << SC3EnqueueTime[p->GetUid()] << " "
294                                 << "tos" << band << " " << SC3Packets);
295                 }
296             } else if (sc3ByteLength > REDSC3TLOW) {
297                 Ran = GenRand();
298                 if (Ran < REDSC3LDRDP) {

```



```

296         Pk_ID = p->GetUid();
297         Drop(p);
298         sc3dropcount++;
299         sc3Ldropcount++;
300         NS_LOG_INFO(linkID << " "
301                     << "SC3 Lthreshold Drop"
302                     << " " << sc3Ldropcount);
303     } else {
304         Pk_ID = p->GetUid();
305         m_packets3.push_back(p);
306         sc3ByteLength += p->GetSize();
307         SC3EnqueueTime[p->GetUid()] = Simulator::Now();
308         SC3Packets++;
309         NS_LOG_INFO("SC3Pkt_InsertTime"
310                     << " " << linkID << " " << p->GetUid() << " "
311                     << p->GetSize() << " "
312                     << SC3EnqueueTime[p->GetUid()] << " "
313                     << "tos" << band << " " << SC3Packets);
314     }
315 } else {
316     Pk_ID = p->GetUid();
317     m_packets3.push_back(p);
318     sc3ByteLength += p->GetSize();
319     SC3EnqueueTime[p->GetUid()] = Simulator::Now();
320     SC3Packets++;
321     NS_LOG_INFO("SC3Pkt_InsertTime"
322                 << " " << linkID << " " << p->GetUid() << " "
323                 << p->GetSize() << " "
324                 << SC3EnqueueTime[p->GetUid()] << " "
325                 << "tos" << band << " " << SC3Packets);
326 }
327 } else {
328     Pk_ID = p->GetUid();
329     Drop(p);
330     sc3dropcount++;
331     sc3Bdropcount++;
332     NS_LOG_INFO(linkID << " "
333                 << "SC3 Buffer Drop"
334                 << " " << sc3Bdropcount);
335 }
336 } else if ( p->GetUid() != Pk_ID || p->GetUid() == 0 ) {
337     if (sc1ByteLength < (float)SC1BYTEBUFFER && sc1ByteLength <=
338         sc2ByteLength &&
339         sc1ByteLength <= sc3ByteLength ) {
340         Pk_ID = p->GetUid();
341         m_packets1.push_back(p);
342         sc1ByteLength += p->GetSize();
343         SC1EnqueueTime[p->GetUid()] = Simulator::Now();
344         SC1Packets++;
345         SC4PKTsSC1++;
346         NS_LOG_INFO("SC4Pkt_Insert"
347                     << " " << linkID << " " << p->GetUid() << " " <<
348                     p->GetSize() << " "
349                     << "tos" << band << " "
350                     << "SC4PKTs in SC1"
351                     << " " << SC4PKTsSC1);
352         NS_LOG_INFO("SC1Pkt_InsertTime"
353                     << " " << linkID << " " << p->GetUid() << " " <<
354                     p->GetSize() << " "
355                     << SC1EnqueueTime[p->GetUid()] << " "
356                     << "tos" << band << " " << SC1Packets);

```

```

352
353 } else if (sc2ByteLength < (float)SC2BYTEBUFFER && sc2ByteLength <=
sc1ByteLength &&
354     sc2ByteLength <= sc3ByteLength ) {
355     Pk_ID = p->GetUid();
356     m_packets2.push_back(p);
357     sc2ByteLength += p->GetSize();
358     SC2EnqueueTime[p->GetUid()] = Simulator::Now();
359     SC2Packets++;
360     SC4PKTsSC2++;
361     NS_LOG_INFO("SC4Pkt_Insert"
362         << " " << linkID << " " << p->GetUid() << " " <<
p->GetSize() << " "
363         << "tos" << band << " "
364         << "SC4PKTs in SC2"
365         << " " << SC4PKTsSC2);
366     NS_LOG_INFO("SC2Pkt_InsertTime"
367         << " " << linkID << " " << p->GetUid() << " " <<
p->GetSize() << " "
368         << SC2EnqueueTime[p->GetUid()] << " "
369         << "tos" << band << " " << SC2Packets);
370
371 } else if (sc3ByteLength < (float)SC3BYTEBUFFER && sc3ByteLength <=
sc1ByteLength &&
372     sc3ByteLength <= sc2ByteLength) {
373     Pk_ID = p->GetUid();
374     m_packets3.push_back(p);
375     sc3ByteLength += p->GetSize();
376     SC3EnqueueTime[p->GetUid()] = Simulator::Now();
377     SC3Packets++;
378     SC4PKTsSC3++;
379     NS_LOG_INFO("SC4Pkt_Insert"
380         << " " << linkID << " " << p->GetUid() << " " <<
p->GetSize() << " "
381         << "tos" << band << " "
382         << "SC4PKTs in SC3"
383         << " " << SC4PKTsSC3);
384     NS_LOG_INFO("SC3Pkt_InsertTime"
385         << " " << linkID << " " << p->GetUid() << " " <<
p->GetSize() << " "
386         << SC3EnqueueTime[p->GetUid()] << " "
387         << "tos" << band << " " << SC3Packets);
388
389 } else {
390     Pk_ID = p->GetUid();
391     Drop(p);
392     sc4dropcount++;
393     NS_LOG_INFO(linkID << " "
394         << "SC4 Buffer Drop"
395         << " " << sc4dropcount);
396     return false;
397 }
398 }
399 }
400
401
402 // DoEnqueue Function (Packet Mode)
403 //packet mode, NOT CONSIDERED IN THE RESEARCH
404 else {
405     if (band == 1 && (p->GetUid() != Pk_ID || p->GetUid() == 0)) {
406         if (m_packets1.size() < SC1PKTBUFFER) {

```

```

407         if (m_packets1.size() > REDSC1THIGHPKT) {
408             Ran = GenRand();
409             if (Ran < REDSC1HDRDP) {
410                 Pk_ID = p->GetUid();
411                 Drop(p);
412                 sc1dropcount++;
413                 sc1Hdropcount++;
414                 NS_LOG_INFO(linkID << " "
415                             << "SC1 Hthreshold Drop"
416                             << " " << sc1Hdropcount);
417             } else {
418                 Pk_ID = p->GetUid();
419                 m_packets1.push_back(p);
420                 sc1ByteLength += p->GetSize();
421                 SC1EnqueueTime[p->GetUid()] = Simulator::Now();
422                 SC1Packets++;
423                 NS_LOG_INFO("SC1Pkt_InsertTime"
424                             << " " << linkID << " " << p->GetUid() << " "
425                             << p->GetSize() << " "
426                             << SC1EnqueueTime[p->GetUid()] << "tos" << band
427                             << " " << SC1Packets);
428             }
429         } else if (m_packets1.size() > REDSC1LOWPKT) {
430             Ran = GenRand();
431             if (Ran < REDSC1LDRDP) {
432                 Pk_ID = p->GetUid();
433                 Drop(p);
434                 sc1dropcount++;
435                 sc1Ldropcount++;
436                 NS_LOG_INFO(linkID << " "
437                             << "SC1 Lthreshold Drop"
438                             << " " << sc1Ldropcount);
439             } else {
440                 Pk_ID = p->GetUid();
441                 m_packets1.push_back(p);
442                 sc1ByteLength += p->GetSize();
443                 SC1EnqueueTime[p->GetUid()] = Simulator::Now();
444                 SC1Packets++;
445                 NS_LOG_INFO("SC1Pkt_InsertTime"
446                             << " " << linkID << " " << p->GetUid() << " "
447                             << p->GetSize() << " "
448                             << SC1EnqueueTime[p->GetUid()] << " "
449                             << "tos" << band << " " << SC1Packets);
450             }
451         } else {
452             Pk_ID = p->GetUid();
453             m_packets1.push_back(p);
454             sc1ByteLength += p->GetSize();
455             SC1EnqueueTime[p->GetUid()] = Simulator::Now();
456             SC1Packets++;
457             NS_LOG_INFO("SC1Pkt_InsertTime"
458                         << " " << linkID << " " << p->GetUid() << " "
459                         << p->GetSize() << " "
460                         << SC1EnqueueTime[p->GetUid()] << " "
461                         << "tos" << band << " " << SC1Packets);
462         }
463     } else {
464         Pk_ID = p->GetUid();
465         Drop(p);
466         sc1dropcount++;
467         sc1Bdropcount++;

```

```

464         NS_LOG_INFO(linkID << " "
465                     << "SC1 Buffer Drop"
466                     << " " << sc1Bdropcount);
467     }
468
469     } else if (band == 2 && (p->GetUid() != Pk_ID || p->GetUid() == 0)) {
470         if (m_packets2.size() < SC2PKTBUFFER) {
471             if (m_packets2.size() > REDSC2THIGHPKT) {
472                 Ran = GenRand();
473                 if (Ran < REDSC2HDRDP) {
474                     Pk_ID = p->GetUid();
475                     Drop(p);
476                     sc2dropcount++;
477                     sc2Hdropcount++;
478                     NS_LOG_INFO(linkID << " "
479                               << "SC2 Hthreshold Drop"
480                               << " " << sc2Hdropcount);
481                 } else {
482                     Pk_ID = p->GetUid();
483                     m_packets2.push_back(p);
484                     sc2ByteLength += p->GetSize();
485                     SC2EnqueueTime[p->GetUid()] = Simulator::Now();
486                     SC2Packets++;
487                     NS_LOG_INFO("SC2Pkt_InsertTime"
488                               << " " << linkID << " " << p->GetUid() << " " <<
489                               << p->GetSize() << " " <<
490                               << SC2EnqueueTime[p->GetUid()] << "tos" << band << " " <<
491                               << " " << SC2Packets);
492                 }
493             } else if (m_packets2.size() > REDSC2TLOWPKT) {
494                 Ran = GenRand();
495                 if (Ran < REDSC2LDRDP) {
496                     Pk_ID = p->GetUid();
497                     Drop(p);
498                     sc2dropcount++;
499                     sc2Ldropcount++;
500                     NS_LOG_INFO(linkID << " "
501                               << "SC2 Lthreshold Drop"
502                               << " " << sc2Ldropcount);
503                 } else {
504                     Pk_ID = p->GetUid();
505                     m_packets2.push_back(p);
506                     sc2ByteLength += p->GetSize();
507                     SC2EnqueueTime[p->GetUid()] = Simulator::Now();
508                     SC2Packets++;
509                     NS_LOG_INFO("SC2Pkt_InsertTime"
510                               << " " << linkID << " " << p->GetUid() << " " <<
511                               << p->GetSize() << " " <<
512                               << SC2EnqueueTime[p->GetUid()] << " " <<
513                               << "tos" << band << " " << " " << SC2Packets);
514                 }
515             } else {
516                 Pk_ID = p->GetUid();
517                 m_packets2.push_back(p);
518                 sc2ByteLength += p->GetSize();
519                 SC2EnqueueTime[p->GetUid()] = Simulator::Now();
520                 SC2Packets++;
521                 NS_LOG_INFO("SC2Pkt_InsertTime"
522                           << " " << linkID << " " << p->GetUid() << " " <<
523                           << p->GetSize() << " " <<
524                           << SC2EnqueueTime[p->GetUid()] << " " <<

```



```

521                                     << "tos" << band << " " << SC2Packets);
522     }
523 } else {
524     Pk_ID = p->GetUid();
525     Drop(p);
526     sc2dropcount++;
527     sc2Bdropcount++;
528     NS_LOG_INFO(linkID << " "
529                 << "SC2 Buffer Drop"
530                 << " " << sc2Bdropcount);
531 }
532 } else if (band == 3 && (p->GetUid() != Pk_ID || p->GetUid() == 0)) {
533     if (m_packets3.size() < SC3PKTBUFFER) {
534         if (m_packets3.size() > REDSC3THIGHPKT) {
535             Ran = GenRand();
536             if (Ran < REDSC3HDROP) {
537                 Pk_ID = p->GetUid();
538                 Drop(p);
539                 sc3dropcount++;
540                 sc3Hdropcount++;
541                 NS_LOG_INFO(linkID << " "
542                             << "SC3 Hthreshold Drop"
543                             << " " << sc3Hdropcount);
544             } else {
545                 Pk_ID = p->GetUid();
546                 m_packets3.push_back(p);
547                 sc3ByteLength += p->GetSize();
548                 SC3EnqueueTime[p->GetUid()] = Simulator::Now();
549                 SC3Packets++;
550                 NS_LOG_INFO("SC3Pkt_InsertTime"
551                             << " " << linkID << " " << p->GetUid() << " "
552                             << p->GetSize() << " "
553                             << SC3EnqueueTime[p->GetUid()] << "tos" << band
554                             << " " << SC3Packets);
555             }
556         } else if (m_packets3.size() > REDSC3TLOWPKT) {
557             Ran = GenRand();
558             if (Ran < REDSC3LDROP) {
559                 Pk_ID = p->GetUid();
560                 Drop(p);
561                 sc3dropcount++;
562                 sc3Ldropcount++;
563                 NS_LOG_INFO(linkID << " "
564                             << "SC3 Lthreshold Drop"
565                             << " " << sc3Ldropcount);
566             } else {
567                 Pk_ID = p->GetUid();
568                 m_packets3.push_back(p);
569                 sc3ByteLength += p->GetSize();
570                 SC3EnqueueTime[p->GetUid()] = Simulator::Now();
571                 SC3Packets++;
572                 NS_LOG_INFO("SC3Pkt_InsertTime"
573                             << " " << linkID << " " << p->GetUid() << " "
574                             << p->GetSize() << " "
575                             << SC3EnqueueTime[p->GetUid()] << " "
576                             << "tos" << band << " " << SC3Packets);
577             }
578         } else {
579             Pk_ID = p->GetUid();
580             m_packets3.push_back(p);
581             sc3ByteLength += p->GetSize();

```

```

579         SC3EnqueueTime[p->GetUid()] = Simulator::Now();
580         SC3Packets++;
581         NS_LOG_INFO("SC3Pkt_InsertTime"
582                     << " " << linkID << " " << p->GetUid() << " " <<
583                     << p->GetSize() << " " <<
584                     << SC3EnqueueTime[p->GetUid()] << " " <<
585                     << "tos" << band << " " << SC3Packets);
586     }
587     } else {
588         Pk_ID = p->GetUid();
589         Drop(p);
590         sc3dropcount++;
591         sc3Bdropcount++;
592         NS_LOG_INFO(linkID << " " <<
593                     << "SC3 Buffer Drop"
594                     << " " << sc3Bdropcount);
595     }
596     } else {
597         // update 141114
598         if (m_packets1.size() < (float)SC1PKTBUFFER && m_packets1.size() <=
599             m_packets2.size() &&
600             m_packets1.size() <= m_packets3.size() && (p->GetUid() != Pk_ID ||
601                 p->GetUid() == 0)) {
602             Pk_ID = p->GetUid();
603             m_packets1.push_back(p);
604             sc1ByteLength += p->GetSize();
605             SC1EnqueueTime[p->GetUid()] = Simulator::Now();
606             SC1Packets++;
607             SC4PKTsSC1++;
608             NS_LOG_INFO("SC4Pkt_Insert"
609                         << " " << linkID << " " << p->GetUid() << " " <<
610                         << p->GetSize() << " " <<
611                         << "tos" << band << " " <<
612                         << "SC4PKTs in SC1"
613                         << " " << SC4PKTsSC1);
614             NS_LOG_INFO("SC1Pkt_InsertTime"
615                         << " " << linkID << " " << p->GetUid() << " " <<
616                         << p->GetSize() << " " <<
617                         << SC1EnqueueTime[p->GetUid()] << " " <<
618                         << "tos" << band << " " << SC1Packets);
619         } else if (m_packets2.size() < (float)SC2PKTBUFFER &&
620             m_packets2.size() <= m_packets1.size() &&
621             m_packets2.size() <= m_packets3.size() && (p->GetUid() !=
622                 Pk_ID || p->GetUid() == 0)) {
623             Pk_ID = p->GetUid();
624             m_packets2.push_back(p);
625             sc2ByteLength += p->GetSize();
626             SC2EnqueueTime[p->GetUid()] = Simulator::Now();
627             SC2Packets++;
628             SC4PKTsSC2++;
629             NS_LOG_INFO("SC4Pkt_Insert"
630                         << " " << linkID << " " << p->GetUid() << " " <<
631                         << p->GetSize() << " " <<
632                         << "tos" << band << " " <<
633                         << "SC4PKTs in SC2"
634                         << " " << SC4PKTsSC2);
635             NS_LOG_INFO("SC2Pkt_InsertTime"
636                         << " " << linkID << " " << p->GetUid() << " " <<
637                         << p->GetSize() << " " <<
638                         << SC2EnqueueTime[p->GetUid()] << " " <<

```

```

631         << "tos" << band << " " << SC2Packets);
632
633     } else if (m_packets3.size() < (float)SC3PKTBUFFER &&
m_packets3.size() <= m_packets1.size() &&
634         m_packets3.size() <= m_packets2.size() && (p->GetUid() !=
Pk_ID || p->GetUid() == 0)) {
635         Pk_ID = p->GetUid();
636         m_packets3.push_back(p);
637         sc3ByteLength += p->GetSize();
638         SC3EnqueueTime[p->GetUid()] = Simulator::Now();
639         SC3Packets++;
640         SC4PKTsSC3++;
641         NS_LOG_INFO("SC4Pkt_Insert"
642             << " " << linkID << " " << p->GetUid() << " " <<
p->GetSize() << " "
643             << "tos" << band << " "
644             << "SC4PKTs in SC3"
645             << " " << SC4PKTsSC3);
646         NS_LOG_INFO("SC3Pkt_InsertTime"
647             << " " << linkID << " " << p->GetUid() << " " <<
p->GetSize() << " "
648             << SC3EnqueueTime[p->GetUid()] << " "
649             << "tos" << band << " " << SC3Packets);
650
651     } else {
652         Pk_ID = p->GetUid();
653         Drop(p);
654         sc4dropcount++;
655         NS_LOG_INFO(linkID << " "
656             << "SC4 Buffer Drop"
657             << " " << sc4dropcount);
658
659         return false;
660     }
661 }
662 }
663 return true;
664 }
665
666 // DoDequeue Function (weights (buckets) are Based on the number of Packets)
667
668 Ptr<Packet> RDWQueue::DoDequeue(void)
669 {
670     NS_LOG_INFO(linkID);
671     Ptr<Packet> p = NULL;
672     if (PktDeqScPromo == 1) { // class 1->2->3,1->2->3
673         if (DeQModelCurrCls == 1 && m_packets1.size() > 0) {
674             if (SC1BUCKETS == 1 || m_packets1.size() <= 1) { // next class
675                 DeQModelCurrCls = 2;
676                 SC1BUCKETS = SC1BUCKETSCPY + 1;
677             }
678             p = m_packets1.front();
679             SC1DequeueTime[p->GetUid()] = Simulator::Now();
680             m_packets1.pop_front();
681             sc1ByteLength -= p->GetSize();
682             SC1BUCKETS--;
683             band = Classify(p);
684
685             if (band != 0) {
686                 UpdateLinkUtilization(band, p->GetSize());
687                 UpdatePacketsPerSec(band);

```

```

688         SC1DepartPackets++;
689         NS_LOG_INFO("SC1Pkt_DepartureTime"
690                     << " " << linkID << " " << p->GetUId() << " " <<
691                     p->GetSize() << " "
692                     << SC1DequeueTime[p->GetUId()].GetSeconds() << " "
693                     << "tos" << band << " " << SC1DepartPackets);
694     }
695     } else if (DeQModelCurrCls == 2 && m_packets2.size() > 0) {
696         if (SC2BUCKETS == 1 || m_packets2.size() <= 1) {
697             DeQModelCurrCls = 3;
698             SC2BUCKETS = SC2BUCKETSCPY + 1;
699         }
700         p = m_packets2.front();
701         SC2DequeueTime[p->GetUId()] = Simulator::Now();
702         m_packets2.pop_front();
703         sc2ByteLength -= p->GetSize();
704         SC2BUCKETS--;
705         band = Classify(p);
706         if (band != 0) {
707             UpdateLinkUtilization(band, p->GetSize());
708             UpdatePacketsPerSec(band);
709             SC2DepartPackets++;
710             NS_LOG_INFO("SC2Pkt_DepartureTime"
711                         << " " << linkID << " " << p->GetUId() << " " <<
712                         p->GetSize() << " "
713                         << SC2DequeueTime[p->GetUId()].GetSeconds() << " "
714                         << "tos" << band << " " << SC2DepartPackets);
715         }
716     } else if (DeQModelCurrCls == 3 && m_packets3.size() > 0) {
717         if (SC3BUCKETS == 1 || m_packets3.size() <= 1) {
718             DeQModelCurrCls = 1;
719             SC3BUCKETS = SC3BUCKETSCPY + 1;
720         }
721         p = m_packets3.front();
722         SC3DequeueTime[p->GetUId()] = Simulator::Now();
723         m_packets3.pop_front();
724         sc3ByteLength -= p->GetSize();
725         SC3BUCKETS--;
726         band = Classify(p);
727         if (band != 0) {
728             UpdateLinkUtilization(band, p->GetSize());
729             UpdatePacketsPerSec(band);
730             SC3DepartPackets++;
731             NS_LOG_INFO("SC3Pkt_DepartureTime"
732                         << " " << linkID << " " << p->GetUId() << " " <<
733                         p->GetSize() << " "
734                         << (SC3DequeueTime[p->GetUId()].GetSeconds() << " "
735                         << "tos" << band << " " << SC3DepartPackets);
736         }
737     }
738     } else { // class 1->2->3 with promote
739         if (SC1BUCKETS > 0) {
740             // sc1
741             NS_LOG_INFO(linkID << " SC1 Dequeue");
742             if (m_packets1.size()) {
743                 p = m_packets1.front();
744                 SC1DequeueTime[p->GetUId()] = Simulator::Now();
745                 m_packets1.pop_front();
746                 sc1ByteLength -= p->GetSize();
747                 SC1BUCKETS--;

```



```

746         band = Classify(p);
747         if (band != 0) {
748             UpdateLinkUtilization(band, p->GetSize());
749             UpdatePacketsPerSec(band);
750             SC1DepartPackets++;
751             NS_LOG_INFO("SC1Pkt_DepartureTime"
752                 << " " << linkID << " " << p->GetUid() << " "
753                 << p->GetSize() << " "
754                 << SC1DequeueTime[p->GetUid()].GetSeconds() << " "
755                 << "tos" << band << " " << SC1DepartPackets);
756         }
757     }
758     } else if (SC2BUCKETS > 0) {
759         // sc2
760         NS_LOG_INFO(linkID << " SC2 Dequeue");
761         if (m_packets2.size()) {
762             p = m_packets2.front();
763             SC2DequeueTime[p->GetUid()] = Simulator::Now();
764             m_packets2.pop_front();
765             sc2ByteLength -= p->GetSize();
766             SC2BUCKETS--;
767             band = Classify(p);
768             if (band != 0) {
769                 UpdateLinkUtilization(band, p->GetSize());
770                 UpdatePacketsPerSec(band);
771                 SC2DepartPackets++;
772                 NS_LOG_INFO("SC2Pkt_DepartureTime"
773                     << " " << linkID << " " << p->GetUid() << " "
774                     << p->GetSize() << " "
775                     << SC2DequeueTime[p->GetUid()].GetSeconds() << " "
776                     << "tos" << band << " " << SC2DepartPackets);
777             }
778         }
779     } else if (SC3BUCKETS > 0) {
780         // sc3
781         NS_LOG_INFO(linkID << " SC3 Dequeue");
782         if (m_packets3.size()) {
783             p = m_packets3.front();
784             SC3DequeueTime[p->GetUid()] = Simulator::Now();
785             m_packets3.pop_front();
786             sc3ByteLength -= p->GetSize();
787             SC3BUCKETS--;
788             band = Classify(p);
789             if (band != 0) {
790                 UpdateLinkUtilization(band, p->GetSize());
791                 UpdatePacketsPerSec(band);
792                 SC3DepartPackets++;
793                 NS_LOG_INFO("SC3Pkt_DepartureTime"
794                     << " " << linkID << " " << p->GetUid() << " "
795                     << p->GetSize() << " "
796                     << SC3DequeueTime[p->GetUid()].GetSeconds() << " "
797                     << "tos" << band << " " << SC3DepartPackets);
798             }
799         }
800     }
801     if (SC1BUCKETS == 0 && SC2BUCKETS == 0 && SC3BUCKETS == 0) {
802         SC1BUCKETS = SC1BUCKETSCPY;
803         SC2BUCKETS = SC2BUCKETSCPY;
804         SC3BUCKETS = SC3BUCKETSCPY;
805         NS_LOG_INFO("All Zeros" << " " << Simulator::Now());

```

```

804     }
805 }
806
807
808 if (p == 0) { //NULL
809     if (m_packets1.size() >0) {
810         p = m_packets1.front();
811         SC1DequeueTime[p->GetUid()] = Simulator::Now();
812         m_packets1.pop_front();
813         sc1ByteLength -= p->GetSize();
814         SC1BUCKETS--;
815         band = Classify(p);
816         if (band != 0) {
817             UpdateLinkUtilization(band, p->GetSize());
818             UpdatePacketsPerSec(band);
819             SC1DepartPackets++;
820             NS_LOG_INFO(linkID << " "
821                         << "SC1 Passive Dequeue");
822             NS_LOG_INFO("SC1Pkt_DepartureTime"
823                         << " " << linkID << " " << p->GetUid() << " " <<
824                         p->GetSize() << " "
825                         << SC1DequeueTime[p->GetUid()].GetSeconds() << " "
826                         << "tos" << band << " " << SC1DepartPackets);
827         }
828     } else if (m_packets2.size() >0) {
829         p = m_packets2.front();
830         SC2DequeueTime[p->GetUid()] = Simulator::Now();
831         m_packets2.pop_front();
832         sc2ByteLength -= p->GetSize();
833         SC2BUCKETS--;
834         band = Classify(p);
835         if (band != 0) {
836             UpdateLinkUtilization(band, p->GetSize());
837             UpdatePacketsPerSec(band);
838             SC2DepartPackets++;
839             NS_LOG_INFO(linkID << " "
840                         << "SC2 Passive Dequeue");
841             NS_LOG_INFO("SC2Pkt_DepartureTime"
842                         << " " << linkID << " " << p->GetUid() << " " <<
843                         p->GetSize() << " "
844                         << SC2DequeueTime[p->GetUid()].GetSeconds() << " "
845                         << "tos" << band << " " << SC2DepartPackets);
846         }
847     } else if (m_packets3.size() >0 ) {
848         p = m_packets3.front();
849         SC3DequeueTime[p->GetUid()] = Simulator::Now();
850         m_packets3.pop_front();
851         sc3ByteLength -= p->GetSize();
852         SC3BUCKETS--;
853         band = Classify(p);
854         if (band != 0) {
855             UpdateLinkUtilization(band, p->GetSize());
856             UpdatePacketsPerSec(band);
857             SC3DepartPackets++;
858             NS_LOG_INFO(linkID << " "
859                         << "SC3 Passive Dequeue");
860             NS_LOG_INFO("SC3Pkt_DepartureTime"
861                         << " " << linkID << " " << p->GetUid() << " " <<
862                         p->GetSize() << " "
863                         << SC3DequeueTime[p->GetUid()].GetSeconds() << " "

```

```

862         << "tos" << band << " " << SC3DepartPackets);
863     }
864 }
865
866
867 if (currSecond != (int)Simulator::Now().GetSeconds()) {
868     NS_LOG_INFO(linkID << " NumberOfPktsPerSec " << currSecond << " is " <<
869         NumberOfPktsPerSec);
870     NumberOfPktsPerSec = 0;
871     currSecond = (int)Simulator::Now().GetSeconds();
872     if ((int)currSecond % 20 == 0) {
873         NS_LOG_INFO(linkID << " NumberOfPktsPerK " << currSecond << " is " <<
874             NumberOfPktsPerK);
875         NumberOfPktsPerK = 0;
876     }
877 }
878
879 NumberOfPktsPerSec++;
880 NumberOfPktsPerK++;
881 return p;
882 }
883
884 // Updating (Calculating) the link utilization within an interval k Function.
885 void RDWQueue::UpdateLinkUtilization(uint32_t band, uint32_t pktsize)
886 {
887     if(band == 1) {
888         sc1pktsinsecond += pktsize;
889     } else if(band == 2) {
890         sc2pktsinsecond += pktsize;
891     } else if(band == 3) {
892         sc3pktsinsecond += pktsize;
893     }
894 }
895
896 // Get the link utilization within an interval k Function.
897 void RDWQueue::getLinkUtilization()
898 {
899     float a=0,b=0,c=0,d=0;
900
901     // for upstream links
902     if ((linkID == 134) || (linkID == 145) || // Scenairo 1 link capacity utilization
903         (linkID == 234) || (linkID == 245) || // Scenairo 2 link capacity utilization
904         (linkID == 323) || (linkID == 334) || (linkID == 356) || (linkID == 367))
905         // Scenairo 3 link capacity utilization
906     {
907         a = (float)sc1pktsinsecond * 8 / Case1_conf::UpstreamLINKCAPACITY;
908         b = (float)sc2pktsinsecond * 8 / Case1_conf::UpstreamLINKCAPACITY;
909         c = (float)sc3pktsinsecond * 8 / Case1_conf::UpstreamLINKCAPACITY;
910         d = a + b + c;
911         std::cout << Simulator::Now() << " " << "LinkUtilization in second for output
912             port:" << " " << linkID << " " << "Link Bandwidth (bits/sec):" << " " <<
913             Case1_conf::LINKCAPACITY << " " << "SC1:" << " " << a << " " << "SC2:" << " " << b << "
914             " << "SC3:" << " " << c << " " << "Total:" << " " << d << std::endl;
915     }
916
917 // for downstream links
918 else if ( (linkID == 168) || (linkID == 189) || // Scenairo 1 link capacity
919 utilization
920         (linkID == 268) || (linkID == 269) ||
921         (linkID == 2610) || (linkID == 2811) ||
922         (linkID == 2911) || (linkID == 21011) || // Scenairo 2 link capacity utilization
923         (linkID == 31013) || (linkID == 31113) ||

```



```

916         (linkID == 31213) || (linkID == 3810) || (linkID == 3811) ||
917         (linkID == 3812) || (linkID == 3910) || (linkID == 3911) ||
918         (linkID == 3912)) // Scenairo 3 link capacity utilization
919     {
920         a = (float)sc1pktsinsecond* 8 / Case1_conf::DownstreamLINKCAPACITY;
921         b = (float)sc2pktsinsecond * 8 / Case1_conf::DownstreamLINKCAPACITY;
922         c = (float)sc3pktsinsecond * 8 / Case1_conf::DownstreamLINKCAPACITY;
923         d = a + b + c;
924         std::cout << Simulator::Now() << " " << "LinkUtilization in second for output port:" << " " << linkID << " " << "Link Bandwidth (bits/sec):" << " " << Case1_conf::LINKCAPACITY << " " << "SC1:" << " " << a << " " << "SC2:" << " " << b << " " << "SC3:" << " " << c << " " << "Total:" << " " << d << std::endl;
925     }
926 }
927 // for domains links
928 else if ((linkID == 156) || // scenairo 1 between domains link capacity utilization
929         (linkID == 256) || // scenairo 2 between domains link capacity
930         (linkID == 348) || (linkID == 379)) // scenairo 3 between domains link capacity utilization
931 {
932     a = (float)sc1pktsinsecond* 8 / Case1_conf::DomainsLINKCAPACITY;
933     b = (float)sc2pktsinsecond * 8 / Case1_conf::DomainsLINKCAPACITY;
934     c = (float)sc3pktsinsecond * 8 / Case1_conf::DomainsLINKCAPACITY;
935     d = a + b + c;
936     std::cout << Simulator::Now() << " " << "DomainsLinkUtilization in second for output port:" << " " << linkID << " " << "Link Bandwidth (bits/sec):" << " " << Case1_conf::DomainsLINKCAPACITY << " " << "SC1:" << " " << a << " " << "SC2:" << " " << b << " " << "SC3:" << " " << c << " " << "Total:" << " " << d << std::endl;
937 }
938 }
939 // for destinations links
940 else if ((linkID == 1910) ||
941         (linkID == 21112) || // scenairo 2 Destination link capacity
942         (linkID == 31314)) // scenairo 3 Destination link capacity
943 {
944     a = (float)sc1pktsinsecond* 8 / Case1_conf::DestinationLINKCAPACITY;
945     b = (float)sc2pktsinsecond * 8 / Case1_conf::DestinationLINKCAPACITY;
946     c = (float)sc3pktsinsecond * 8 / Case1_conf::DestinationLINKCAPACITY;
947     d = a + b + c;
948     std::cout << Simulator::Now() << " " << "DestinationLinkUtilization in second for output port:" << " " << linkID << " " << "Link Bandwidth (bits/sec):" << " " << Case1_conf::DestinationLINKCAPACITY << " " << "SC1:" << " " << a << " " << "SC2:" << " " << b << " " << "SC3:" << " " << c << " " << "Total:" << " " << d << std::endl;
949 }
950 sc1pktsinsecond = sc2pktsinsecond = sc3pktsinsecond = 0;
951 }
952
953 // Get service class queue size in byte function
954 uint32_t RDWQueue::GetQueueSizeBytes(uint32_t band)
955 {
956     if (band == 1) {
957         return SC1CurreUsage.back();
958     } else if (band == 2) {
959         return SC2CurreUsage.back();
960     } else if (band == 3) {
961         return SC3CurreUsage.back();
962     }
963     return 0;
964 }

```

```

965 // Classification Function
966 uint32_t RDWQueue::Classify(Ptr<const Packet> p)
967 {
968     uint32_t band = 0;
969     QosTag tosTag;
970     if (p->PeekPacketTag(tosTag)) {
971         band = (uint32_t)tosTag.GetTid();
972     }
973     return band;
974 }
975 // Generate Random number (GenRand) function
976 double RDWQueue::GenRand(void)
977 {
978     Ptr<ExponentialRandomVariable> x = CreateObject<ExponentialRandomVariable>();
979     x->SetAttribute("Mean", DoubleValue(1));
980     x->SetAttribute("Bound", DoubleValue(0.5));
981     double value = x->GetValue();
982     return value;
983 }
984 // Calculate the GCD value Function.
985 int calculateGCD(int a, int b)
986 {
987     if (b > a) {
988         int tmp = b;
989         b = a;
990         a = tmp;
991     }
992     if (a % b == 0) {
993         return b;
994     } else {
995         return calculateGCD(a % b, b);
996     }
997 }
998 // Calculate scheduling rates function for service classes in the edge and core
    routers for DiffServ domains
999 void RDWQueue::CalculateSchedulingRate(void)
1000 {
1001     float rates[3];
1002     float* res;
1003     if (BYTEmode) {
1004         if (linkID == 145 || linkID == 189 || //Scenairo1
1005             linkID == 245 || linkID == 2811 || linkID == 2911 || linkID == 21011
1006             || linkID == 334 || linkID == 367 || linkID == 31013 || linkID ==
1007             31113 || linkID == 31213) //sceniro 3 fixed weights queues
1008         {
1009             res = CalculateSchedulingRateFixed(rates); // For the core routers
1010         }
1011         else if (CalRatePktType == 1)
1012         {
1013             res = CalculateSchedulingRateByte(rates); // For the edge routers
1014         }
1015         else
1016         {
1017             res = CalculateSchedulingRatePktType2(rates); // Not considered in
1018             research
1019         }
1020     }
1021     else {
1022         // packet mode, it is not used in the research.
1023         /* if (linkID == 45 || linkID == 89) {

```

```

1022         res = CalculateSchedulingRateFixed(rates);
1023
1024     } else if (CalRatePktType == 1) {
1025         res = CalculateSchedulingRatePktType1(rates);
1026     } else {
1027         res = CalculateSchedulingRatePktType2(rates);
1028     }*/
1029 }
1030 if (linkID == 268 || linkID == 269 || linkID == 2610 || // Scenairo 2
(Dynamic and Average of average)
1031     linkID == 3810 || linkID == 3811 || linkID == 3812 || // Sceairo 3
(Dynamic and Average of average)
1032     linkID == 3910 || linkID == 3911 || linkID == 3912) // Sceairo 3 (Dynamic
and Average of average)
1033 {
1034     *res = 0;
1035     *(res+1) = 0;
1036     *(res+2) = 0;
1037 }
1038 if ((linkID == 134) || (linkID == 145) || (linkID == 156) ||
1039     (linkID == 168) || (linkID == 189) || (linkID == 1910) ||
1040     (linkID == 234) || (linkID == 245) || (linkID == 256) || // Scenairo 2 Fixed
and Dynamic weights
1041     (linkID == 2811) || (linkID == 2911) || (linkID == 21011) || (linkID
== 21112) || // Scenairo 2 Fixed and Dynamic weights
1042     (linkID == 323) || (linkID == 334) || (linkID == 356) || (linkID == 367) || (linkID
== 348) || // Scenairo 3 Fixed and Dynamic weights
1043     (linkID == 379) || (linkID == 31013) || (linkID == 31113) || (linkID
== 31213) || (linkID == 31314)) // Scenairo 3 Fixed and Dynamic weights
1044 {
1045     CalculateBucket(*res, *(res + 1), *(res + 2));
1046 }
1047 }
1048
1049 // Calculate fixed scheduling rates function for service classes in the core
routers for DiffServ domains
1050 float* RDWQueue::CalculateSchedulingRateFixed(float rates[3])
1051 {
1052
1053     // calculate the estimated average queue length for service classes
1054     // queue length = (1-row)*Qlength(k-1) + row * Qlength(k)
1055     if (sc1ByteLength == 0 || m_packets1.size() == 0)
1056     {
1057         SC1CurrUsage = 0;
1058         SC1Usage.push_back(0);
1059         SC1CurreUsage.push_back(0);
1060         SC1PrevUsage.push_back(0);
1061     }
1062
1063     else
1064     {
1065         double SC1CurrUsage =
1066             (1 -
Case1_conf::ROWFORAVGQLENGTH)*SC1PrevUsage[SC1PrevUsage.size()-2] + (Case1_conf::ROWFORAVGQLENGTH*sc1ByteLength);
1067         SC1Usage.push_back(sc1ByteLength);
1068         SC1CurreUsage.push_back(SC1CurrUsage);
1069         SC1PrevUsage.push_back(SC1CurrUsage);
1070     }
1071     NS_LOG_INFO("SC1 Average Queue Length" << " " << linkID << " " << "Instantenous SC1
Length:" << " " << SC1Usage.back() << " " << "Previous SC1 Average Length:" << "

```



```

1072     "<<SC1PrevUsage[SC1PrevUsage.size()-2]<<" "<<"Average SC1 Length:"<<"
1073     "<<SC1CurreUsage.back());
1074     if (sc2ByteLength==0 || m_packets2.size() == 0)
1075     {
1076         SC2CurrUsage=0;
1077         SC2Usage.push_back(0);
1078         SC2CurreUsage.push_back(0);
1079         SC2PrevUsage.push_back(0);
1080     }
1081     else
1082     {
1083         double SC2CurrUsage =
1084             (1 -
1085              Case1_conf::ROWFORAVGQLENGTH)*SC2PrevUsage[SC2PrevUsage.size()-2]+(Case1_conf::ROWFORAVGQLENGTH*sc2ByteLength);
1086         SC2Usage.push_back(sc2ByteLength);
1087         SC2CurreUsage.push_back(SC2CurrUsage);
1088         SC2PrevUsage.push_back(SC2CurrUsage);
1089     }
1090     NS_LOG_INFO("SC2 Average Queue Length"<<" "<<linkID<<" "<<"Instantenous SC2
1091     Length:"<<" "<<SC2Usage.back()<<" "<<"Previous SC2 Average Length:"<<"
1092     "<<SC2PrevUsage[SC2PrevUsage.size()-2]<<" "<<"Average SC2 Length:"<<"
1093     "<<SC2CurreUsage.back());
1094
1095     if (sc3ByteLength==0 || m_packets3.size() == 0)
1096     {
1097         SC3CurrUsage=0;
1098         SC3Usage.push_back(0);
1099         SC3CurreUsage.push_back(0);
1100         SC3PrevUsage.push_back(0);
1101     }
1102     else
1103     {
1104         double SC3CurrUsage =
1105             (1 -
1106              Case1_conf::ROWFORAVGQLENGTH)*SC3PrevUsage[SC3PrevUsage.size()-2]+(Case1_conf::ROWFORAVGQLENGTH*sc3ByteLength);
1107         SC3Usage.push_back(sc3ByteLength);
1108         SC3CurreUsage.push_back(SC3CurrUsage);
1109         SC3PrevUsage.push_back(SC3CurrUsage);
1110     }
1111     NS_LOG_INFO("SC3 Average Queue Length"<<" "<<linkID<<" "<<"Instantenous SC3
1112     Length:"<<" "<<SC3Usage.back()<<" "<<"Previous SC3 Average Length:"<<"
1113     "<<SC3PrevUsage[SC3PrevUsage.size()-2]<<" "<<"Average SC3 Length:"<<"
1114     "<<SC3CurreUsage.back());
1115
1116     NS_LOG_INFO("Average Queue Length Report " << linkID << " "
1117     "<<SC1CurreUsage.back()<<" "<< SC2CurreUsage.back()<<"
1118     "<<SC3CurreUsage.back());
1119
1120     // calculate the delay per packet in each class
1121
1122     // store all the packet delay for later use
1123     std::list<uint64_t> SC1CurrDelay;
1124     for (std::map<uint64_t, Time>::iterator iter = SC1DequeueTime.begin(); iter !=
1125     SC1DequeueTime.end(); iter++) {
1126         uint64_t delay = (iter->second).GetMilliseconds() -
1127         SC1EnqueueTime[iter->first].GetMilliseconds();

```

```

1117         if (delay < 3600000) {
1118             SC1CurrDelay.push_back(delay);
1119             SC1Delay.push_back(delay);
1120             SC1EnqueueTime.erase(iter->first);
1121             SC1DequeueTime.erase(iter->first);
1122         }
1123     }
1124     SC1DequeueTime.clear();
1125     // calculate the average delay for service class queues during this k
1126     float SC1AVGDELAY = 0;
1127     for (std::list<uint64_t>::iterator iter = SC1CurrDelay.begin(); iter != SC1CurrDelay.end(); iter++) {
1128         SC1AVGDELAY += *iter;
1129     }
1130     SC1AVGDELAY = SC1AVGDELAY / SC1CurrDelay.size();
1131     if (std::isnan(SC1AVGDELAY))
1132     {
1133         SC1AVGDELAY=0;
1134     }
1135     SC1CurreDelay.push_back(SC1AVGDELAY);
1136     SC1CurrDelay.clear();
1137
1138     std::list<uint64_t> SC2CurrDelay;
1139     for (std::map<uint64_t, Time>::iterator iter = SC2DequeueTime.begin(); iter != SC2DequeueTime.end(); iter++) {
1140         uint64_t delay = (iter->second).GetMilliseconds() - SC2EnqueueTime[iter->first].GetMilliseconds();
1141         if (delay < 3600000) {
1142             SC2CurrDelay.push_back(delay);
1143             SC2Delay.push_back(delay);
1144             SC2EnqueueTime.erase(iter->first);
1145             SC2DequeueTime.erase(iter->first);
1146         }
1147     }
1148     SC2DequeueTime.clear();
1149     float SC2AVGDELAY = 0;
1150     for (std::list<uint64_t>::iterator iter = SC2CurrDelay.begin(); iter != SC2CurrDelay.end(); iter++) {
1151         SC2AVGDELAY += *iter;
1152     }
1153     SC2AVGDELAY = SC2AVGDELAY / SC2CurrDelay.size();
1154     if (std::isnan(SC2AVGDELAY))
1155     {
1156         SC2AVGDELAY=0;
1157     }
1158
1159     SC2CurreDelay.push_back(SC2AVGDELAY);
1160     SC2CurrDelay.clear();
1161
1162     std::list<uint64_t> SC3CurrDelay;
1163     for (std::map<uint64_t, Time>::iterator iter = SC3DequeueTime.begin(); iter != SC3DequeueTime.end(); iter++) {
1164         uint64_t delay = (iter->second).GetMilliseconds() - SC3EnqueueTime[iter->first].GetMilliseconds();
1165         if (delay < 3600000) {
1166             SC3CurrDelay.push_back(delay);
1167             SC3Delay.push_back(delay);
1168             SC3EnqueueTime.erase(iter->first);
1169             SC3DequeueTime.erase(iter->first);
1170         }
1171     }

```



```

1172     SC3DequeTime.clear();
1173     float SC3AVGDELAY = 0;
1174     for (std::list<uint64_t>::iterator iter = SC3CurrDelay.begin(); iter !=
1175         SC3CurrDelay.end(); iter++) {
1176         SC3AVGDELAY += *iter;
1177     }
1178     SC3AVGDELAY = SC3AVGDELAY / SC3CurrDelay.size();
1179     if (std::isnan(SC3AVGDELAY))
1180     {
1181         SC3AVGDELAY=0;
1182     }
1183     SC3CurreDelay.push_back(SC3AVGDELAY);
1184     SC3CurrDelay.clear();
1185     NS_LOG_INFO("Average Queue Delay Report " << linkID << " "
1186         <<SC1CurreDelay.back()<<" " << SC2CurreDelay.back()<<"
1187         "<<SC3CurreDelay.back());
1188
1189     // Assigne fixed rates for service class queues at the core routers of the
1190     DiffServ domains.
1191     rates[0] = 5;//5
1192     rates[1] = 4;//4
1193     rates[2] = 3;//3
1194     NS_LOG_INFO("Queue Scheduling Rate Report " << linkID << " " <<rates[0]<<"
1195         "<<rates[1]<<" " <<rates[2]);
1196
1197     return rates;
1198 }
1199 // Calling the function of Calculating dynamic scheduling rates for service
1200 classes in the edge routers for DiffServ domains in bytes/sec.
1201
1202 float* RDWQueue::CalculateSchedulingRatePktType1(float rates[3])
1203 { // scheduling rate= length/delay , then gcd to bucket
1204     return CalculateSchedulingRateByte(rates);
1205 }
1206
1207 // Not Considered in the research
1208 /*float* RDWQueue::CalculateSchedulingRatePktType2(float rates[3])
1209 {
1210     rates = CalculateSchedulingRateByte(rates);
1211     // rate=length/delay, then bucket = rate/avg pkt_size
1212     float sclpktlth = 0;
1213     for (std::deque<Ptr<Packet> >::iterator b = m_packets1.begin(); b !=
1214         m_packets1.end(); b++) {
1215         sclpktlth += (*b)->GetSize();
1216     }
1217     sclpktlth = sclpktlth / m_packets1.size();
1218
1219     float sc2pktlth = 0;
1220     for (std::deque<Ptr<Packet> >::iterator b = m_packets2.begin(); b !=
1221         m_packets2.end(); b++) {
1222         sc2pktlth += (*b)->GetSize();
1223     }
1224     sc2pktlth = sc2pktlth / m_packets2.size();
1225
1226     float sc3pktlth = 0;
1227     for (std::deque<Ptr<Packet> >::iterator b = m_packets3.begin(); b !=
1228         m_packets3.end(); b++) {
1229         sc3pktlth += (*b)->GetSize();
1230     }
1231     sc3pktlth = sc3pktlth / m_packets3.size();
1232 }

```

```

1224         sc3pktlth = sc3pktlth / m_packets3.size();
1225
1226         rates[0] = rates[0] / sc1pktlth;
1227         rates[1] = rates[1] / sc2pktlth;
1228         rates[2] = rates[2] / sc3pktlth;
1229         return rates;
1230     }*/
1231
1232     // Calculate dynamic scheduling rates function for service classes in the edge routers for DiffServ domains
1233
1234     float* RDWQueue::CalculateSchedulingRateByte(float rates[3])
1235     {
1236         // calculate the estimated average queue length for service classes
1237
1238         // queue length = (1-row)*Qlength(k-1) + row * Qlength(k)
1239         if (sc1ByteLength==0 || m_packets1.size() == 0)
1240         {
1241             SC1CurrUsage=0;
1242             SC1Usage.push_back(0);
1243             SC1CurreUsage.push_back(0);
1244             SC1PrevUsage.push_back(0);
1245             if (linkID == 268 || linkID == 269 || linkID == 2610 || // scenairo 2
1246                 Dynamic and Average of Average
1247                 linkID == 3810 || linkID == 3811 || linkID == 3812 || // scenairo 3
1248                 Dynamic and Average of Average
1249                 linkID == 3910 || linkID == 3911 || linkID == 3912) // scenairo 3
1250                 Dynamic and Average of Average
1251             {
1252                 UpdateAverageQueueLength1(1,SC1CurreUsage.back());
1253                 UpdateAverageQueueLength2(1,SC1CurreUsage.back());
1254             }
1255         }
1256         else
1257         {
1258             SC1CurrUsage =
1259             (1 -
1260             Case1_conf::ROWFORAVGQLENGTH)*SC1PrevUsage[SC1PrevUsage.size()-2]+(Case1_conf::ROWFORAVGQLENGTH*sc1ByteLength);
1261             SC1Usage.push_back(sc1ByteLength); // display instantenous values.
1262             SC1CurreUsage.push_back(SC1CurrUsage);
1263             SC1PrevUsage.push_back(SC1CurrUsage);
1264             if (linkID == 268 || linkID == 269 || linkID == 2610 || // scenairo 2 Dynamic
1265                 and Average of Average
1266                 linkID == 3810 || linkID == 3811 || linkID == 3812 || // scenairo 3
1267                 Dynamic and Average of Average
1268                 linkID == 3910 || linkID == 3911 || linkID == 3912) // scenairo 3 Dynamic
1269                 and Average of Average
1270             {
1271                 UpdateAverageQueueLength1(1,SC1CurreUsage.back());
1272                 UpdateAverageQueueLength2(1,SC1CurreUsage.back());
1273             }
1274         }
1275
1276         NS_LOG_INFO("SC1 Average Queue Length"<<" "<<linkID<<" "<<"Instantenous SC1
1277         Length:"<<" "<<SC1Usage.back()<<" "<<"Previous SC1 Average Length:"<<"
1278         "<<SC1PrevUsage[SC1PrevUsage.size()-2]<<" "<<"Average SC1 Length:"<<"

```

```

1273     "<<SC1CurreUsage.back());
1274
1275     if (sc2ByteLength==0 || m_packets2.size() == 0)
1276     {
1277         SC2CurrUsage=0;
1278         SC2Usage.push_back(0);
1279         SC2CurreUsage.push_back(0);
1280         SC2PrevUsage.push_back(0);
1281         if (linkID == 268 || linkID == 269 || linkID == 2610 || // scenairo 2
1282             Dynamic and Average of Average
1283             linkID == 3810 || linkID == 3811 || linkID == 3812 || // scenairo 3
1284             Dynamic and Average of Average
1285             linkID == 3910 || linkID == 3911 || linkID == 3912) // scenairo 3
1286             Dynamic and Average of Average
1287         {
1288             UpdateAverageQueueLength1(2,SC2CurreUsage.back());
1289             UpdateAverageQueueLength2(2,SC2CurreUsage.back());
1290         }
1291     }
1292
1293     else
1294     {
1295         SC2CurrUsage =
1296         (1 -
1297         Case1_conf::ROWFORAVGQLENGTH)*SC2PrevUsage[SC2PrevUsage.size()-2]+(Case1_conf::ROWFORAVGQLENGTH*sc2ByteLength);
1298         SC2Usage.push_back(sc2ByteLength);
1299         SC2CurreUsage.push_back(SC2CurrUsage);
1300         SC2PrevUsage.push_back(SC2CurrUsage);
1301         if (linkID == 268 || linkID == 269 || linkID == 2610 || // scenairo 2 Dynamic
1302             and Average of Average
1303             linkID == 3810 || linkID == 3811 || linkID == 3812 || // scenairo 3
1304             Dynamic and Average of Average
1305             linkID == 3910 || linkID == 3911 || linkID == 3912) // scenairo 3 Dynamic
1306             and Average of Average
1307         {
1308             UpdateAverageQueueLength1(2,SC2CurreUsage.back());
1309             UpdateAverageQueueLength2(2,SC2CurreUsage.back());
1310         }
1311     }
1312
1313     NS_LOG_INFO("SC2 Average Queue Length"<<" "<<linkID<<" "<<"Instantenous SC2
1314     Length:"<<" "<<SC2Usage.back()<<" "<<"Previous SC2 Average Length:"<<"
1315     "<<SC2PrevUsage[SC2PrevUsage.size()-2]<<" "<<"Average SC2 Length:"<<"
1316     "<<SC2CurreUsage.back());
1317
1318     if (sc3ByteLength==0 || m_packets3.size() == 0)
1319     {
1320         SC3CurrUsage=0;
1321         SC3Usage.push_back(0);
1322         SC3CurreUsage.push_back(0);
1323         SC3PrevUsage.push_back(0);
1324         if (linkID == 268 || linkID == 269 || linkID == 2610 || // scenairo 2
1325             Dynamic and Average of Average
1326             linkID == 3810 || linkID == 3811 || linkID == 3812 || // scenairo 3
1327             Dynamic and Average of Average
1328             linkID == 3910 || linkID == 3911 || linkID == 3912) // scenairo 3
1329             Dynamic and Average of Average
1330         {
1331             UpdateAverageQueueLength1(3,SC3CurreUsage.back());

```



```

1319         UpdateAverageQueueLength2(3, SC3CurrUsage.back());
1320     }
1321
1322 }
1323
1324 else
1325 {
1326     SC3CurrUsage =
1327     (1 -
1328         Case1_conf::ROWFORAVGQLENGTH)*SC3PrevUsage[SC3PrevUsage.size()-2]+(Case1_conf::ROWFORAVGQLENGTH*sc3ByteLength);
1329     SC3Usage.push_back(sc3ByteLength);
1330     SC3CurrUsage.push_back(SC3CurrUsage);
1331     SC3PrevUsage.push_back(SC3CurrUsage);
1332
1333     // For routers with multi out ports
1334     if (linkID == 268 || linkID == 269 || linkID == 2610 || // scenairo 2 Dynamic
1335         and Average of Average
1336         linkID == 3810 || linkID == 3811 || linkID == 3812 || // scenairo 3
1337         Dynamic and Average of Average
1338         linkID == 3910 || linkID == 3911 || linkID == 3912) // scenairo 3 Dynamic
1339         and Average of Average
1340     {
1341         UpdateAverageQueueLength1(3, SC3CurrUsage.back());
1342         UpdateAverageQueueLength2(3, SC3CurrUsage.back());
1343     }
1344
1345     // To display the average queue length
1346     NS_LOG_INFO("SC3 Average Queue Length"<<" "<<linkID<<" "<<"Instantenous SC3
1347     Length:"<<" "<<SC3Usage.back()<<" "<<"Previous SC3 Average Length:"<<"
1348     "<<SC3PrevUsage[SC3PrevUsage.size()-2]<<" "<<"Average SC3 Length:"<<"
1349     "<<SC3CurrUsage.back());
1350
1351     NS_LOG_INFO("Average Queue Length Report " << linkID << " "
1352     <<SC1CurrUsage.back()<<" "<< SC2CurrUsage.back()<<"
1353     "<<SC3CurrUsage.back());
1354
1355     // calculate the delay per packet in each class
1356
1357     // store all the packet delay for later use
1358     std::list<uint64_t> SC1CurrDelay;
1359     for (std::map<uint64_t, Time>::iterator iter = SC1DequeueTime.begin(); iter !=
1360     SC1DequeueTime.end(); iter++) {
1361         uint64_t delay = (iter->second).GetMilliseconds() -
1362         SC1EnqueueTime[iter->first].GetMilliseconds();
1363         if (delay < 3600000 ) {
1364             SC1CurrDelay.push_back(delay);
1365             SC1Delay.push_back(delay);
1366             SC1EnqueueTime.erase(iter->first);
1367             SC1DequeueTime.erase(iter->first);
1368         }
1369     }
1370     SC1DequeueTime.clear();
1371     // calculate the average delay for each service class queue during this k
1372     float SC1AVGDELAY = 0;
1373     for (std::list<uint64_t>::iterator iter = SC1CurrDelay.begin(); iter !=
1374     SC1CurrDelay.end(); iter++) {
1375         SC1AVGDELAY += *iter;
1376     }

```

```

1367     }
1368     SC1AVGDELAY = SC1AVGDELAY / SC1CurrDelay.size();
1369     if (std::isnan(SC1AVGDELAY) || std::isinf(SC1AVGDELAY))
1370     {
1371         SC1AVGDELAY=0;
1372     }
1373     SC1CurreDelay.push_back(SC1AVGDELAY); // to store average queue delay value
1374
1375     // For routers with multi out ports
1376     if (linkID == 268 || linkID == 269 || linkID == 2610 ||
1377         linkID == 3810 || linkID == 3811 || linkID == 3812 ||
1378         linkID == 3910 || linkID == 3911 || linkID == 3912)
1379     {
1380         UpdateAverageQueueDelay(1,SC1CurreDelay.back());
1381     }
1382     SC1CurrDelay.clear();
1383
1384
1385     std::list<uint64_t> SC2CurrDelay;
1386     for (std::map<uint64_t, Time>::iterator iter = SC2DequeueTime.begin(); iter !=
1387         SC2DequeueTime.end(); iter++) {
1388         uint64_t delay = (iter->second).GetMilliseconds() -
1389             SC2EnqueueTime[iter->first].GetMilliseconds();
1390         if (delay < 3600000 ) {
1391             SC2CurrDelay.push_back(delay);
1392             SC2Delay.push_back(delay);
1393             SC2EnqueueTime.erase(iter->first);
1394             SC2DequeueTime.erase(iter->first);
1395         }
1396     }
1397     SC2DequeueTime.clear();
1398     float SC2AVGDELAY = 0;
1399     for (std::list<uint64_t>::iterator iter = SC2CurrDelay.begin(); iter !=
1400         SC2CurrDelay.end(); iter++) {
1401         SC2AVGDELAY += *iter;
1402     }
1403     SC2AVGDELAY = SC2AVGDELAY / SC2CurrDelay.size();
1404     if (std::isnan(SC2AVGDELAY) || std::isinf(SC2AVGDELAY))
1405     {
1406         SC2AVGDELAY=0;
1407     }
1408     SC2CurreDelay.push_back(SC2AVGDELAY);
1409
1410     // For routers with multi out ports
1411     if (linkID == 268 || linkID == 269 || linkID == 2610 ||
1412         linkID == 3810 || linkID == 3811 || linkID == 3812 ||
1413         linkID == 3910 || linkID == 3911 || linkID == 3912)
1414     {
1415         UpdateAverageQueueDelay(2,SC2CurreDelay.back());
1416     }
1417     SC2CurrDelay.clear();
1418
1419     std::list<uint64_t> SC3CurrDelay;
1420     for (std::map<uint64_t, Time>::iterator iter = SC3DequeueTime.begin(); iter !=
1421         SC3DequeueTime.end(); iter++) {
1422         uint64_t delay = (iter->second).GetMilliseconds() -
1423             SC3EnqueueTime[iter->first].GetMilliseconds();
1424         if (delay < 36000000 ) {
1425             SC3CurrDelay.push_back(delay);

```

```

1423         SC3Delay.push_back(delay);
1424         SC3EnqueueTime.erase(iter->first);
1425         SC3DequeueTime.erase(iter->first);
1426     }
1427 }
1428 SC3DequeueTime.clear();
1429 float SC3AVGDELAY = 0;
1430 for (std::list<uint64_t>::iterator iter = SC3CurrDelay.begin(); iter !=
SC3CurrDelay.end(); iter++) {
1431     SC3AVGDELAY += *iter;
1432 }
1433 SC3AVGDELAY = SC3AVGDELAY / SC3CurrDelay.size();
1434 if (std::isnan(SC3AVGDELAY) || std::isinf(SC3AVGDELAY))
1435 {
1436     SC3AVGDELAY=0;
1437 }
1438 SC3CurreDelay.push_back(SC3AVGDELAY); // to store the average queue delay value
1439
1440 // For routers with multi out ports
1441 if (linkID == 268 || linkID == 269 || linkID == 2610 ||
1442     linkID == 3810 || linkID == 3811 || linkID == 3812 ||
1443     linkID == 3910 || linkID == 3911 || linkID == 3912)
1444 {
1445     UpdateAverageQueueDelay(3,SC3CurreDelay.back());
1446 }
1447 SC3CurrDelay.clear();
1448
1449 NS_LOG_INFO("Average Queue Delay Report " << linkID << " "
<<SC1CurreDelay.back()<<" " << SC2CurreDelay.back()<<"
"<<SC3CurreDelay.back());
1450
1451 // Calculating dynamic scheduling rates for service classes within an interval k
1452 if (SC1CurreDelay.back() !=0 && SC2CurreDelay.back() !=0 &&
SC3CurreDelay.back() !=0 && SC1CurreUsage.back() !=0 && SC2CurreUsage.back() !=0 &&
SC3CurreUsage.back() !=0) //1
1453 {
1454     SC1ScheRate = SC1CurreUsage.back()/(SC1CurreDelay.back()/1000) ;
1455
1456 // SC 2 scheduling Rate = 2 * r1 * q2 / q1
1457 SC2ScheRate = 0.5 * SC1ScheRate * SC2CurreUsage.back()/SC1CurreUsage.back();
1458
1459 // SC 3 scheduling Rate = 4* r1 * q3 / q1
1460 SC3ScheRate = 0.25 *SC1ScheRate * SC3CurreUsage.back()/SC1CurreUsage.back();
1461
1462 }
1463
1464 else if (SC1CurreDelay.back()==0 || SC1CurreUsage.back()==0) //2
1465 {
1466     SC1ScheRate=0;
1467     SC2ScheRate=SC2CurreUsage.back()/(SC2CurreDelay.back()/1000);
1468     SC3ScheRate=0.5*SC2ScheRate*(SC3CurreUsage.back()/SC2CurreUsage.back());
1469
1470 }
1471
1472 else if ((SC1CurreDelay.back()==0 || SC1CurreUsage.back()==0) &&
(SC2CurreUsage.back()==0 ||SC2CurreDelay.back()==0 )) //3
1473 {
1474     SC1ScheRate=0;
1475     SC2ScheRate=0;
1476     SC3ScheRate = SC3CurreUsage.back()/(SC3CurreDelay.back()/1000);
1477

```



```

1478     }
1479     else if ((SC1CurreDelay.back()==0 || SC1CurreUsage.back()==0) &&
1480             (SC3CurreDelay.back()==0 || SC3CurreUsage.back()==0)) //4
1481     {
1482         SC1ScheRate=0;
1483         SC2ScheRate = SC2CurreUsage.back()/(SC2CurreDelay.back()/1000);
1484         SC3ScheRate=0;
1485     }
1486     else if ((SC2CurreDelay.back()==0 || SC2CurreUsage.back()==0) &&
1487             (SC3CurreDelay.back() ==0 || SC3CurreUsage.back()==0)) //5
1488     {
1489         SC1ScheRate = SC1CurreUsage.back()/(SC1CurreDelay.back()/1000);
1490         SC2ScheRate=0;
1491         SC3ScheRate=0;
1492     }
1493     else if (SC2CurreDelay.back()==0 || SC2CurreUsage.back()==0 ) //6
1494     {
1495         SC1ScheRate = SC1CurreUsage.back() / (SC1CurreDelay.back()/1000);
1496         SC2ScheRate=0;
1497         SC3ScheRate = 0.5 * SC1ScheRate * SC3CurreUsage.back()/SC1CurreUsage.back();
1498     }
1499
1500
1501     else if (SC3CurreDelay.back()==0 || SC3CurreUsage.back()==0) //7
1502     {
1503         SC1ScheRate = SC1CurreUsage.back()/(SC1CurreDelay.back()/1000);
1504         SC2ScheRate = 0.5*SC1ScheRate*(SC2CurreUsage.back()/SC1CurreUsage.back());
1505         SC3ScheRate=0;
1506     }
1507
1508     else if ((SC1CurreDelay.back()==0 && SC1CurreUsage.back()==0) &&
1509             (SC2CurreDelay.back()==0 && SC2CurreUsage.back()==0) &&
1510             (SC3CurreDelay.back()==0 && SC3CurreUsage.back()==0)) //8
1511     {
1512         SC1ScheRate =0;
1513         SC2ScheRate =0;
1514         SC3ScheRate =0;
1515     }
1516
1517     if (std::isnan(SC1ScheRate) || std::isinf(SC1ScheRate))
1518     {
1519         SC1ScheRate=0;
1520     }
1521     else if (std::isnan(SC2ScheRate) || std::isinf(SC2ScheRate))
1522     {
1523         SC2ScheRate=0;
1524     }
1525     else if (std::isnan(SC3ScheRate) || std::isinf(SC3ScheRate))
1526     {
1527         SC3ScheRate=0;
1528     }
1529
1530     NS_LOG_INFO("Queue Scheduling Rate Report " << linkID << " " <<SC1ScheRate<<"
1531                "<< SC2ScheRate<<" "<<SC3ScheRate);
1532
1533     NS_LOG_INFO("CalculateSchedulingRate " << linkID << " " << SC1ScheRate << " "
1534                "<< SC2ScheRate << " " << SC3ScheRate
1535                << " " << SC1CurreUsage.back()<< " " <<
1536                SC2CurreUsage.back()<< " "

```

```

1532                                     <<SC3CurreUsage.back());
1533
1534 if ((linkID == 134) || (linkID ==156) || (linkID == 168) || (linkID == 1910) || 2
//scenairo 1 Dynamic scheduling rate calculation
1535     (linkID == 234)|| (linkID == 256)|| (linkID ==21112)|| //scenairo 2 2
Dynamic scheduling rate calculation
1536     (linkID == 323) || (linkID ==356) || (linkID == 348)|| // scenairo 3 2
Dynamic scheduling rate calculation
1537     (linkID == 379) || (linkID == 31314)) // scenairo 3 Dynamic scheduling 2
rate calculation
1538 {
1539     rates[0] = SC1ScheRate;
1540     rates[1] = SC2ScheRate;
1541     rates[2] = SC3ScheRate;
1542 }
1543
1544 // For routers with multi out ports
1545
1546 else if ((linkID == 268) || (linkID == 269) || (linkID ==2610)||
1547     (linkID == 3810) || (linkID == 3811) || (linkID ==3812)||
1548     (linkID == 3910) || (linkID == 3911) || (linkID ==3912)) // 2
scenairo 2 set zero to sceduling rate
1549 {
1550     rates[0] = 0;
1551     rates[1] = 0;
1552     rates[2] = 0;
1553 }
1554
1555 return rates;
1556 }
1557
1558 // Calculate Bucket function
1559 bool RDWQueue::CalculateBucket(float SC1ScheRate, float SC2ScheRate, float 2
SC3ScheRate)
1560 { float r1,r2,r3;
1561     NS_LOG_INFO("NEW SCHEDULE RATE VALUE " << linkID << " " << SC1ScheRate << " " 2
<< SC2ScheRate << " " << SC3ScheRate);
1562     if (SC1ScheRate==0 && SC2ScheRate==0 && SC3ScheRate==0)
1563     {
1564         r1=r2=r3=0;
1565     }
1566     else
1567     {
1568         r1 = SC1ScheRate / (SC1ScheRate + SC2ScheRate + SC3ScheRate);
1569         r2 = SC2ScheRate / (SC1ScheRate + SC2ScheRate + SC3ScheRate);
1570         r3 = SC3ScheRate / (SC1ScheRate + SC2ScheRate + SC3ScheRate);
1571
1572         SC1ScheRate = r1 / (r1 + r2 + r3);
1573         SC2ScheRate = r2 / (r1 + r2 + r3);
1574         SC3ScheRate = r3 / (r1 + r2 + r3);
1575     }
1576     NS_LOG_INFO("NEW SCHEDULE RATE NORMALIZED VALUE " << linkID << " " << 2
SC1ScheRate << " " << SC2ScheRate << " "
1577                                     << SC3ScheRate);
1578     // gcd
1579     int i1 = (int)(SC1ScheRate * 100);
1580     int i2 = (int)(SC2ScheRate * 100);
1581     int i3 = (int)(SC3ScheRate * 100);
1582     int gcd = 0;
1583

```



```

1584
1585 double sc1pktlth=0, sc2pktlth=0, sc3pktlth=0;
1586
1587 // For upstream links
1588 if ((linkID == 134) || //Scenairo 1 dynamic calculate weight according to Link capacity
1589     (linkID == 234) || //Scenairo 2 dynamic calculate weight according to Link capacity
1590     (linkID == 323) || (linkID == 356) ) //Scenairo 3 dynamic calculate weight according to Link capacity // Scenairo 3 dynamic calculate weight according to Link capacity
1591 {
1592
1593     if (i1 == 0) {
1594         if (i2 == 0) {
1595             if (i3 == 0) {
1596                 // 0,0,0 //1
1597                 gcd = 1;
1598                 SC1BUCKETS =0;
1599                 SC2BUCKETS =0;
1600                 SC3BUCKETS =0;
1601                 sc1pktlth=0;
1602                 sc2pktlth=0;
1603                 sc3pktlth=0;
1604                 std::cout<<"stat 1"<<" "<<linkID<<"
1605                     "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
1606                     "<<"sc1pktlth"<<" "<<"sc2pktlth"<<" "<<"sc2pktlth"<<"
1607                     "<<"sc3pktlth"<<" "<<"sc3pktlth"<<" "<<"i1"<<" "<<i1<<"i2"<<"
1608                     "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
1609
1610             } else if (i3 !=0){
1611                 // 0,0,1 //2
1612                 gcd = i3;
1613                 sc1pktlth=0;
1614                 sc2pktlth=0;
1615                 for (std::deque<Ptr<Packet> >::iterator c = m_packets3.begin(); c
1616                     != m_packets3.end(); c++) {
1617                     sc3pktlth += (*c)->GetSize();
1618                 }
1619                 sc3pktlth = sc3pktlth / m_packets3.size();
1620                 SC1BUCKETS =0;
1621                 SC2BUCKETS =0;
1622                 SC3BUCKETS =( Case1_conf::UpstreamLINKCAPACITY)/(8*sc3pktlth);//350
1623
1624                 std::cout<<"stat 2"<<" "<<linkID<<"
1625                     "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
1626                     "<<"sc1pktlth"<<" "<<"sc2pktlth"<<" "<<"sc2pktlth"<<"
1627                     "<<"sc3pktlth"<<" "<<"sc3pktlth"<<" "<<"i1"<<" "<<i1<<"i2"<<"
1628                     "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
1629
1630             }
1631         } else {
1632             if (i3 == 0) {
1633                 // 0,1,0 //3
1634                 gcd = i2;
1635                 sc1pktlth=0;
1636                 for (std::deque<Ptr<Packet> >::iterator b = m_packets2.begin(); b
1637                     != m_packets2.end(); b++) {
1638                     sc2pktlth += (*b)->GetSize();
1639                 }
1640                 sc2pktlth = sc2pktlth / m_packets2.size();

```

```

1631         sc3pktlth =0;
1632         SC1BUCKETS =0;
1633         SC2BUCKETS =(Case1_conf::UpstreamLINKCAPACITY)/(8*sc2pktlth);//1400
1634         SC3BUCKETS =0;
1635         std::cout<<"stat 3"<<" "<<linkID<<"
            "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
            "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
            "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
            "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;

1636
1637     } else {
1638         // 0,1,1 //4
1639         gcd = calculateGCD(i2, i3);
1640         sc1pktlth=0;
1641         for (std::deque<Ptr<Packet> >::iterator b = m_packets2.begin();
            b != m_packets2.end(); b++) {
1642             sc2pktlth += (*b)->GetSize();
1643         }
1644         sc2pktlth = sc2pktlth / m_packets2.size();
1645         for (std::deque<Ptr<Packet> >::iterator c = m_packets3.begin(); c
            != m_packets3.end(); c++) {
1646             sc3pktlth += (*c)->GetSize();
1647         }
1648         sc3pktlth = sc3pktlth / m_packets3.size();
1649         SC1BUCKETS =0;
1650         SC2BUCKETS =((i2/gcd) *
            Case1_conf::UpstreamLINKCAPACITY)/(100*8*sc2pktlth);//1400
1651         SC3BUCKETS =((i3/gcd) *
            Case1_conf::UpstreamLINKCAPACITY)/(100*8*sc3pktlth);//350
1652         std::cout<<"stat 4"<<" "<<linkID<<"
            "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
            "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
            "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
            "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;

1653     }
1654 } //i1==1
1655 } else {
1656     if (i2 == 0) {
1657         if (i3 == 0) {
1658             // 1,0,0 //5
1659             std::cout<<"stat 5"<<" "<<linkID<<"
            "<<Simulator::Now()/1000000000<<" "<<"i1"<<" "<<i1<<"i2"<<"
            "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
1660
1661             gcd = i1;
1662             for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
            != m_packets1.end(); a++) {
1663                 sc1pktlth += (*a)->GetSize();
1664             }
1665             sc1pktlth = sc1pktlth / m_packets1.size();
1666             sc2pktlth=0;
1667             sc3pktlth=0;
1668             SC1BUCKETS =(Case1_conf::UpstreamLINKCAPACITY)/(8*sc1pktlth);
1669
1670             SC2BUCKETS =0;
1671             SC3BUCKETS =0;
1672             std::cout<<"stat 5"<<" "<<linkID<<"
            "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
            "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
            "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
            "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;

```

```

1673
1674
1675     } else {
1676         // 1,0,1 //6
1677         gcd = calculateGCD(i1, i3);
1678         for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
1679             != m_packets1.end(); a++) {
1680             sc1pktlth += (*a)->GetSize();
1681         }
1682         sc1pktlth = sc1pktlth / m_packets1.size();
1683         sc2pktlth=0;
1684         for (std::deque<Ptr<Packet> >::iterator c = m_packets3.begin(); c
1685             != m_packets3.end(); c++) {
1686             sc3pktlth += (*c)->GetSize();
1687         }
1688         sc3pktlth = sc3pktlth / m_packets3.size();
1689         SC1BUCKETS = ((i1/gcd) *
1690             Case1_conf::UpstreamLINKCAPACITY)/(100*8*sc1pktlth);
1691         SC2BUCKETS =0;
1692         SC3BUCKETS = ((i3/gcd) *
1693             Case1_conf::UpstreamLINKCAPACITY)/(100*8*sc3pktlth);
1694         std::cout<<"stat 6"<<" "<<linkID<<"
1695             "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
1696             "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
1697             "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
1698             "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
1699     }
1700 } else {
1701     if (i3 == 0) {
1702         // 1,1,0 //7
1703         gcd = calculateGCD(i1, i2);
1704         for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
1705             != m_packets1.end(); a++) {
1706             sc1pktlth += (*a)->GetSize();
1707         }
1708         sc1pktlth = sc1pktlth / m_packets1.size();
1709         for (std::deque<Ptr<Packet> >::iterator b =
1710             m_packets2.begin(); b != m_packets2.end(); b++) {
1711             sc2pktlth += (*b)->GetSize();
1712         }
1713         sc2pktlth = sc2pktlth / m_packets2.size();
1714         sc3pktlth =0;
1715         SC1BUCKETS = ((i1/gcd) *
1716             Case1_conf::UpstreamLINKCAPACITY)/(100*8*sc1pktlth);
1717         SC2BUCKETS = ((i2/gcd) *
1718             Case1_conf::UpstreamLINKCAPACITY)/(100*8*sc2pktlth);
1719         SC3BUCKETS =0;
1720         std::cout<<"stat 7"<<" "<<linkID<<"
1721             "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
1722             "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
1723             "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
1724             "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
1725     } else {
1726         // 1,1,1 //8
1727         gcd = calculateGCD(calculateGCD(i1, i2), i3);
1728         for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
1729             != m_packets1.end(); a++) {
1730             sc1pktlth += (*a)->GetSize();
1731         }
1732     }
1733 }

```



```

1717     }
1718     sc1pktlth = sc1pktlth / m_packets1.size();
1719     for (std::deque<Ptr<Packet> >::iterator b =
1720           m_packets2.begin(); b != m_packets2.end(); b++) {
1721         sc2pktlth += (*b)->GetSize();
1722     }
1723     sc2pktlth = sc2pktlth / m_packets2.size();
1724     for (std::deque<Ptr<Packet> >::iterator c = m_packets3.begin(); c
1725           != m_packets3.end(); c++) {
1726         sc3pktlth += (*c)->GetSize();
1727     }
1728     sc3pktlth = sc3pktlth / m_packets3.size();
1729     SC1BUCKETS = ((i1/gcd) *
1730                  Case1_conf::UpstreamLINKCAPACITY)/(100*8*sc1pktlth);
1731     SC2BUCKETS = ((i2/gcd) *
1732                  Case1_conf::UpstreamLINKCAPACITY)/(100*8*sc2pktlth);
1733     SC3BUCKETS = ((i3/gcd) *
1734                  Case1_conf::UpstreamLINKCAPACITY)/(100*8*sc3pktlth);
1735     std::cout<<"stat 8"<<" "<<linkID<<"
1736               "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
1737               "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
1738               "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
1739               "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
1740 }
1741 }
1742 }
1743 NS_LOG_INFO("NEW SCHEDULE RATE BUCKETS " << linkID <<" "<<"
1744             "<<Case1_conf::UpstreamLINKCAPACITY<<" "<< gcd <<" "<< SC1BUCKETS <<"
1745             "<< SC2BUCKETS <<" "<< SC3BUCKETS);
1746 }
1747 //for downstream links
1748 else if ( (linkID ==168) || //Scenairo 1 dynamic calculate weight according to
1749 Link capacity
1750 (linkID ==268) || (linkID == 269) || (linkID == 2610) || //Scenairo 2 dynamic
1751 calculate weight according to Link capacity
1752 (linkID == 3810) || (linkID ==3811 ) || //Scenairo 3 dynamic calculate weight
1753 according to Link capacity
1754 (linkID ==3812) || (linkID ==3910) || (linkID ==3911)|| (linkID ==3912)) //
1755 Scenairo 3 dynamic calculate weight according to Link capacity
1756 {
1757 if (i1 == 0) {
1758     if (i2 == 0) {
1759         if (i3 == 0) {
1760             // 0,0,0 //1
1761             gcd = 1;
1762             SC1BUCKETS =0;
1763             SC2BUCKETS =0;
1764             SC3BUCKETS =0;
1765             sc1pktlth=0;
1766             sc2pktlth=0;
1767             sc3pktlth=0;
1768             std::cout<<"stat 1"<<" "<<linkID<<"
1769                   "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
1770                   "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
1771                   "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"

```

```

1760         "<<i2<<" "<<i3"<<" " <<i3<<std::endl;
1761     } else if (i3 !=0){
1762         // 0,0,1 //2
1763         gcd = i3;
1764         sc1pktlth=0;
1765         sc2pktlth=0;
1766         for (std::deque<Ptr<Packet> >::iterator c = m_packets3.begin(); c
1767             != m_packets3.end(); c++) {
1768             sc3pktlth += (*c)->GetSize();
1769         }
1770         sc3pktlth = sc3pktlth / m_packets3.size();
1771         SC1BUCKETS =0;
1772         SC2BUCKETS =0;
1773         SC3BUCKETS =( Case1_conf::DownstreamLINKCAPACITY)/(8*sc3pktlth);
1774
1775         std::cout<<"stat 2"<<" "<<linkID<<"
1776             "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
1777             "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
1778             "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<i1"<<" "<<i1<<"i2"<<"
1779             "<<i2<<" "<<i3"<<" " <<i3<<std::endl;
1780     }
1781 } else {
1782     if (i3 == 0) {
1783         // 0,1,0 //3
1784         gcd = i2;
1785         sc1pktlth=0;
1786         for (std::deque<Ptr<Packet> >::iterator b = m_packets2.begin(); b
1787             != m_packets2.end(); b++) {
1788             sc2pktlth += (*b)->GetSize();
1789         }
1790         sc2pktlth = sc2pktlth / m_packets2.size();
1791         sc3pktlth =0;
1792         SC1BUCKETS =0;
1793         SC2BUCKETS =(Case1_conf::DownstreamLINKCAPACITY)/(8*sc2pktlth);
1794
1795         SC3BUCKETS =0;
1796         std::cout<<"stat 3"<<" "<<linkID<<"
1797             "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
1798             "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
1799             "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<i1"<<" "<<i1<<"i2"<<"
1800             "<<i2<<" "<<i3"<<" " <<i3<<std::endl;
1801     } else {
1802         // 0,1,1 //4
1803         gcd = calculateGCD(i2, i3);
1804         sc1pktlth=0;
1805         for (std::deque<Ptr<Packet> >::iterator b = m_packets2.begin();
1806             b != m_packets2.end(); b++) {
1807             sc2pktlth += (*b)->GetSize();
1808         }
1809         sc2pktlth = sc2pktlth / m_packets2.size();
1810         for (std::deque<Ptr<Packet> >::iterator c = m_packets3.begin(); c
1811             != m_packets3.end(); c++) {
1812             sc3pktlth += (*c)->GetSize();
1813         }
1814         sc3pktlth = sc3pktlth / m_packets3.size();
1815         SC1BUCKETS =0;
1816         SC2BUCKETS =(i2/gcd) *
1817             Case1_conf::DownstreamLINKCAPACITY)/(100*8*sc2pktlth);

```

```

1807         SC3BUCKETS = ((i3/gcd) *
Case1_conf::DownstreamLINKCAPACITY)/(100*8*sc3pktlth);
1808         std::cout<<"stat 4"<<" "<<linkID<<"
"<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
"<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
"<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
"<<i2<<" "<<"i3"<<" "<<i3<<std::endl;

1809     }
1810 } //i1==1
1811 } else {
1812     if (i2 == 0) {
1813         if (i3 == 0) {
1814             // 1,0,0 //5
1815             std::cout<<"stat 5"<<" "<<linkID<<"
"<<Simulator::Now()/1000000000<<" "<<"i1"<<" "<<i1<<"i2"<<"
"<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
1816             gcd = i1;
1817             for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
!= m_packets1.end(); a++) {
1818                 sc1pktlth += (*a)->GetSize();
1819             }
1820             sc1pktlth = sc1pktlth / m_packets1.size();
1821             sc2pktlth=0;
1822             sc3pktlth=0;
1823             SC1BUCKETS = (Case1_conf::DownstreamLINKCAPACITY)/(8*sc1pktlth);
1824             SC2BUCKETS =0;
1825             SC3BUCKETS =0;
1826             std::cout<<"stat 5"<<" "<<linkID<<"
"<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
"<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
"<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
"<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
1827
1828         } else {
1829             // 1,0,1 //6
1830             gcd = calculateGCD(i1, i3);
1831             for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
!= m_packets1.end(); a++) {
1832                 sc1pktlth += (*a)->GetSize();
1833             }
1834             sc1pktlth = sc1pktlth / m_packets1.size();
1835             sc2pktlth=0;
1836             for (std::deque<Ptr<Packet> >::iterator c = m_packets3.begin(); c
!= m_packets3.end(); c++) {
1837                 sc3pktlth += (*c)->GetSize();
1838             }
1839             sc3pktlth = sc3pktlth / m_packets3.size();
1840             SC1BUCKETS = ((i1/gcd) *
Case1_conf::DownstreamLINKCAPACITY)/(100*8*sc1pktlth);
1841             SC2BUCKETS =0;
1842             SC3BUCKETS = ((i3/gcd) *
Case1_conf::DownstreamLINKCAPACITY)/(100*8*sc3pktlth);
1843             std::cout<<"stat 6"<<" "<<linkID<<"
"<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
"<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
"<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
"<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
1844
1845
1846
1847

```



```

1848     }
1849 } else {
1850     if (i3 == 0) {
1851         // 1,1,0 //7
1852         gcd = calculateGCD(i1, i2);
1853         for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
1854             != m_packets1.end(); a++) {
1855             sc1pktlth += (*a)->GetSize();
1856         }
1857         sc1pktlth = sc1pktlth / m_packets1.size();
1858         for (std::deque<Ptr<Packet> >::iterator b =
1859             m_packets2.begin(); b != m_packets2.end(); b++) {
1860             sc2pktlth += (*b)->GetSize();
1861         }
1862         sc2pktlth = sc2pktlth / m_packets2.size();
1863         sc3pktlth = 0;
1864         SC1BUCKETS = ((i1/gcd) *
1865             Case1_conf::DownstreamLINKCAPACITY)/(100*8*sc1pktlth);
1866         SC2BUCKETS = ((i2/gcd) *
1867             Case1_conf::DownstreamLINKCAPACITY)/(100*8*sc2pktlth);
1868         SC3BUCKETS = 0;
1869         std::cout<<"stat 7"<<" "<<linkID<<"
1870             "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
1871             "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
1872             "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
1873             "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
1874
1875     } else {
1876         // 1,1,1 //8
1877         gcd = calculateGCD(calculateGCD(i1, i2), i3);
1878         for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
1879             != m_packets1.end(); a++) {
1880             sc1pktlth += (*a)->GetSize();
1881         }
1882         sc1pktlth = sc1pktlth / m_packets1.size();
1883         for (std::deque<Ptr<Packet> >::iterator b =
1884             m_packets2.begin(); b != m_packets2.end(); b++) {
1885             sc2pktlth += (*b)->GetSize();
1886         }
1887         sc2pktlth = sc2pktlth / m_packets2.size();
1888         for (std::deque<Ptr<Packet> >::iterator c = m_packets3.begin(); c
1889             != m_packets3.end(); c++) {
1890             sc3pktlth += (*c)->GetSize();
1891         }
1892         sc3pktlth = sc3pktlth / m_packets3.size();
1893         SC1BUCKETS = ((i1/gcd) *
1894             Case1_conf::DownstreamLINKCAPACITY)/(100*8*sc1pktlth);
1895         SC2BUCKETS = ((i2/gcd) *
1896             Case1_conf::DownstreamLINKCAPACITY)/(100*8*sc2pktlth);
1897         SC3BUCKETS = ((i3/gcd) *
1898             Case1_conf::DownstreamLINKCAPACITY)/(100*8*sc3pktlth);
1899         std::cout<<"stat 8"<<" "<<linkID<<"
1900             "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
1901             "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
1902             "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
1903             "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
1904     }
1905 }

```

```

1891     }
1892     NS_LOG_INFO("NEW SCHEDULE RATE BUCKETS " << linkID << " " << "
    "<<Case1_conf::DownstreamLINKCAPACITY<< " " << gcd << " " << SC1BUCKETS <<
    " " << SC2BUCKETS << " "
1893                                     << SC3BUCKETS);
1894
1895 }
1896
1897 //for domains links
1898 else if (linkID == 156 || // Scnairo 1 calculte weight of link between upstream
and downstream domains
1899         linkID == 256 || // Scnairo 2 calculte weight of link between upstream
and downstream domains
1900         linkID == 348 || linkID == 379 ){ //Scnairo 3 calculte weight of
link between upstream and downstream domains
1901     if (i1 == 0) {
1902         if (i2 == 0) {
1903             if (i3 == 0) {
1904                 // 0,0,0 //1
1905                 gcd = 1;
1906                 sc1pktlth=0;
1907                 sc2pktlth=0;
1908                 sc3pktlth=0;
1909                 SC1BUCKETS =0;
1910                 SC2BUCKETS =0;
1911                 SC3BUCKETS =0;
1912                 std::cout<<"stat 1"<< " "<<linkID<< "
    "<<Simulator::Now()/1000000000<< " "<<"sc1pktlth"<< "
    "<<sc1pktlth<< " "<<"sc2pktlth"<< " "<<sc2pktlth<< "
    "<<"sc3pktlth"<< " "<<sc3pktlth<< " "<<"i1"<< " "<<i1<<"i2"<< "
    "<<i2<< " "<<"i3"<< " "<<i3<<std::endl;
1913
1914             } else {
1915                 // 0,0,1 //2
1916                 gcd = i3;
1917                 sc1pktlth=0;
1918                 sc2pktlth=0;
1919                 for (std::deque<Ptr<Packet> >::iterator c = m_packets3.begin(); c
    != m_packets3.end(); c++) {
1920                     sc3pktlth += (*c)->GetSize();
1921                 }
1922                 sc3pktlth = sc3pktlth / m_packets3.size();
1923                 SC1BUCKETS =0;
1924                 SC2BUCKETS =0;
1925                 SC3BUCKETS =(Case1_conf::DomainsLINKCAPACITY)/(8*sc3pktlth);
1926                 std::cout<<"stat 2"<< " "<<linkID<< "
    "<<Simulator::Now()/1000000000<< " "<<"sc1pktlth"<< "
    "<<sc1pktlth<< " "<<"sc2pktlth"<< " "<<sc2pktlth<< "
    "<<"sc3pktlth"<< " "<<sc3pktlth<< " "<<"i1"<< " "<<i1<<"i2"<< "
    "<<i2<< " "<<"i3"<< " "<<i3<<std::endl;
1927
1928             }
1929         } else {
1930             if (i3 == 0) {
1931                 // 0,1,0 //3
1932                 gcd = i2;
1933                 sc1pktlth=0;
1934                 for (std::deque<Ptr<Packet> >::iterator b = m_packets2.begin(); b
    != m_packets2.end(); b++) {
1935                     sc2pktlth += (*b)->GetSize();
1936                 }

```



```

1937         sc2pktlth = sc2pktlth / m_packets2.size();
1938         sc3pktlth =0;
1939         SC1BUCKETS =0;
1940         SC2BUCKETS =(Case1_conf::DomainsLINKCAPACITY)/(8*sc2pktlth);
1941         SC3BUCKETS =0;
1942         std::cout<<"stat 3"<<" "<<linkID<<"
            "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
            "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
            "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
            "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
1943
1944     } else {
1945         // 0,1,1 //4
1946         gcd = calculateGCD(i2, i3);
1947         sc1pktlth=0;
1948         for (std::deque<Ptr<Packet> >::iterator b = m_packets2.begin(); b
            != m_packets2.end(); b++) {
1949             sc2pktlth += (*b)->GetSize();
1950         }
1951         sc2pktlth = sc2pktlth / m_packets2.size();
1952         for (std::deque<Ptr<Packet> >::iterator c =
            m_packets3.begin(); c != m_packets3.end(); c++) {
1953             sc3pktlth += (*c)->GetSize();
1954         }
1955         sc3pktlth = sc3pktlth / m_packets3.size();
1956         SC1BUCKETS =0;
1957         SC2BUCKETS =(i2/gcd) *
            Case1_conf::DomainsLINKCAPACITY)/(100*8*sc2pktlth);
1958         SC3BUCKETS =(i3/gcd) *
            Case1_conf::DomainsLINKCAPACITY)/(100*8*sc3pktlth);
1959         std::cout<<"stat 4"<<" "<<linkID<<"
            "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
            "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
            "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
            "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
1960
1961     }
1962     } //i1==1
1963 } else {
1964     if (i2 == 0) {
1965         if (i3 == 0) {
1966             // 1,0,0 //5
1967             gcd = i1;
1968             for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
                != m_packets1.end(); a++) {
1969                 sc1pktlth += (*a)->GetSize();
1970             }
1971             sc1pktlth = sc1pktlth / m_packets1.size();
1972             sc2pktlth=0;
1973             sc3pktlth=0;
1974             SC1BUCKETS =( Case1_conf::DomainsLINKCAPACITY)/(8*sc1pktlth);
1975             SC2BUCKETS =0;
1976             SC3BUCKETS =0;
1977             std::cout<<"stat 5"<<" "<<linkID<<"
                "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
                "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
                "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
                "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
1978
1979
1980     } else {

```

```

1981         // 1,0,1 //6
1982         gcd = calculateGCD(i1, i3);
1983         for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
1984             != m_packets1.end(); a++) {
1985             sc1pktlth += (*a)->GetSize();
1986         }
1987         sc1pktlth = sc1pktlth / m_packets1.size();
1988         sc2pktlth=0;
1989         for (std::deque<Ptr<Packet> >::iterator c = m_packets3.begin(); c
1990             != m_packets3.end(); c++) {
1991             sc3pktlth += (*c)->GetSize();
1992         }
1993         sc3pktlth = sc3pktlth / m_packets3.size();
1994         SC1BUCKETS =((i1/gcd) *
1995             Case1_conf::DomainsLINKCAPACITY)/(100*8*sc1pktlth);
1996         SC2BUCKETS =0;
1997         SC3BUCKETS =((i3/gcd) *
1998             Case1_conf::DomainsLINKCAPACITY)/(100*8*sc3pktlth);
1999         std::cout<<"stat 6"<<" "<<linkID<<"
2000             "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
2001             "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
2002             "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
2003             "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
2004     }
2005 } else {
2006     if (i3 == 0) {
2007         // 1,1,0 //7
2008         gcd = calculateGCD(i1, i2);
2009         for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
2010             != m_packets1.end(); a++) {
2011             sc1pktlth += (*a)->GetSize();
2012         }
2013         sc1pktlth = sc1pktlth / m_packets1.size();
2014         for (std::deque<Ptr<Packet> >::iterator b =
2015             m_packets2.begin(); b != m_packets2.end(); b++) {
2016             sc2pktlth += (*b)->GetSize();
2017         }
2018         sc2pktlth = sc2pktlth / m_packets2.size();
2019         sc3pktlth =0;
2020         SC1BUCKETS =((i1/gcd) *
2021             Case1_conf::DomainsLINKCAPACITY)/(100*8*sc1pktlth);
2022         SC2BUCKETS =((i2/gcd) *
2023             Case1_conf::DomainsLINKCAPACITY)/(100*8*sc2pktlth);
2024         SC3BUCKETS =0;
2025         std::cout<<"stat 7"<<" "<<linkID<<"
2026             "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
2027             "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
2028             "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
2029             "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
2030     }
2031 } else {
2032     // 1,1,1 //8
2033     gcd = calculateGCD(calculateGCD(i1, i2), i3);
2034     for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
2035         != m_packets1.end(); a++) {
2036         sc1pktlth += (*a)->GetSize();
2037     }
2038     sc1pktlth = sc1pktlth / m_packets1.size();

```

```

2025         for (std::deque<Ptr<Packet> >::iterator b =
2026             m_packets2.begin(); b != m_packets2.end(); b++) {
2027             sc2pktlth += (*b)->GetSize();
2028         }
2029         sc2pktlth = sc2pktlth / m_packets2.size();
2030         for (std::deque<Ptr<Packet> >::iterator c =
2031             m_packets3.begin(); c != m_packets3.end(); c++) {
2032             sc3pktlth += (*c)->GetSize();
2033         }
2034         sc3pktlth = sc3pktlth / m_packets3.size();
2035         SC1BUCKETS = ((i1/gcd) *
2036             Case1_conf::DomainsLINKCAPACITY)/(100*8*sc1pktlth);
2037         SC2BUCKETS = ((i2/gcd) *
2038             Case1_conf::DomainsLINKCAPACITY)/(100*8*sc2pktlth);
2039         SC3BUCKETS = ((i3/gcd) *
2040             Case1_conf::DomainsLINKCAPACITY)/(100*8*sc3pktlth);
2041         std::cout<<"stat 8"<<" "<<linkID<<" "<<sc1pktlth<<"
2042             "<<sc1pktlth<<" "<<sc2pktlth<<" "<<sc2pktlth<<"
2043             "<<sc3pktlth<<" "<<sc3pktlth<<" "<<i1<<" "<<i1<<i2<<"
2044             "<<i2<<" "<<i3<<" "<<i3<<std::endl;
2045     }
2046 }
2047
2048 NS_LOG_INFO("NEW SCHEDULE RATE BUCKETS " << linkID <<" "<<"
2049     "<<Case1_conf::DomainsLINKCAPACITY<<" "<< gcd <<" "<< SC1BUCKETS <<" "<<
2050     SC2BUCKETS <<" "<< SC3BUCKETS);
2051
2052 }
2053 // For destinations links
2054 else if (linkID == 1910 || linkID == 2112 ||// Scenairo 2 weight of destination link
2055     linkID == 31314) { // Scenairo 3 weight of destination link
2056     if (i1 == 0) {
2057         if (i2 == 0) {
2058             if (i3 == 0) {
2059                 // 0,0,0 //1
2060                 gcd = 1;
2061                 sc1pktlth=0;
2062                 sc2pktlth=0;
2063                 sc3pktlth=0;
2064                 SC1BUCKETS =0;
2065                 SC2BUCKETS =0;
2066                 SC3BUCKETS =0;
2067                 std::cout<<"stat 1"<<" "<<linkID<<"
2068                     "<<Simulator::Now()/1000000000<<" "<<sc1pktlth<<"
2069                     "<<sc1pktlth<<" "<<sc2pktlth<<" "<<sc2pktlth<<"
2070                     "<<sc3pktlth<<" "<<sc3pktlth<<" "<<i1<<" "<<i1<<i2<<"
2071                     "<<i2<<" "<<i3<<" "<<i3<<std::endl;
2072             } else {
2073                 // 0,0,1 //2
2074                 gcd = i3;
2075                 sc1pktlth=0;
2076                 sc2pktlth=0;
2077                 for (std::deque<Ptr<Packet> >::iterator c = m_packets3.begin(); c
2078                     != m_packets3.end(); c++) {
2079                     sc3pktlth += (*c)->GetSize();

```



```

2070     }
2071     sc3pktlth = sc3pktlth / m_packets3.size();
2072     SC1BUCKETS =0;
2073     SC2BUCKETS =0;
2074     SC3BUCKETS =(Case1_conf::DestinationLINKCAPACITY)/(8*sc3pktlth);
2075     std::cout<<"stat 2"<<" "<<linkID<<"
        "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
        "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
        "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
        "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
2076
2077     }
2078 } else {
2079     if (i3 == 0) {
2080         // 0,1,0 //3
2081         gcd = i2;
2082         sc1pktlth=0;
2083         for (std::deque<Ptr<Packet> >::iterator b = m_packets2.begin(); b
        != m_packets2.end(); b++) {
2084             sc2pktlth += (*b)->GetSize();
2085         }
2086         sc2pktlth = sc2pktlth / m_packets2.size();
2087         sc3pktlth =0;
2088         SC1BUCKETS =0;
2089         SC2BUCKETS =(Case1_conf::DestinationLINKCAPACITY)/(8*sc2pktlth);
2090         SC3BUCKETS =0;
2091         std::cout<<"stat 3"<<" "<<linkID<<"
        "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
        "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
        "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
        "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
2092
2093     } else {
2094         // 0,1,1 //4
2095         gcd = calculateGCD(i2, i3);
2096         sc1pktlth=0;
2097         for (std::deque<Ptr<Packet> >::iterator b = m_packets2.begin(); b
        != m_packets2.end(); b++) {
2098             sc2pktlth += (*b)->GetSize();
2099         }
2100         sc2pktlth = sc2pktlth / m_packets2.size();
2101         for (std::deque<Ptr<Packet> >::iterator c =
        m_packets3.begin(); c != m_packets3.end(); c++) {
2102             sc3pktlth += (*c)->GetSize();
2103         }
2104         sc3pktlth = sc3pktlth / m_packets3.size();
2105         SC1BUCKETS =0;
2106         SC2BUCKETS =((i2/gcd) *
        Case1_conf::DestinationLINKCAPACITY)/(100*8*sc2pktlth);
2107         SC3BUCKETS =((i3/gcd) *
        Case1_conf::DestinationLINKCAPACITY)/(100*8*sc3pktlth);
2108         std::cout<<"stat 4"<<" "<<linkID<<"
        "<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
        "<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
        "<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
        "<<i2<<" "<<"i3"<<" "<<i3<<std::endl;
2109
2110     }
2111     } //i1==1
2112 } else {
2113     if (i2 == 0) {

```

```

2114         if (i3 == 0) {
2115             // 1,0,0 //5
2116             gcd = i1;
2117             for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
2118                 != m_packets1.end(); a++) {
2119                 sc1pktlth += (*a)->GetSize();
2120             }
2121             sc1pktlth = sc1pktlth / m_packets1.size();
2122             sc2pktlth=0;
2123             sc3pktlth=0;
2124             SC1BUCKETS =(Case1_conf::DestinationLINKCAPACITY)/(8*sc1pktlth);
2125             SC2BUCKETS =0;
2126             SC3BUCKETS =0;
2127             std::cout<<"stat 5"<<" "<<"linkID"<<"
2128                 "<<"Simulator::Now()/1000000000"<<" "<<"sc1pktlth"<<"
2129                 "<<"sc1pktlth"<<" "<<"sc2pktlth"<<" "<<"sc2pktlth"<<"
2130                 "<<"sc3pktlth"<<" "<<"sc3pktlth"<<" "<<"i1"<<" "<<"i1"<<"i2"<<"
2131                 "<<"i2"<<" "<<"i3"<<" "<<"i3"<<std::endl;
2132
2133         } else {
2134             // 1,0,1 //6
2135             gcd = calculateGCD(i1, i3);
2136             for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
2137                 != m_packets1.end(); a++) {
2138                 sc1pktlth += (*a)->GetSize();
2139             }
2140             sc1pktlth = sc1pktlth / m_packets1.size();
2141             sc2pktlth=0;
2142             for (std::deque<Ptr<Packet> >::iterator c = m_packets3.begin(); c
2143                 != m_packets3.end(); c++) {
2144                 sc3pktlth += (*c)->GetSize();
2145             }
2146             sc3pktlth = sc3pktlth / m_packets3.size();
2147             SC1BUCKETS =((i1/gcd) *
2148                 Case1_conf::DestinationLINKCAPACITY)/(100*8*sc1pktlth);
2149             SC2BUCKETS =0;
2150             SC3BUCKETS =((i3/gcd) *
2151                 Case1_conf::DestinationLINKCAPACITY)/(100*8*sc3pktlth);
2152             std::cout<<"stat 6"<<" "<<"linkID"<<"
2153                 "<<"Simulator::Now()/1000000000"<<" "<<"sc1pktlth"<<"
2154                 "<<"sc1pktlth"<<" "<<"sc2pktlth"<<" "<<"sc2pktlth"<<"
2155                 "<<"sc3pktlth"<<" "<<"sc3pktlth"<<" "<<"i1"<<" "<<"i1"<<"i2"<<"
2156                 "<<"i2"<<" "<<"i3"<<" "<<"i3"<<std::endl;
2157
2158         }
2159     } else {
2160         if (i3 == 0) {
2161             // 1,1,0 //7
2162             gcd = calculateGCD(i1, i2);
2163             for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
2164                 != m_packets1.end(); a++) {
2165                 sc1pktlth += (*a)->GetSize();
2166             }
2167             sc1pktlth = sc1pktlth / m_packets1.size();
2168             for (std::deque<Ptr<Packet> >::iterator b =
2169                 m_packets2.begin(); b != m_packets2.end(); b++) {
2170                 sc2pktlth += (*b)->GetSize();
2171             }
2172             sc2pktlth = sc2pktlth / m_packets2.size();
2173             sc3pktlth =0;

```

```

2160         SC1BUCKETS = ((i1/gcd) *
Case1_conf::DestinationLINKCAPACITY)/(100*8*sc1pktlth);
2161         SC2BUCKETS = ((i2/gcd) *
Case1_conf::DestinationLINKCAPACITY)/(100*8*sc2pktlth);
2162         SC3BUCKETS = 0;
2163         std::cout<<"stat 7"<<" "<<linkID<<"
"<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
"<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
"<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
"<<i2<<" "<<"i3"<<" "<<i3<<std::endl;

2164
2165
2166     } else {
2167         // 1,1,1 //8
2168         gcd = calculateGCD(calculateGCD(i1, i2), i3);
2169         for (std::deque<Ptr<Packet> >::iterator a = m_packets1.begin(); a
!= m_packets1.end(); a++) {
2170             sc1pktlth += (*a)->GetSize();
2171         }
2172         sc1pktlth = sc1pktlth / m_packets1.size();
2173         for (std::deque<Ptr<Packet> >::iterator b =
m_packets2.begin(); b != m_packets2.end(); b++) {
2174             sc2pktlth += (*b)->GetSize();
2175         }
2176         sc2pktlth = sc2pktlth / m_packets2.size();
2177         for (std::deque<Ptr<Packet> >::iterator c =
m_packets3.begin(); c != m_packets3.end(); c++) {
2178             sc3pktlth += (*c)->GetSize();
2179         }
2180         sc3pktlth = sc3pktlth / m_packets3.size();
2181         SC1BUCKETS = ((i1/gcd) *
Case1_conf::DestinationLINKCAPACITY)/(100*8*sc1pktlth);
2182         SC2BUCKETS = ((i2/gcd) *
Case1_conf::DestinationLINKCAPACITY)/(100*8*sc2pktlth);
2183         SC3BUCKETS = ((i3/gcd) *
Case1_conf::DestinationLINKCAPACITY)/(100*8*sc3pktlth);
2184         std::cout<<"stat 8"<<" "<<linkID<<"
"<<Simulator::Now()/1000000000<<" "<<"sc1pktlth"<<"
"<<sc1pktlth<<" "<<"sc2pktlth"<<" "<<sc2pktlth<<"
"<<"sc3pktlth"<<" "<<sc3pktlth<<" "<<"i1"<<" "<<i1<<"i2"<<"
"<<i2<<" "<<"i3"<<" "<<i3<<std::endl;

2185
2186     }
2187 }
2188 }
2189 NS_LOG_INFO("NEW SCHEDULE RATE BUCKETS " << linkID <<" "<<"
"<<Case1_conf::DestinationLINKCAPACITY<<" "<< gcd <<" "<< SC1BUCKETS <<" "
<< SC2BUCKETS <<" "
<< SC3BUCKETS);

2190
2191
2192 }
2193 // For fixed weights links
2194 else if (linkID == 145 || linkID == 189 || // Scenairo 1 Fixed weights queues
2195 linkID == 245 || linkID == 2811 || linkID == 2911 || linkID == 21011 ||
// Scenairo 2 Fixed weights queues
2196 linkID == 334 || linkID == 367 || linkID == 31013 || linkID == 31113 ||
// Scenairo 3 Fixed weights queues
2197 linkID == 31213)
2198 {
2199     gcd = 1;
2200

```



```

2201         SC1BUCKETS =5;
2202         SC2BUCKETS =4;
2203         SC3BUCKETS =3;
2204         NS_LOG_INFO("NEW SCHEDULE RATE BUCKETS " << linkID << " " <<"
        "<<Case1_conf::LINKCAPACITY<<" " << gcd << " " << SC1BUCKETS << " " <<
        SC2BUCKETS << " "
        << SC3BUCKETS);
2205
2206     }
2207
2208
2209     SC1BUCKETSCPY = SC1BUCKETS;
2210     SC2BUCKETSCPY = SC2BUCKETS;
2211     SC3BUCKETSCPY = SC3BUCKETS;
2212
2213     NS_LOG_FUNCTION(linkID<<" " <<"average pkt size" <<" " <<sc1pktlth<<"
        "<<sc2pktlth<<" " <<sc3pktlth);
2214     NS_LOG_FUNCTION(linkID<<" " <<"i1:" <<"
        "<<i1<<"i2:" <<i2<<"i3:" <<i3<<"GCD" <<gcd<<Case1_conf::LINKCAPACITY);
2215
2216
2217
2218     return true;
2219 }
2220
2221 // Set Scheduling Rate By Factor Function. It is used to determine the congestion
    level in service class queue only without managing resources between Diffserv
    domains
2222 bool RDWQueue::SetSchedulingRateByFactor(int band, float alpha)
2223 {
2224     if (band == 1) {
2225         SetSchedulingRateByFactorSC1 = true;
2226         if (alpha <= SC1CONGESTIONFATORALPHAL) {
2227             NS_LOG_INFO("ByFactor case 1"
                << " " << linkID << " " << band << " " << alpha);
2228             NS_LOG_INFO("SC1 case 1");
2229         } else if (alpha > SC1CONGESTIONFATORALPHAL && alpha <=
            SC1CONGESTIONFATORALPHAH) {
2230             NS_LOG_INFO("ByFactor case 2"
                << " " << linkID << " " << band << " " << alpha);
2231
2232             if (alpha <= (SC1CONGESTIONFATORALPHAL + SC1CONGESTIONFATORALPHAH) / 2) {
2233                 NS_LOG_INFO("SC1 case 2");
2234             } else {
2235                 NS_LOG_INFO("SC1 case 3");
2236             }
2237         } else if (alpha > SC1CONGESTIONFATORALPHAH) {
2238             NS_LOG_INFO("ByFactor case 3"
                << " " << linkID << " " << band << " " << alpha);
2239             NS_LOG_INFO("SC1 case 4");
2240         }
2241     } else if (band == 2) {
2242         SetSchedulingRateByFactorSC2 = true;
2243         if (alpha <= SC2CONGESTIONFATORALPHAL) {
2244             NS_LOG_INFO("ByFactor case 1"
                << " " << linkID << " " << band << " " << alpha);
2245             NS_LOG_INFO("SC2 case 1");
2246         } else if (alpha > SC2CONGESTIONFATORALPHAL && alpha <=
            SC2CONGESTIONFATORALPHAH) {
2247             NS_LOG_INFO("ByFactor case 2"
                << " " << linkID << " " << band << " " << alpha);
2248             if (alpha <= (SC2CONGESTIONFATORALPHAL + SC2CONGESTIONFATORALPHAH) / 2) {
2249                 NS_LOG_INFO("SC2 case 2");
2250             }
2251         }
2252     }
2253 }

```



```

2254         NS_LOG_INFO("SC2 case 2");
2255     } else {
2256         NS_LOG_INFO("SC2 case 3");
2257     }
2258 } else if (alpha > SC2CONGESTIONFATORALPHAH) {
2259     NS_LOG_INFO("ByFactor case 3"
2260         << " " << linkID << " " << band << " " << alpha);
2261     NS_LOG_INFO("SC2 case 4");
2262 }
2263 } else if (band == 3) {
2264     SetSchedulingRateByFactorSC3 = true;
2265     if (alpha <= SC3CONGESTIONFATORALPHAL) {
2266         NS_LOG_INFO("ByFactor case 1"
2267             << " " << linkID << " " << band << " " << alpha);
2268         NS_LOG_INFO("SC3 case 1");
2269     } else if (alpha > SC3CONGESTIONFATORALPHAL && alpha <=
2270 SC3CONGESTIONFATORALPHAH) {
2271         NS_LOG_INFO("ByFactor case 2"
2272             << " " << linkID << " " << band << " " << alpha);
2273         if (alpha <= (SC3CONGESTIONFATORALPHAL + SC3CONGESTIONFATORALPHAH) / 2) {
2274             NS_LOG_INFO("SC3 case 2");
2275         } else {
2276             NS_LOG_INFO("SC3 case 3");
2277         }
2278     } else if (alpha > SC3CONGESTIONFATORALPHAH) {
2279         NS_LOG_INFO("ByFactor case 3"
2280             << " " << linkID << " " << band << " " << alpha);
2281         NS_LOG_INFO("SC3 case 4");
2282     }
2283 }
2284 if (SetSchedulingRateByFactorSC1 && SetSchedulingRateByFactorSC2 &&
2285     SetSchedulingRateByFactorSC3) {
2286     SetSchedulingRateByFactorSC1 = SetSchedulingRateByFactorSC2 =
2287     SetSchedulingRateByFactorSC3 = false;
2288     CalculateBucket(SC1ScheRate, SC2ScheRate, SC3ScheRate);
2289 }
2290 return true;
2291 }
2292 // Show Statistic Report Function. It is used to display statistical information
2293 // related to service class queues within an interval k.
2294 void RDWQueue::ShowStatisticReport(void)
2295 {
2296     // report the status
2297     // avg queue delay
2298     float sclavgdelay = 0;
2299     for (std::list<uint64_t>::iterator it = SC1Delay.begin(); it !=
2300 SC1Delay.end(); it++) {
2301         sclavgdelay += *it;
2302     }
2303     sclavgdelay = sclavgdelay / SC1Delay.size();
2304     if (std::isnan(sclavgdelay))
2305     {
2306         sclavgdelay=0;
2307     }
2308     SC1Delay.clear();
2309 }

```

```

2310     float sc2avgdelay = 0;
2311     for (std::list<uint64_t>::iterator it = SC2Delay.begin(); it !=
2312          SC2Delay.end(); it++) {
2313         sc2avgdelay += *it;
2314     }
2315     sc2avgdelay = sc2avgdelay / SC2Delay.size();
2316     if (std::isnan(sc2avgdelay))
2317     {
2318         sc2avgdelay=0;
2319     }
2320     SC2Delay.clear();
2321
2322
2323
2324     float sc3avgdelay = 0;
2325     for (std::list<uint64_t>::iterator it = SC3Delay.begin(); it !=
2326          SC3Delay.end(); it++) {
2327         sc3avgdelay += *it;
2328     }
2329     sc3avgdelay = sc3avgdelay / SC3Delay.size();
2330     if (std::isnan(sc3avgdelay))
2331     {
2332         sc3avgdelay=0;
2333     }
2334     SC3Delay.clear();
2335
2336
2337     // avg queue length
2338     // scheduling rate
2339     float sclavgquelen = 0;
2340     for (std::list<double>::iterator it = SC1Usage.begin(); it != SC1Usage.end();
2341          it++) {
2342         sclavgquelen = *it; // to display the instantonous current length in
2343                             // byte for whole period of simulation .
2344     }
2345
2346     float sc2avgquelen = 0;
2347     for (std::list<double>::iterator it = SC2Usage.begin(); it != SC2Usage.end();
2348          it++) {
2349         sc2avgquelen = *it;
2350     }
2351
2352     float sc3avgquelen = 0;
2353     for (std::list<double>::iterator it = SC3Usage.begin(); it != SC3Usage.end();
2354          it++) {
2355         sc3avgquelen = *it;
2356     }
2357
2358     NS_LOG_INFO("QueueReport " << linkID << " " << sclavgdelay << " " <<
2359                sc2avgdelay << " " << sc3avgdelay << " " <<
2360                sclavgquelen << " " << sc2avgquelen << " " <<
2361                sc3avgquelen << " " << sclByteLength
2362                << " " << sc2ByteLength << " " << sc3ByteLength);
2363 }
2364 // Set buffer size for service class queue in bytes

```

```

2363 void RDWQueue::setBYTEBUFFER(uint32_t sc1, uint32_t sc2, uint32_t sc3)
2364 {
2365     SC1BYTEBUFFER = sc1;
2366     SC2BYTEBUFFER = sc2;
2367     SC3BYTEBUFFER = sc3;
2368 }
2369
2370 // Set the higher threshold parameter of the WRED for service class queue.
2371
2372 void RDWQueue::setREDTHIGH(double sc1, double sc2, double sc3)
2373 {
2374     REDSC1THIGH = sc1;
2375     REDSC2THIGH = sc2;
2376     REDSC3THIGH = sc3;
2377 }
2378 // Set Higher drop probability parameter of the WRED for each service class queue.
2379 void RDWQueue::setREDHDROP(double sc1, double sc2, double sc3)
2380 {
2381     REDSC1HDROP = sc1;
2382     REDSC2HDROP = sc2;
2383     REDSC3HDROP = sc3;
2384 }
2385 // Set the lower threshold parameter of the WRED for service class queue.
2386
2387 void RDWQueue::setREDTLLOW(double sc1, double sc2, double sc3)
2388 {
2389     REDSC1TLOW = sc1;
2390     REDSC2TLOW = sc2;
2391     REDSC3TLOW = sc3;
2392 }
2393
2394 // Set Lower drop probability parameter of the WRED for each service class queue.
2395
2396 void RDWQueue::setREDLDROP(double sc1, double sc2, double sc3)
2397 {
2398     REDSC1LDROP = sc1;
2399     REDSC2LDROP = sc2;
2400     REDSC3LDROP = sc3;
2401 }
2402 // Set the lower threshold parameter for the congestion level in each service
class.
2403 void RDWQueue::setALPHAL(float sc1, float sc2, float sc3)
2404 {
2405     SC1CONGESTIONFATORALPHAL = sc1;
2406     SC2CONGESTIONFATORALPHAL = sc2;
2407     SC3CONGESTIONFATORALPHAL = sc3;
2408 }
2409
2410 // Set the higher threshold parameter for the congestion level in each service
class.
2411
2412 void RDWQueue::setALPHAH(float sc1, float sc2, float sc3)
2413 {
2414     SC1CONGESTIONFATORALPHAH = sc1;
2415     SC2CONGESTIONFATORALPHAH = sc2;
2416     SC3CONGESTIONFATORALPHAH = sc3;
2417 }
2418 // Set fixed beta level (Not considered in the research)
2419 void RDWQueue::setBELTA(float sc1, float sc2, float sc3)
2420 {
2421     CONGESTIONFATBELTA1 = sc1;

```

```

2422     CONGESTIONFATBELTA2 = sc2;
2423     CONGESTIONFATBELTA3 = sc3;
2424 }
2425 // Calculating the scheduling rates for service class queues at the router with
multi out ports
2426 bool RDWQueue::SetSchedulingRateByAverage(int band, double sc1averagelength,
double sc2averagelength, double sc3averagelength, double sc1averagedelay, double
sc2averagedelay, double sc3averagedelay )
2427 {
2428     double SC1ScheRateAvg, SC2ScheRateAvg, SC3ScheRateAvg;
2429
2430     if (sc1averagedelay!=0 && sc1averagelength!=0 && sc2averagedelay!=0 &&
sc2averagelength!=0 && sc3averagedelay!=0 && sc3averagelength!=0) //1
2431     {
2432         SC1ScheRateAvg = sc1averagelength / (sc1averagedelay/1000) ;
2433         SC2ScheRateAvg = 0.5 * SC1ScheRateAvg * sc2averagelength / sc1averagelength;
2434         SC3ScheRateAvg = 0.25 * SC1ScheRateAvg * sc3averagelength / sc1averagelength;
2435     }
2436     else if (sc1averagedelay==0 || sc1averagelength==0) //2
2437     {
2438         SC1ScheRateAvg=0;
2439         SC2ScheRateAvg = sc2averagelength / (sc2averagedelay/1000);
2440         SC3ScheRateAvg = 0.5 * SC2ScheRateAvg * sc3averagelength / sc2averagelength;
2441     }
2442     else if (sc2averagedelay==0 || sc2averagelength==0) //3
2443     {
2444         SC1ScheRateAvg = sc1averagelength / (sc1averagedelay/1000);
2445         SC2ScheRateAvg=0;
2446         SC3ScheRateAvg = 0.5 * SC1ScheRateAvg * sc3averagelength / sc1averagelength;
2447     }
2448     else if (sc3averagedelay==0 || sc3averagelength==0) //4
2449     {
2450         SC1ScheRateAvg = sc1averagelength / (sc1averagedelay/1000);
2451         SC2ScheRateAvg = 0.5 * SC1ScheRateAvg * sc2averagelength / sc1averagelength;
2452         SC3ScheRateAvg=0;
2453     }
2454     else if ((sc1averagedelay==0 || sc1averagelength==0) && (sc2averagedelay==0 ||
sc2averagelength==0) ) //5
2455     {
2456         SC1ScheRateAvg=0;
2457         SC2ScheRateAvg=0;
2458         SC3ScheRateAvg = sc3averagelength/(sc3averagedelay/1000);
2459     }
2460     else if ((sc1averagedelay==0 || sc1averagelength==0) && (sc3averagedelay==0 ||
sc3averagelength==0)) //6
2461     {
2462         SC1ScheRateAvg=0;
2463         SC2ScheRateAvg = sc2averagelength / (sc2averagedelay/1000);
2464         SC3ScheRateAvg=0;
2465     }
2466     else if ((sc2averagedelay==0 || sc2averagelength==0) && (sc3averagedelay==0 ||
sc3averagelength==0) ) //7
2467     {
2468         SC1ScheRateAvg = sc1averagelength / (sc1averagedelay/1000);
2469         SC2ScheRateAvg=0;
2470         SC3ScheRateAvg=0;
2471     }
2472 }
2473
2474
2475     else if (sc1averagedelay==0 && sc1averagelength==0 && sc2averagedelay==0 &&

```



```

2476     sc2averagelength==0 && sc3averagedelay==0 && sc3averagelength==0) //8
2477     {
2478         SC1ScheRateAvg = SC2ScheRateAvg = SC3ScheRateAvg =0;
2479     }
2480     if (std::isnan(SC1ScheRateAvg) || std::isinf(SC1ScheRateAvg))
2481     {
2482         SC1ScheRateAvg=0;
2483     }
2484     else if (std::isnan(SC2ScheRateAvg) || std::isinf(SC2ScheRateAvg))
2485     {
2486         SC2ScheRateAvg=0;
2487     }
2488     else if (std::isnan(SC3ScheRateAvg) || std::isinf(SC3ScheRateAvg))
2489     {
2490         SC3ScheRateAvg=0;
2491     }
2492
2493
2494     if (band == 1) {
2495         SetSchedulingRateByAverageSC1 = true;
2496         SC1ScheRate = SC1ScheRateAvg ;
2497         NS_LOG_INFO("SC1 rate is set to Average");
2498     } else if (band == 2) {
2499         SetSchedulingRateByAverageSC2 = true;
2500         SC2ScheRate = SC2ScheRateAvg;
2501         NS_LOG_INFO("SC2 rate is set to Average");
2502     } else if (band == 3) {
2503         SetSchedulingRateByAverageSC3 = true;
2504         SC3ScheRate = SC3ScheRateAvg;
2505         NS_LOG_INFO("SC3 rate is set to Average");
2506     }
2507
2508
2509     if (SetSchedulingRateByAverageSC1 && SetSchedulingRateByAverageSC2 &&
2510         SetSchedulingRateByAverageSC3) {
2511         SetSchedulingRateByAverageSC1 = SetSchedulingRateByAverageSC2 =
2512         SetSchedulingRateByAverageSC3 = false;
2513         CalculateBucket(SC1ScheRate, SC2ScheRate, SC3ScheRate);
2514         NS_LOG_INFO("CalculateSchedulingRateByAverage " << linkID << " " <<
2515         SC1ScheRate << " " << SC2ScheRate << " " << SC3ScheRate);
2516         NS_LOG_INFO("Average Length Values " << linkID << " " << band << " " <<
2517         sclaveragelength << " " << sc2averagelength << " " <<sc3averagelength);
2518         NS_LOG_INFO("Average Delay Values " << linkID << " " << band << " " <<
2519         sclaveragedelay <<" " << sc2averagedelay << " " << sc3averagedelay);
2520     }
2521     return true;
2522 }
2523
2524 // To get the average queue delay for each service class queue at the router with
2525 multiple out ports
2526 void RDWQueue::UpdateAverageQueueDelay(int band, double AverageQueueDelay)
2527 {
2528     if (band ==1)
2529     {
2530         if (AverageQueueDelay>=0){
2531             SC1CurrAverageDelay.push_back(AverageQueueDelay);}
2532         else SC1CurrAverageDelay.push_back(0);
2533         NS_LOG_INFO("SC1UpdateAverageQueueDelay" << linkID << " " <<
2534         SC1CurrAverageDelay.back());

```

```

2529     }
2530
2531     else if (band ==2)
2532     {
2533         if (AverageQueueDelay>=0){
2534             SC2CurrAverageDelay.push_back(AverageQueueDelay);}
2535         else SC2CurrAverageDelay.push_back(0);
2536         NS_LOG_INFO("SC2UpdateAverageQueueDelay" << linkID << " " <<
2537                     SC2CurrAverageDelay.back());
2538     }
2539
2540     else if (band ==3)
2541     {
2542         if (AverageQueueDelay>=0){
2543             SC3CurrAverageDelay.push_back(AverageQueueDelay);}
2544         else SC3CurrAverageDelay.push_back(0);
2545         NS_LOG_INFO("SC3UpdateAverageQueueDelay" << linkID << " " <<
2546                     SC3CurrAverageDelay.back());
2547     }
2548
2549     // Used in the process of selecting mininum Average queue delay for each service
2550     // class queue at the router with multiple out ports to calculate the service class
2551     // scheduling rate at the router with multiple out ports.
2552
2553     double RDWQueue::GetAverageQueueDelay(int band)
2554     {
2555         double AverageQueueDelay;
2556         if (band == 1)
2557         {
2558             AverageQueueDelay= SC1CurrAverageDelay.front();
2559             NS_LOG_INFO("SC1GetAverageQueueDelay" << linkID << " " << AverageQueueDelay);
2560             SC1CurrAverageDelay.erase (SC1CurrAverageDelay.begin());
2561             if (AverageQueueDelay>=0)
2562             {
2563                 return AverageQueueDelay;
2564             }
2565         }
2566
2567         else if (band ==2)
2568         {
2569             AverageQueueDelay= SC2CurrAverageDelay.front();
2570             NS_LOG_INFO("SC2GetAverageQueueDelay" << linkID << " " << AverageQueueDelay);
2571             SC2CurrAverageDelay.erase (SC2CurrAverageDelay.begin());
2572             if (AverageQueueDelay>=0)
2573             {
2574                 return AverageQueueDelay;
2575             }
2576         }
2577
2578         else if (band ==3)
2579         {
2580             AverageQueueDelay= SC3CurrAverageDelay.front();
2581             NS_LOG_INFO("SC3GetAverageQueueDelay" << linkID << " " << AverageQueueDelay);
2582             SC3CurrAverageDelay.erase (SC3CurrAverageDelay.begin());
2583             if (AverageQueueDelay>=0)
2584             {
2585                 return AverageQueueDelay;
2586             }
2587         }
2588
2589         return 0;
2590     }
2591
2592     // To get the average queue length for each service class queue at the router with
2593     // multiple out ports (used in the process of managing resources between DiffServ
2594     // domains to calculate the maximum congestion level in service classs queue)

```

```

2584
2585 void RDWQueue::UpdateAverageQueueLength1 (int band, double AverageQueueLength)
2586 {
2587     if (band ==1)
2588     {
2589         SC1CurrAverageLength1.push_back(AverageQueueLength);
2590         NS_LOG_INFO("SC1UpdateAverageQueueLength" << linkID << " " <<
2591         SC1CurrAverageLength1.back());
2592     }
2593     else if (band ==2)
2594     {
2595         SC2CurrAverageLength1.push_back(AverageQueueLength);
2596         NS_LOG_INFO("SC2UpdateAverageQueueLength" << linkID << " " <<
2597         SC2CurrAverageLength1.back());
2598     }
2599     else if (band ==3)
2600     {
2601         SC3CurrAverageLength1.push_back(AverageQueueLength);
2602         NS_LOG_INFO("SC3UpdateAverageQueueLength" << linkID << " " <<
2603         SC3CurrAverageLength1.back());
2604     }
2605 }
2606 // Used in the process of selecting maximum Average queue length for each service
2607 // class queue at the router with multiple out ports to specify the maximum
2608 // congestion level.
2609
2610 double RDWQueue::GetAverageQueueLength1(int band)
2611 {
2612     double AverageQueueLength1;
2613     if (band == 1)
2614     {
2615         AverageQueueLength1= SC1CurrAverageLength1.front();
2616         NS_LOG_INFO("SC1GetAverageQueueLength" << linkID << " " <<
2617         AverageQueueLength1);
2618         SC1CurrAverageLength1.erase (SC1CurrAverageLength1.begin());
2619         return AverageQueueLength1;
2620     }
2621     else if (band ==2)
2622     {
2623         AverageQueueLength1= SC2CurrAverageLength1.front();
2624         NS_LOG_INFO("SC2GetAverageQueueLength" << linkID << " " <<
2625         AverageQueueLength1);
2626         SC2CurrAverageLength1.erase (SC2CurrAverageLength1.begin());
2627         return AverageQueueLength1;
2628     }
2629     else if (band ==3)
2630     {
2631         AverageQueueLength1= SC3CurrAverageLength1.front();
2632         NS_LOG_INFO("SC3GetAverageQueueLength" << linkID << " " <<
2633         AverageQueueLength1);
2634         SC3CurrAverageLength1.erase (SC3CurrAverageLength1.begin());
2635         return AverageQueueLength1;
2636     }
2637     return 0;
2638 }
2639 // To get the average queue length for each service class queue at the router with
2640 // multiple out ports
2641

```



```

2636 void RDWQueue::UpdateAverageQueueLength2 (int band, double AverageQueueLength)
2637 {
2638     if (band ==1)
2639     {
2640         SC1CurrAverageLength2.push_back(AverageQueueLength);
2641         NS_LOG_INFO("SC1UpdateAverageQueueLength" << linkID << " " <<
2642             SC1CurrAverageLength2.back());
2643     }
2644     else if (band ==2)
2645     {
2646         SC2CurrAverageLength2.push_back(AverageQueueLength);
2647         NS_LOG_INFO("SC2UpdateAverageQueueLength" << linkID << " " <<
2648             SC2CurrAverageLength2.back());
2649     }
2650     else if (band ==3)
2651     {
2652         SC3CurrAverageLength2.push_back(AverageQueueLength);
2653         NS_LOG_INFO("SC3UpdateAverageQueueLength" << linkID << " " <<
2654             SC3CurrAverageLength2.back());
2655     }
2656 }
2657 // Used in the process of selecting maximum Average queue length for each service
2658 // class queue at the router with multiple out ports to calculate the service class
2659 // scheduling rate at the router with multiple out ports .
2660
2661 double RDWQueue::GetAverageQueueLength2(int band)
2662 {
2663     double AverageQueueLength2;
2664     if (band == 1)
2665     {
2666         AverageQueueLength2= SC1CurrAverageLength2.front();
2667         NS_LOG_INFO("SC1GetAverageQueueLength" << linkID << " " <<
2668             AverageQueueLength2);
2669         SC1CurrAverageLength2.erase (SC1CurrAverageLength2.begin());
2670         return AverageQueueLength2;
2671     }
2672     else if (band ==2)
2673     {
2674         AverageQueueLength2= SC2CurrAverageLength2.front();
2675         NS_LOG_INFO("SC2GetAverageQueueLength" << linkID << " " <<
2676             AverageQueueLength2);
2677         SC2CurrAverageLength2.erase (SC2CurrAverageLength2.begin());
2678         return AverageQueueLength2;
2679     }
2680     else if (band ==3)
2681     {
2682         AverageQueueLength2= SC3CurrAverageLength2.front();
2683         NS_LOG_INFO("SC3GetAverageQueueLength" << linkID << " " <<
2684             AverageQueueLength2);
2685         SC3CurrAverageLength2.erase (SC3CurrAverageLength2.begin());
2686         return AverageQueueLength2;
2687     }
2688     return 0;
2689 }
2690 // Managing the service classes scheduling rates between DiffServ domains function
2691 // based on the congestion level and scheduling rate update factor (beta)
2692 bool RDWQueue::SetSchedulingRateByLinearFactor(int band, float avgqueuelength)
2693 {

```

```

2688     if (band == 1) {
2689         SetSchedulingRateByLinearFactorSC1 = true;
2690         float sclalpha = avgqueuelength/SC1BYTEBUFFER;
2691         if (sclalpha <= SC1CONGESTIONFATORALPHAL) {
2692             NS_LOG_INFO("ByLinearFactor case 1"
2693                 << " " << linkID << " " << band << " " << "alpha value:"
2694                 << " " << sclalpha << " " << "beta value:" << " " << 0);
2695             SC1ScheRate = SC1ScheRate;
2696             NS_LOG_INFO("SC1 case 1");
2697         } else if (sclalpha > SC1CONGESTIONFATORALPHAL && sclalpha <
2698             SC1CONGESTIONFATORALPHAH) {
2699             float sclbeta =
2700                 Case1_conf::SC1BETAMAX*((sclalpha-SC1CONGESTIONFATORALPHAL)/(SC1CONGESTI
2701                 ONFATORALPHAH-SC1CONGESTIONFATORALPHAL));
2702             NS_LOG_INFO("ByLinearFactor case 2"
2703                 << " " << linkID << " " << band << " " << "alpha value:"
2704                 << " " << sclalpha << " " << "Beta value:" << " " << sclbeta);
2705             SC1ScheRate = SC1ScheRate - sclbeta * SC1ScheRate;
2706         }
2707     } else { NS_LOG_INFO("ByLinearFactor case 3"
2708         << " " << linkID << " " << band << " " << "alpha value:"
2709         << " " << sclalpha << " " << "beta value:" << " " <<
2710         Case1_conf::SC1BETAMAX);
2711         SC1ScheRate = SC1ScheRate - (SC1ScheRate * Case1_conf::SC1BETAMAX);
2712         NS_LOG_INFO("SC1 case 3");
2713     }
2714 }
2715
2716 if (band == 2) {
2717     SetSchedulingRateByLinearFactorSC2 = true;
2718     float sc2alpha = avgqueuelength/SC2BYTEBUFFER;
2719     if (sc2alpha <= SC2CONGESTIONFATORALPHAL) {
2720         NS_LOG_INFO("ByLinearFactor case 1"
2721             << " " << linkID << " " << band << " " << "alpha value:"
2722             << " " << sc2alpha << " " << "beta value:" << " " << 0);
2723         SC2ScheRate = SC2ScheRate;
2724         NS_LOG_INFO("SC2 case 1");
2725     } else if (sc2alpha > SC2CONGESTIONFATORALPHAL && sc2alpha <
2726         SC2CONGESTIONFATORALPHAH) {
2727         float sc2beta = Case1_conf::SC2BETAMAX *
2728             (sc2alpha-SC2CONGESTIONFATORALPHAL)/(SC2CONGESTIONFATORALPHAH-SC2CONGESTI
2729             ONFATORALPHAL);
2730         NS_LOG_INFO("ByLinearFactor case 2"
2731             << " " << linkID << " " << band << " " << "alpha value:"
2732             << " " << sc2alpha << " " << "Beta value:" << " " << sc2beta);
2733         SC2ScheRate = SC2ScheRate - sc2beta * SC2ScheRate;
2734     }
2735 } else { NS_LOG_INFO("ByLinearFactor case 3"
2736     << " " << linkID << " " << band << " " << "alpha
2737     value:" << " " << sc2alpha << " " << "beta value:" << " " <<
2738     Case1_conf::SC2BETAMAX);
2739     SC2ScheRate = SC2ScheRate - (SC2ScheRate * Case1_conf::SC2BETAMAX);
2740     NS_LOG_INFO("SC2 case 3");
2741 }
2742
2743 if (band == 3) {
2744     SetSchedulingRateByLinearFactorSC3 = true;

```

```

2735     float sc3alpha = avgqueuelength/SC3BYTEBUFFER;
2736     if (sc3alpha <= SC3CONGESTIONFATORALPHAL) {
2737         NS_LOG_INFO("ByLinearFactor case 1"
2738             << " " << linkID << " " << band << " " << "alpha value:"
                << " " << sc3alpha << " " << "beta value:" << " " << 0);
2739         SC3ScheRate = SC3ScheRate;
2740         NS_LOG_INFO("SC3 case 1");
2741     } else if (sc3alpha > SC3CONGESTIONFATORALPHAL && sc3alpha <
SC3CONGESTIONFATORALPHAH) {
2742         float sc3beta = Case1_conf::SC3BETAMAX *
                (sc3alpha-SC3CONGESTIONFATORALPHAL)/(SC3CONGESTIONFATORALPHAH-SC3CONGESTIONFATORALPHAL);
2743         NS_LOG_INFO("ByLinearFactor case 2"
2744             << " " << linkID << " " << band << " " << "alpha value:"
                << " " << sc3alpha << " " << "Beta value:" << " " << sc3beta);
2745         SC3ScheRate = SC3ScheRate - sc3beta * SC3ScheRate;
2746     }
2747     else { NS_LOG_INFO("ByLinearFactor case 3"
2748         << " " << linkID << " " << band << " " << "alpha value:"
                << " " << sc3alpha << " " << "beta value:" << " " <<
                Case1_conf::SC3BETAMAX);
2749         SC3ScheRate = SC3ScheRate - (SC3ScheRate * Case1_conf::SC3BETAMAX);
2750         NS_LOG_INFO("SC3 case 3");
2751     }
2752 }
2753
2754 if (SetSchedulingRateByLinearFactorSC1 && SetSchedulingRateByLinearFactorSC2 &&
SetSchedulingRateByLinearFactorSC3) {
2755     SetSchedulingRateByLinearFactorSC1 = SetSchedulingRateByLinearFactorSC2 =
SetSchedulingRateByLinearFactorSC3 = false;
2756     CalculateBucket(SC1ScheRate, SC2ScheRate, SC3ScheRate);
2757 }
2758 return true;
2759 }
2760

```

## A-4: Traffic Model Test NS3 Simulation Code:

```
1  #ifndef TOSAPP_H
2  #define TOSAPP_H
3
4  #include "ns3/core-module.h"
5  #include "ns3/network-module.h"
6  #include "ns3/internet-module.h"
7  #include "ns3/point-to-point-module.h"
8  #include "ns3/applications-module.h"
9  #include "ns3/csma-module.h"
10 #include "ns3/qos-tag.h"
11
12
13 using namespace ns3;
14
15 class TosApp : public Application
16 {
17 public:
18     //TosApp();
19     TosApp(uint8_t tos, uint32_t TrafPatt);
20     virtual ~TosApp();
21
22
23     void Setup (Ptr<Socket> socket, Address address, uint32_t packetSize, uint32_t nPackets, DataRate dataRate);
24     void Setup(Ptr<Socket> socket, Address address, uint32_t packetSize, uint32_t nPackets, DataRate dataRate, double mean, double bound, int onofftype);
25
26 private:
27     virtual void StartApplication (void);
28     virtual void StopApplication (void);
29
30     void ScheduleTx (void);
31     void SendPacket (void);
32     uint32_t m_onofftype; // 1=random value 2= fixed value
33     double m_ontime;
34     double m_offtime;
35     double m_fliptimestamp;
36     double interval;
37     uint32_t m_currentONOFFstat; // 1=on 0=off
38
39     Ptr<Socket> m_socket;
40     Address m_peer;
41     uint32_t m_packetSize;
42     uint32_t m_nPackets;
43     DataRate m_dataRate;
44     EventId m_sendEvent;
45     bool m_running;
46     uint32_t m_packetsSent;
47     QosTag qostag;
48     uint32_t trafficPattern;
49 };
50
51 #endif // TOSAPP_H
52
```



```

1  #include "TosApp.h"
2  #include "ns3/random-variable-stream.h"
3  #include "Case1_conf.h"
4  #include "ns3/ipv4-header.h"
5
6  using namespace ns3;
7
8
9
10 TosApp::TosApp(uint8_t tos, uint32_t TrafPatt)
11     : m_socket(0)
12     , m_peer()
13     , m_packetSize(0)
14     , m_nPackets(0)
15     , m_dataRate(0)
16     , m_sendEvent()
17     , m_running(false)
18     , m_packetsSent(0)
19     , trafficPattern(TrafPatt)
20 {
21     qostag.SetTid(tos);
22 }
23
24 TosApp::~TosApp()
25 {
26     m_socket = 0;
27 }
28
29 void TosApp::Setup(Ptr<Socket> socket, Address address, uint32_t packetSize,
30 uint32_t nPackets, DataRate dataRate)
31 {
32     m_socket = socket;
33     m_peer = address;
34     m_packetSize = packetSize; //packet size
35     m_nPackets = nPackets;
36     //m_nPackets = (rand() / double(RAND_MAX)) * nPackets;
37     m_dataRate = dataRate;
38     // edit 24/08/2016
39     m_onofftype = 3;
40     m_currentONOFFstat = 1; // on stat
41
42     if (m_onofftype == 1) { // varies
43         Ptr<ExponentialRandomVariable> x = CreateObject<ExponentialRandomVariable>();
44         x->SetAttribute("Mean", DoubleValue(Case1_conf::ONTIMEEXPONMEAN));
45         x->SetAttribute("Bound", DoubleValue(Case1_conf::ONTIMEEXPONBOUND));
46         m_fliptimestamp = Simulator::Now().GetSeconds() + x->GetValue();
47     } else if (m_onofftype == 2) { // fixed
48         Ptr<ExponentialRandomVariable> x = CreateObject<ExponentialRandomVariable>();
49         x->SetAttribute("Mean", DoubleValue(Case1_conf::ONTIMEEXPONMEAN));
50         x->SetAttribute("Bound", DoubleValue(Case1_conf::ONTIMEEXPONBOUND));
51         m_ontime = x->GetValue();
52         m_fliptimestamp = Simulator::Now().GetSeconds() + m_ontime;
53
54         x = CreateObject<ExponentialRandomVariable>();
55         x->SetAttribute("Mean", DoubleValue(Case1_conf::OFFTIMEEXPONMEAN));
56         x->SetAttribute("Bound", DoubleValue(Case1_conf::OFFTIMEEXPONBOUND));
57         m_offtime = x->GetValue();
58         std::cout << m_ontime << " " << m_offtime << std::endl;
59     } else {
60         m_fliptimestamp = 1000000000;

```

```

61         }//////////
62     }
63     // traffic with on-off period
64     void TosApp::Setup(Ptr<Socket> socket,
65         Address address,
66         uint32_t packetSize,
67         uint32_t nPackets,
68         DataRate dataRate,
69         double mean,
70         double bound,
71         int onofftype)
72     {
73         m_socket = socket;
74         m_peer = address;
75         m_packetSize = packetSize;
76         m_nPackets = (rand() / double(RAND_MAX)) * nPackets;
77         m_dataRate = dataRate;
78
79         if (trafficPattern == 0) {
80             // constant
81             Case1_conf::CONSTANTRADVAR = mean;
82         } else if (trafficPattern == 1) {
83             // exponential
84             Case1_conf::EXPONRADVARMEAN = mean;
85             Case1_conf::EXPONRADVARBOUND = bound;
86         } else if (trafficPattern == 2) {
87             // pareto
88             Case1_conf::PARETORADVARMEAN = mean;
89             Case1_conf::PARETORADVARSAHPE = bound;
90         }
91         m_onofftype = onofftype;
92         m_currentONOFFstat = 1; // on stat
93
94         if (m_onofftype == 1) { // varies
95             Ptr<ExponentialRandomVariable> x = CreateObject<ExponentialRandomVariable>();
96             x->SetAttribute("Mean", DoubleValue(Case1_conf::ONTIMEEXPONMEAN));
97             x->SetAttribute("Bound", DoubleValue(Case1_conf::ONTIMEEXPONBOUND));
98             m_fliptimestamp = Simulator::Now().GetSeconds() + x->GetValue();
99
100         } else if (m_onofftype == 2) { // fixed
101             Ptr<ExponentialRandomVariable> x = CreateObject<ExponentialRandomVariable>();
102             x->SetAttribute("Mean", DoubleValue(Case1_conf::ONTIMEEXPONMEAN));
103             x->SetAttribute("Bound", DoubleValue(Case1_conf::ONTIMEEXPONBOUND));
104             m_ontime = x->GetValue();
105             m_fliptimestamp = Simulator::Now().GetSeconds() + m_ontime;
106
107             x = CreateObject<ExponentialRandomVariable>();
108             x->SetAttribute("Mean", DoubleValue(Case1_conf::OFFTIMEEXPONMEAN));
109             x->SetAttribute("Bound", DoubleValue(Case1_conf::OFFTIMEEXPONBOUND));
110             m_offtime = x->GetValue();
111             std::cout << "m_ontime" << m_ontime << " " << "m_offtime" << m_offtime <<
112             std::endl;
113         } else {
114             m_fliptimestamp = 1000000000;
115         }
116     }
117     ///////////////
118     void TosApp::StartApplication(void)
119     {
120         m_running = true;

```

```

121     m_packetsSent = 0;
122     m_socket->Bind();
123     m_socket->Connect(m_peer);
124     SendPacket();
125 }
126
127 void TosApp::StopApplication(void)
128 {
129     m_running = false;
130
131     if (m_sendEvent.IsRunning()) {
132         Simulator::Cancel(m_sendEvent);
133     }
134
135     if (m_socket) {
136         m_socket->Close();
137     }
138 }
139
140 void TosApp::SendPacket(void)
141 { // 13/05 edit
142     if (trafficPattern==3) // VoIP traffic packet size
143     {
144         m_packetSize = 200;
145     }
146     else if (trafficPattern==4) // Video conferencing traffic packet size
147     {
148         Ptr<ExponentialRandomVariable> x = CreateObject<ExponentialRandomVariable>();
149         x->SetAttribute("Mean", DoubleValue(34.285)); //previous 200
150         x->SetAttribute("Bound", DoubleValue(200));
151         m_packetSize = (uint32_t)(x->GetValue()) + 1300;
152     }
153     else if (trafficPattern==5) // FTP traffic
154     {
155         Ptr<ExponentialRandomVariable> x = CreateObject<ExponentialRandomVariable>();
156         x->SetAttribute("Mean", DoubleValue(91.4285)); //357
157         x->SetAttribute("Bound", DoubleValue(300)); // amount of difference between
max. and min.
158         m_packetSize = (uint32_t)(x->GetValue())+200; // Min. value.
159     }
160     else if (trafficPattern==6) // Database Query message traffic,Best effort
161     {
162         m_packetSize = 512;
163     }
164
165     // random packet size
166
167     /*Ptr<ExponentialRandomVariable> x = CreateObject<ExponentialRandomVariable>();
168     x->SetAttribute("Mean", DoubleValue(Case1_conf::EXPONPKTSIZEMEAN));
169     x->SetAttribute("Bound", DoubleValue(Case1_conf::EXPONPKTSIZEBOUND));
170     m_packetSize = (uint32_t)(x->GetValue()) * Case1_conf::TRAFFICPKTSIZE) + 1;*/
171     /*std::cout<< m_socket->GetNode()-> GetId() <<" pkt size "<<m_packetSize<<"
tos " << (uint32_t)qostag.GetTid ()
<<std::endl;*/
172
173     Ptr<Packet> packet = Create<Packet>(m_packetSize);
174     packet->AddPacketTag(qostag);
175     std::cout << "Pkt_create " << packet->GetUid() <<" "<<Simulator::Now()<<"
"<<(uint32_t)qostag.GetTid()<< std::endl;
176     m_socket->Send(packet);
177
178

```



```

179 // For testing the generated traffic
180 std::cout << "Node ID." << m_socket->GetNode()-> GetId() << " " << "Pkt_create" << "
181 " << "pkt size:" << m_packetSize << " " << packet->GetUid() << "
182 " << Simulator::Now() << " " << (uint32_t)qostag.GetTid() << std::endl;
183
184 if (++m_packetsSent < m_nPackets) {
185     ScheduleTx();
186 }
187
188 void TosApp::ScheduleTx(void)
189 {
190     // update of on-off application
191     if (Simulator::Now().GetSeconds() >= m_fliptimestamp) {
192         if (m_currentONOFFstat == 1) { // on -> off
193             if (m_onofftype == 1) {
194                 Ptr<ExponentialRandomVariable> x =
195                     CreateObject<ExponentialRandomVariable>();
196                 x->SetAttribute("Mean", DoubleValue(Case1_conf::OFFTIMEEXPONMEAN));
197                 x->SetAttribute("Bound", DoubleValue(Case1_conf::OFFTIMEEXPONBOUND));
198                 double interval = x->GetValue();
199                 while(interval <= 0){
200                     interval = x->GetValue();
201                 }
202                 m_fliptimestamp = Simulator::Now().GetSeconds() + interval;
203             } else if (m_onofftype == 2) {
204                 m_fliptimestamp = Simulator::Now().GetSeconds() + m_offtime;
205             }
206             m_currentONOFFstat = 0; // go off
207             std::cout << "now off " << Simulator::Now().GetSeconds() << " " <<
208                 m_fliptimestamp << std::endl;
209         } else { // off -> on
210             if (m_onofftype == 1) {
211                 Ptr<ExponentialRandomVariable> x =
212                     CreateObject<ExponentialRandomVariable>();
213                 x->SetAttribute("Mean", DoubleValue(Case1_conf::ONTIMEEXPONMEAN));
214                 x->SetAttribute("Bound", DoubleValue(Case1_conf::ONTIMEEXPONBOUND));
215                 double interval = x->GetValue();
216                 while(interval <= 0){
217                     interval = x->GetValue();
218                 }
219                 m_fliptimestamp = Simulator::Now().GetSeconds() + interval;
220             } else if (m_onofftype == 2) {
221                 m_fliptimestamp = Simulator::Now().GetSeconds() + m_ontime;
222             }
223             m_currentONOFFstat = 1; // go on
224             std::cout << "now on " << Simulator::Now().GetSeconds() << " " <<
225                 m_fliptimestamp << std::endl;
226         }
227     }
228
229     if (m_currentONOFFstat == 1) // if on, send packets
230     {
231         ///////////////////////////////////////////////////
232         if (m_running) {
233             double value = 0;
234
235             if (trafficPattern == 0) {
236                 // constant
237                 Ptr<ConstantRandomVariable> x = CreateObject<ConstantRandomVariable>();

```

```

234         x->SetAttribute("Constant", DoubleValue(Case1_conf::CONSTANTRADVAR));
235         value = x->GetValue();
236     } else if (trafficPattern == 1) {
237         // exponential
238         Ptr<ExponentialRandomVariable> x =
239             CreateObject<ExponentialRandomVariable>();
240         x->SetAttribute("Mean", DoubleValue(Case1_conf::EXPONRADVARMEAN));
241         x->SetAttribute("Bound", DoubleValue(Case1_conf::EXPONRADVARBOUND));
242         value = x->GetValue();
243     } else if (trafficPattern == 2) {
244         // pareto
245         Ptr<ParetoRandomVariable> x = CreateObject<ParetoRandomVariable>();
246         x->SetAttribute("Mean", DoubleValue(Case1_conf::PARETORADVARMEAN));
247         x->SetAttribute("Shape", DoubleValue(Case1_conf::PARETORADVARSHAPE));
248         value = x->GetValue();
249     }
250     //edit 13/05
251     else if (trafficPattern == 3) {
252         // VoIP packets
253         Ptr<ConstantRandomVariable> x = CreateObject<ConstantRandomVariable>();
254         x->SetAttribute("Constant", DoubleValue(0.02));
255         value = x->GetValue();
256         value = value - 0.02;
257         /*Ptr<ExponentialRandomVariable> x =
258             CreateObject<ExponentialRandomVariable>();
259             x->SetAttribute("Mean", DoubleValue(0.02));
260             x->SetAttribute("Bound", DoubleValue(0));
261             value = x->GetValue();*/
262     } else if (trafficPattern == 4) {
263         // Video conferencing packets
264         Ptr<ExponentialRandomVariable> x =
265             CreateObject<ExponentialRandomVariable>();
266         x->SetAttribute("Mean", DoubleValue(0.02916)); //0.005
267         x->SetAttribute("Bound", DoubleValue(0.006));
268         value = x->GetValue();
269     } else if (trafficPattern == 5) {
270         // traffic (FTP)
271         Ptr<ExponentialRandomVariable> x =
272             CreateObject<ExponentialRandomVariable>();
273         x->SetAttribute("Mean", DoubleValue(0.01093)); //0.0028
274         x->SetAttribute("Bound", DoubleValue(0.006));
275         value = x->GetValue();
276     } else if (trafficPattern == 6) {
277         // Database Query message traffic, Best effort
278         Ptr<ExponentialRandomVariable> x =
279             CreateObject<ExponentialRandomVariable>();
280         x->SetAttribute("Mean", DoubleValue(0.05));
281         x->SetAttribute("Bound", DoubleValue(0.006));
282         value = x->GetValue();
283     }
284     }
285     // update of on-off application
286     else {
287         Time tNext(Seconds(m_fliptimeStamp-Simulator::Now().GetSeconds()+0.001));
288         Simulator::Schedule(tNext, &TosApp::ScheduleTx, this);

```

```
289     }  
290 }  
291 ///////////////////////////////////  
292
```

## **A-5: Test Scenarios Network Topology Simulation using NS3:**

The topologies of the test scenarios presented in section 6.3 are built by the execution of the network topology application. In this application, objects from the queuing model application (RDWQueue), the traffic model application (TosApp) and the packet sink application (PacketSink) are created to represent the queues, traffic sources and the destinations of the DiffServ upstream and downstream domains respectively. Any computer network is constructed from multiple elements that perform various functions. These elements can be switches, routers, servers, access points, etc. To represent these elements using the network simulator NS3, this simulation must create a node for each element. Each node has at least one network device that is used to connect it to another node via a link. Any two nodes are connected via a point to point channel or link – if it is a wired network. The point to point channel should also be built and have its attributes defined in terms of delay, rate and inter-frame gap. The type of the network device is based on the type of the channel that is used to connect it to the other node. The NS3 software provides a number of (Helpers) that can be used to instantiate NS3 models (class references) on a node. The (Helpers) that are used in this simulation are as follows.

1. InternetStackHelper – this is used to aggregate the IP/TCP/UDP functionalities of existing nodes. In this simulation program, “stack” is an instance of NS3::InternetStackHelper.
2. IPv4AddressHelper – this is a simple IPv4 address generator. It is used to assign simple IPv4 address to node interfaces. In this simulation program, “address” is an instance of NS3::Ipv4AddressHelper.
3. PointToPointHelper – this is used to build a point to point network device object. In this simulation program, “p2pXY” is an instance of NS3::PointToPointHelper which is used to setup a point to point interface link that connects the point to point network devices at nodes X and Y.
4. PacketSinkHelper – this is used to instantiate a packet sink application on the destination node. In this simulation program, “packetSinkHelper” is an instance of NS3::PacketSinkHelperHelper .

Any two nodes (or more) in NS3 represent a network and both will have IP and mask addresses. The IP addresses for the network hosts (nodes) and the addresses of their network devices must also be defined when building a network topology. Within the node, the functionality of the network element is represented. If the element represents a DiffServ router then an object from

RDWQueue is created, provided with an identification, and its attributes will be configured and will be setup inside the node. If the element represents a traffic source or destination then an object from either TosApp or PacketSink is created, provided with an identification, and its attributes will be configured and will be setup inside the node. Before adding a traffic source to a node, the TCP and UDP socket addresses for the source must be created in order to organise the process of sending packets from the traffic source applications to the sink applications at the destination. A “socket” in the context of communication means creating an endpoint for communication. The socket address is a combination of an IP address for the source node and a port number for the destination node. Based on the socket address, the Internet socket delivers incoming data packets to the appropriate application at the destination for processing. The management process of any service class queue is accomplished within the network topology application through using the type of the service class and the identification of the queue. This means that the process of managing resources across different DiffServ domains and the process of configuring the service class scheduling rates for the queues that belong to the respective service class at the ingress routers of the downstream domains – as per scenarios 2 and 3 – are undertaken by this application. These two processes also have to be identified and scheduled by the NS3 scheduler (Simulator::Schedule Class Reference) during the simulation run.

Moreover, the time that a packet is received at the destination is also calculated in the network topology application by comparing the destination address that is set in the packet header with the address of a particular destination node and taking the current simulation time. This time will be used to measure the End to End Delay for each service class’ traffic.

#### A-5.1: Test Scenario 1, Topology Simulation Diagram and NS3 Code:

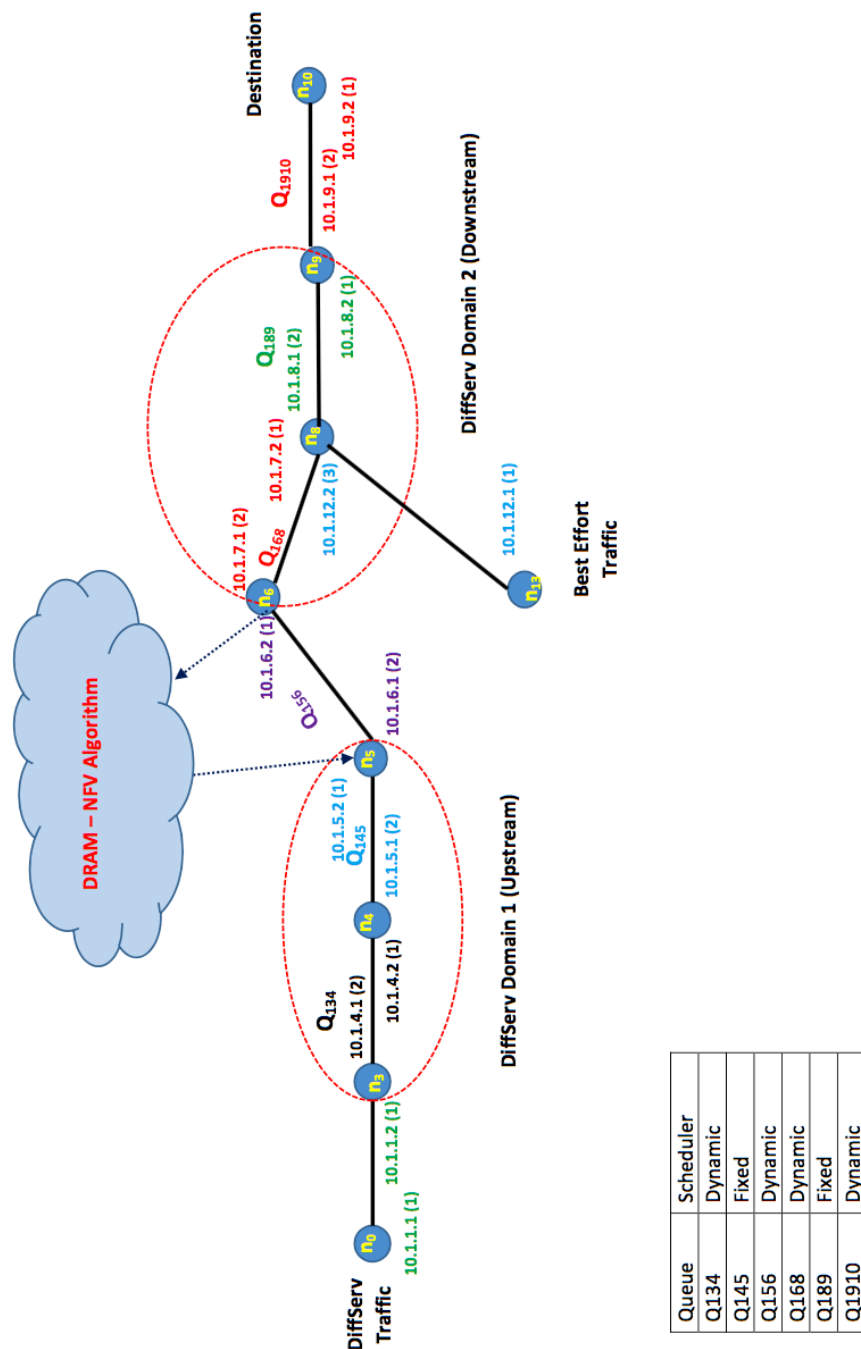


Figure 9-8, test scenario 1 simulation network topology diagram.

```

#ifndef CASE1_H
#define CASE1_H

#include "ns3/core-module.h"
#include "ns3/network-module.h"
#include "ns3/internet-module.h"
#include "ns3/point-to-point-module.h"
#include "ns3/applications-module.h"
#include "ns3/csma-module.h"
#include "RDWQueue.h"
#include "ns3/qos-tag.h" // added for onoff appl.
using namespace ns3;

class Case1
{
public:
    Case1();
    virtual ~Case1();

    NodeContainer nodes;
    Ptr<RDWQueue> queue134;
    Ptr<RDWQueue> queue145;
    Ptr<RDWQueue> queue156;
    Ptr<RDWQueue> queue168;
    Ptr<RDWQueue> queue189;
    Ptr<RDWQueue> queue1910;

    void ScheduleSchedulingRateUpdate(void);
    //void TrafficFromTo(int from, int to, int ProtocolType ,int TrafficType, int ToS);
    void TrafficFromTo1(int from, int to, int ProtocolType ,int TrafficType, int ToS);
    void TrafficFromTo2(int from, int to, int ProtocolType ,int TrafficType, int ToS);
    void TrafficFromTo3(int from, int to, int ProtocolType ,int TrafficType, int ToS);

    void OnOffTrafficFromTo(int from, int to, int ProtocolType, int TrafficType, int ToS); // added for
onoff app.
    QosTag qosTag; //added for onoff appl.

};

#endif // CASE1_H

```



```

1  #include "Case1.h"
2  #include "TosApp.h"
3  #include "Case1_conf.h"
4
5
6  using namespace ns3;
7
8  // static void MacRecv(Ptr<const Packet> packet)
9  static void MacRecv(const Ptr<const Packet> packet, Ptr<Ipv4> ipv4, const uint32_t ȧ
interface)
10 {
11     Ipv4Header header;
12     packet->PeekHeader(header);
13     if (header.GetDestination().IsEqual(ipv4->GetAddress(interface, 0).GetLocal())) {
14         QosTag tosTag;
15         uint32_t band = 0;
16         if (packet->PeekPacketTag(tosTag)) {
17             band = (uint32_t)tosTag.GetTid();
18         }
19         if (band != 0 ) {
20             std::cout << "Pkt_recv " << packet->GetUid() << " " << ȧ
Simulator::Now() << " " << band << std::endl;
21         }
22     }
23 }
24
25 Case1::Case1()
26 {
27     nodes.Create(14);
28
29     InternetStackHelper stack;
30     stack.Install(nodes);
31
32     Ipv4AddressHelper address;
33     address.SetBase("10.1.1.0", "255.255.255.252");
34
35     // 0-3
36     PointToPointHelper p2p03;
37     p2p03.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
38     p2p03.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
39     address.Assign(p2p03.Install(nodes.Get(0), nodes.Get(3)));
40     // 1-3
41     address.SetBase("10.1.2.0", "255.255.255.252");
42     PointToPointHelper p2p13;
43     p2p13.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
44     p2p13.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
45     address.Assign(p2p13.Install(nodes.Get(1), nodes.Get(3)));
46     // 2-3
47     address.SetBase("10.1.3.0", "255.255.255.252");
48     PointToPointHelper p2p23;
49     p2p23.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
50     p2p23.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
51     address.Assign(p2p23.Install(nodes.Get(2), nodes.Get(3)));
52
53     // 3-4
54     // notice: queue need to be set before install!!!
55     // you can't get the queue from outside the queue, so you need to update the ȧ
parameter in the queue function.
56     // you need to set InterframeGap to decide the queue speed
57
58     address.SetBase("10.1.4.0", "255.255.255.252");

```

```

59 PointToPointHelper p2p34;
60 p2p34.SetChannelAttribute("Delay",
TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
61 p2p34.SetDeviceAttribute("DataRate",
DataRateValue(DataRate(Case1_conf::UpstreamLINKCAPACITY)));
62 p2p34.SetDeviceAttribute("InterframeGap", TimeValue(MilliSeconds(0)));
63 queue134 = CreateObject<RDWQueue>(134);
64 queue134->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
Case1_conf::SC3BYTEBUFFER);
65 queue134->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
66 queue134->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
67 queue134->setREDTLLOW(Case1_conf::REDSC1TLLOW, Case1_conf::REDSC2TLLOW,
Case1_conf::REDSC3TLLOW);
68 queue134->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
Case1_conf::REDSC3LDROP);
69 queue134->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
70 Case1_conf::SC2CONGESTIONFATORALPHAL,
71 Case1_conf::SC3CONGESTIONFATORALPHAL);
72 queue134->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
73 Case1_conf::SC2CONGESTIONFATORALPHAH,
74 Case1_conf::SC3CONGESTIONFATORALPHAH);
75 queue134->setBELTA(
76 Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
Case1_conf::CONGESTIONFATBELTA3);
77 address.Assign(p2p34.Install(nodes.Get(3), nodes.Get(4)));
78
nodes.Get(3)->GetDevice(4)->GetObject<PointToPointNetDevice>()->SetQueue(queue134);
79 // 4-5
80
81 address.SetBase("10.1.5.0", "255.255.255.252");
82 PointToPointHelper p2p45;
83 p2p45.SetChannelAttribute("Delay",
TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
84 p2p45.SetDeviceAttribute("DataRate",
DataRateValue(DataRate(Case1_conf::UpstreamLINKCAPACITY)));
85 p2p45.SetDeviceAttribute("InterframeGap", TimeValue(MilliSeconds(0.5)));
86 queue145 = CreateObject<RDWQueue>(145);
87 queue145->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
Case1_conf::SC3BYTEBUFFER);
88 queue145->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
89 queue145->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
90 queue145->setREDTLLOW(Case1_conf::REDSC1TLLOW, Case1_conf::REDSC2TLLOW,
Case1_conf::REDSC3TLLOW);
91 queue145->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
Case1_conf::REDSC3LDROP);
92 queue145->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
93 Case1_conf::SC2CONGESTIONFATORALPHAL,
94 Case1_conf::SC3CONGESTIONFATORALPHAL);
95 queue145->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
96 Case1_conf::SC2CONGESTIONFATORALPHAH,
97 Case1_conf::SC3CONGESTIONFATORALPHAH);
98 queue145->setBELTA(
99 Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
Case1_conf::CONGESTIONFATBELTA3);
100 address.Assign(p2p45.Install(nodes.Get(4), nodes.Get(5)));
101

```

```

nodes.Get(4)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue142
5);
102
103 // 5-6
104
105 address.SetBase("10.1.6.0", "255.255.255.252");
106 PointToPointHelper p2p56;
107 p2p56.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
108 p2p56.SetDeviceAttribute("DataRate",
DataRateValue(DataRate(Case1_conf::DomainsLINKCAPACITY)));
109 p2p56.SetDeviceAttribute("InterframeGap",
TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP)));
110 queue156 = CreateObject<RDWQueue>(156);
111 queue156->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
Case1_conf::SC3BYTEBUFFER);
112 queue156->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
113 queue156->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
114 queue156->setREDTLow(Case1_conf::REDSC1TLow, Case1_conf::REDSC2TLow,
Case1_conf::REDSC3TLow);
115 queue156->setREDLDRop(Case1_conf::REDSC1LDRop, Case1_conf::REDSC2LDRop,
Case1_conf::REDSC3LDRop);
116 queue156->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
Case1_conf::SC2CONGESTIONFATORALPHAL,
Case1_conf::SC3CONGESTIONFATORALPHAL);
117
118 queue156->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
Case1_conf::SC2CONGESTIONFATORALPHAH,
Case1_conf::SC3CONGESTIONFATORALPHAH);
119
120 queue156->setBELTA(
Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
Case1_conf::CONGESTIONFATBELTA3);
121
122 address.Assign(p2p56.Install(nodes.Get(5), nodes.Get(6)));
123
124 nodes.Get(5)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue152
6);
125
126 // 6-8
127
128
129 address.SetBase("10.1.7.0", "255.255.255.252");
130 PointToPointHelper p2p68;
131 p2p68.SetChannelAttribute("Delay",
TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
132 p2p68.SetDeviceAttribute("DataRate",
DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
133 p2p68.SetDeviceAttribute("InterframeGap",
TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+2)));
134 queue168 = CreateObject<RDWQueue>(168);
135 queue168->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
Case1_conf::SC3BYTEBUFFER);
136 queue168->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
137 queue168->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
138 queue168->setREDTLow(Case1_conf::REDSC1TLow, Case1_conf::REDSC2TLow,
Case1_conf::REDSC3TLow);
139 queue168->setREDLDRop(Case1_conf::REDSC1LDRop, Case1_conf::REDSC2LDRop,
Case1_conf::REDSC3LDRop);
140 queue168->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
Case1_conf::SC2CONGESTIONFATORALPHAL,
Case1_conf::SC3CONGESTIONFATORALPHAL);
141
142

```

```

143 queue168->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
144                     Case1_conf::SC2CONGESTIONFATORALPHAH,
145                     Case1_conf::SC3CONGESTIONFATORALPHAH);
146 queue168->setBELTA(
147     Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
148     Case1_conf::CONGESTIONFATBELTA3);
149 address.Assign(p2p68.Install(nodes.Get(6), nodes.Get(8)));

nodes.Get(6)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue168);

150
151 // 8-9
152
153 address.SetBase("10.1.8.0", "255.255.255.252");
154 PointToPointHelper p2p89;
155 p2p89.SetChannelAttribute("Delay",
156     TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
157 p2p89.SetDeviceAttribute("DataRate",
158     DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
159 p2p89.SetDeviceAttribute("InterframeGap",
160     TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+4)));
161 queue189 = CreateObject<RDWQueue>(189);
162 queue189->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
163     Case1_conf::SC3BYTEBUFFER);
164 queue189->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
165     Case1_conf::REDSC3THIGH);
166 queue189->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
167     Case1_conf::REDSC3HDROP);
168 queue189->setREDTLLOW(Case1_conf::REDSC1TLOW, Case1_conf::REDSC2TLOW,
169     Case1_conf::REDSC3TLOW);
170 queue189->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
171     Case1_conf::REDSC3LDROP);
172 queue189->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
173     Case1_conf::SC2CONGESTIONFATORALPHAL,
174     Case1_conf::SC3CONGESTIONFATORALPHAL);
175 queue189->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
176     Case1_conf::SC2CONGESTIONFATORALPHAH,
177     Case1_conf::SC3CONGESTIONFATORALPHAH);
178 queue189->setBELTA(
179     Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
180     Case1_conf::CONGESTIONFATBELTA3);
181 address.Assign(p2p89.Install(nodes.Get(8), nodes.Get(9)));

nodes.Get(8)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue189);

174 // 9-10
175
176 address.SetBase("10.1.9.0", "255.255.255.252");
177 PointToPointHelper p2p910;
178 p2p910.SetChannelAttribute("Delay",
179     TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
180 p2p910.SetDeviceAttribute("DataRate",
181     DataRateValue(DataRate(Case1_conf::DestinationLINKCAPACITY)));
182 p2p910.SetDeviceAttribute("InterframeGap",
183     TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+6)));
184 queue1910 = CreateObject<RDWQueue>(1910);
185 queue1910->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
186     Case1_conf::SC3BYTEBUFFER);
187 queue1910->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
188     Case1_conf::REDSC3THIGH);
189 queue1910->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
190     Case1_conf::REDSC3HDROP);

```



```

185 Case1_conf::REDSC3HDROP);
186 queue1910->setREDTLow(Case1_conf::REDSC1TLow, Case1_conf::REDSC2TLow,
Case1_conf::REDSC3TLow);
187 queue1910->setREDLDRop(Case1_conf::REDSC1LDRop, Case1_conf::REDSC2LDRop,
Case1_conf::REDSC3LDRop);
188 queue1910->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
Case1_conf::SC2CONGESTIONFATORALPHAL,
189 Case1_conf::SC3CONGESTIONFATORALPHAL);
190 queue1910->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
Case1_conf::SC2CONGESTIONFATORALPHAH,
191 Case1_conf::SC3CONGESTIONFATORALPHAH);
192 queue1910->setBELTA(
Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
193 Case1_conf::CONGESTIONFATBELTA3);
194 address.Assign(p2p910.Install(nodes.Get(9), nodes.Get(10)));
195
196 nodes.Get(9)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue1910);

197
198
199 // 9-11
200
201 address.SetBase("10.1.10.0", "255.255.255.252");
202 PointToPointHelper p2p911;
203 p2p911.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
204 p2p911.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000)));
205 address.Assign(p2p911.Install(nodes.Get(9), nodes.Get(11)));
206
207 // 9-12
208
209 address.SetBase("10.1.11.0", "255.255.255.252");
210 PointToPointHelper p2p912;
211 p2p912.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
212 p2p912.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000)));
213 address.Assign(p2p912.Install(nodes.Get(9), nodes.Get(12)));
214
215 // 13-8
216 address.SetBase("10.1.12.0", "255.255.255.252");
217 PointToPointHelper p2p138;
218 p2p138.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
219 p2p138.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
220 address.Assign(p2p138.Install(nodes.Get(13), nodes.Get(8)));
221
222 for (uint32_t i = 0; i < nodes.GetN(); i++) {
223     for (uint32_t j = 1; j < nodes.Get(i)->GetNDevices(); j++) {
224         std::cout<<i<<" "<<nodes.Get(i)->GetObject<Ipv4>()->GetAddress(j,
0).GetLocal()<<"Device"<<nodes.Get(i)->GetNDevices()<<"Get device
index"<<nodes.Get(i)->GetDevice(j)<<std::endl;
225     }
226 }
227
228
229 // sink
230 uint16_t sinkPort = 8080;
231
232 PacketSinkHelper udpSinkHelper("ns3::UdpSocketFactory",
InetSocketAddress(Ipv4Address::GetAny(), sinkPort));
233 PacketSinkHelper tcpSinkHelper10tcp("ns3::TcpSocketFactory",
InetSocketAddress(Ipv4Address::GetAny(), sinkPort));
234
235 ApplicationContainer sinkApps10 = udpSinkHelper.Install(nodes.Get(10));

```

```

236 sinkApps10.Start(Seconds(0.));
237 ApplicationContainer sinkApps10tcp = tcpSinkHelper10tcp.Install(nodes.Get(10));
238 sinkApps10tcp.Start(Seconds(0.));
239
240 ApplicationContainer sinkApps11 = udpSinkHelper.Install(nodes.Get(11));
241 sinkApps11.Start(Seconds(0.));
242 ApplicationContainer sinkApps11tcp = tcpSinkHelper10tcp.Install(nodes.Get(11));
243 sinkApps11tcp.Start(Seconds(0.));
244
245 ApplicationContainer sinkApps12 = udpSinkHelper.Install(nodes.Get(12));
246 sinkApps12.Start(Seconds(0.));
247 ApplicationContainer sinkApps12tcp = tcpSinkHelper10tcp.Install(nodes.Get(12));
248 sinkApps12tcp.Start(Seconds(0.));
249
250
251 // traffic 0
252 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
253 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
254 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
255 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
256 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
257 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
258 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
259 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
260 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
261 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
262
263 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
264 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
265 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
266 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
267 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
268 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
269 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
270 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
271 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
272 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
273
274 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);
275 TrafficFromTo3(0, 10, 3, 3, 1);TrafficFromTo3(0, 10, 3, 3, 1);

```





	2);TrafficFromTo3(0, 10, 4, 4, 2);	
309		
310	TrafficFromTo3(0, 10, 4, 4, 2);TrafficFromTo3(0, 10, 4, 4, 2);	২
311	TrafficFromTo3(0, 10, 4, 4, 2);TrafficFromTo3(0, 10, 4, 4, 2);	২
312	TrafficFromTo3(0, 10, 4, 4, 2);TrafficFromTo3(0, 10, 4, 4, 2);	২
313	TrafficFromTo3(0, 10, 4, 4, 2);TrafficFromTo3(0, 10, 4, 4, 2);	২
314	TrafficFromTo3(0, 10, 4, 4, 2);TrafficFromTo3(0, 10, 4, 4, 2);	২
315	TrafficFromTo3(0, 10, 4, 4, 2);TrafficFromTo3(0, 10, 4, 4, 2);	২
316	TrafficFromTo3(0, 10, 4, 4, 2);TrafficFromTo3(0, 10, 4, 4, 2);	২
317	TrafficFromTo3(0, 10, 4, 4, 2);TrafficFromTo3(0, 10, 4, 4, 2);	২
318	TrafficFromTo3(0, 10, 4, 4, 2);TrafficFromTo3(0, 10, 4, 4, 2);	২
319	TrafficFromTo3(0, 10, 4, 4, 2);TrafficFromTo3(0, 10, 4, 4, 2);	২
320		
321		
322		
323	//+++++	
324	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
325	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
326	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
327	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
328	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
329	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
330	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
331	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
332	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
333	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
334		
335	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
336	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
337	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
338	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
339	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
340	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২
341	TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);	২

```

342     TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);
343     TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);
344     TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);
345     TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);
346     TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);
347     TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);
348     TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);
349     TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);
350     TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);
351     TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);
352     TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);
353     TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);
354     TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);
355     TrafficFromTo3(0, 10, 5, 5, 3);TrafficFromTo3(0, 10, 5, 5, 3);
356
357
358
359
360     // SC4 traffic
361     TrafficFromTo3(13, 10, 6, 6, 4);
362     TrafficFromTo3(13, 10, 6, 6, 4);
363
364
365
366     //=====
367
368     // routing
369     Ipv4GlobalRoutingHelper::PopulateRoutingTables();
370
371     for (double i = 20000; i < Case1_conf::SIM_STOP_Sec * 1000; i += 1000) {
372
373         Simulator::Schedule(MilliSeconds(i), &Case1::ScheduleSchedulingRateUpdate,
374             this);
375
376     }
377
378     Config::ConnectWithoutContext("/NodeList/*/ $ns3::Ipv4L3Protocol/Rx",
379         MakeCallback(&MacRecv));
380 }
381
382 // on-off Application - scenairo 1
383 void Case1::TrafficFromTo1(int from, int to, int ProtocolType, int TrafficType,
384     int ToS)
385 {
386     uint16_t sinkPort = 8080;
387     Address

```

```

387     sinkAddress(InetSocketAddress(nodes.Get(to)->GetObject<Ipv4>()->GetAddress(1,
388     0).GetLocal(), sinkPort));
389     Ptr<Socket> TrafficSocket;
390     Ptr<ToSApp> app1 = CreateObject<ToSApp>(ToS, TrafficType);
391     if (ProtocolType == 3) { // edit 13/05 VoIP traffic
392         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
393         UdpSocketFactory::GetTypeId());
394         app1->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
395         (80000/1600)*Case1_conf::SIM_STOP_Sec, DataRate("80000"),1,1,1); //125000
396     } else if (ProtocolType == 4) { // edit 13/05 Video Conferencing traffic
397         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
398         UdpSocketFactory::GetTypeId());
399         app1->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
400         (384000/11200)*Case1_conf::SIM_STOP_Sec, DataRate("384000"),1,1,1);
401         //500000 //previous rate 2240000
402         // video conferencing rate 128-384 kbits/sec,
403         https://en.wikipedia.org/wiki/Bit_rate
404     } else if (ProtocolType == 5) { // edit 13/05 (FTP) traffic TcpSocketFactory
405         TrafficSocket =
406         Socket::CreateSocket(nodes.Get(from),TcpSocketFactory::GetTypeId());
407         app1->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
408         (256000/2800)*Case1_conf::SIM_STOP_Sec, DataRate("256000"),1,1,1);
409         //179000, // previous rate 10000000
410     } else if (ProtocolType == 6) { // edit 13/05 // Database Query message
411         TrafficSocket =
412         Socket::CreateSocket(nodes.Get(from),UdpSocketFactory::GetTypeId());
413         app1->Setup(TrafficSocket, sinkAddress,
414         Case1_conf::TRAFFICPKTSIZE, (81920/4096)*Case1_conf::SIM_STOP_Sec ,
415         DataRate("81920"),1,1,1); //50000
416     }
417     nodes.Get(from)->AddApplication(app1);
418     app1->SetStartTime(Seconds(1.));
419 }
420 void Case1::TrafficFromTo2(int from, int to, int ProtocolType, int TrafficType,
421 int ToS)
422 {
423     uint16_t sinkPort = 8080;
424     Address
425     sinkAddress(InetSocketAddress(nodes.Get(to)->GetObject<Ipv4>()->GetAddress(1,
426     0).GetLocal(), sinkPort));
427     Ptr<Socket> TrafficSocket;
428     Ptr<ToSApp> app2 = CreateObject<ToSApp>(ToS, TrafficType);
429     if (ProtocolType == 3) { // edit 13/05 VoIP traffic
430         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
431         UdpSocketFactory::GetTypeId());
432         app2->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
433         (80000/1600)*Case1_conf::SIM_STOP_Sec, DataRate("80000"),1,1,2);
434     } else if (ProtocolType == 4) { // edit 13/05 Video Conferencing traffic
435         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
436         UdpSocketFactory::GetTypeId());
437         app2->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
438         (384000/11200)*Case1_conf::SIM_STOP_Sec, DataRate("384000"),1,1,2);
439         //previous rate 2240000
440         // video conferencing rate 128-384 kbits/sec,
441         https://en.wikipedia.org/wiki/Bit_rate

```

```

424 } else if (ProtocolType == 5) { // edit 13/05 (FTP) traffic TcpSocketFactory
425     TrafficSocket =
426     Socket::CreateSocket(nodes.Get(from), TcpSocketFactory::GetTypeId());
427     app2->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
428     (256000/2800)*Case1_conf::SIM_STOP_Sec, DataRate("256000"), 1, 1, 2);
429 } else if (ProtocolType == 6) { // edit 13/05 // Database Query message
430     traffic - Best effort traffic
431     TrafficSocket =
432     Socket::CreateSocket(nodes.Get(from), UdpSocketFactory::GetTypeId());
433     app2->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
434     (81920/4096)*Case1_conf::SIM_STOP_Sec, DataRate("81920"), 1, 1, 2);
435 }
436 nodes.Get(from)->AddApplication(app2);
437 app2->SetStartTime(Seconds(1.));
438 }
439 void Case1::TrafficFromTo3(int from, int to, int ProtocolType, int TrafficType,
440 int ToS)
441 {
442     uint16_t sinkPort = 8080;
443     Address
444     sinkAddress(InetSocketAddress(nodes.Get(to)->GetObject<Ipv4>()->GetAddress(1,
445     0).GetLocal(), sinkPort));
446     Ptr<Socket> TrafficSocket;
447     Ptr<ToSApp> app3 = CreateObject<ToSApp>(ToS, TrafficType);
448     if (ProtocolType == 3) { // edit 13/05 VoIP traffic
449         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
450         UdpSocketFactory::GetTypeId());
451         app3->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
452         (80000/1600)*Case1_conf::SIM_STOP_Sec, DataRate("80000"), 1, 1, 3); //125000
453     } else if (ProtocolType == 4) { // edit 13/05 Video Conferencing traffic
454         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
455         UdpSocketFactory::GetTypeId());
456         app3->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
457         (384000/11200)*Case1_conf::SIM_STOP_Sec, DataRate("384000"), 1, 1, 3);
458         //500000 //previous rate 2240000
459         // video conferencing rate 128-384 kbits/sec,
460         https://en.wikipedia.org/wiki/Bit\_rate
461     }
462     else if (ProtocolType == 5) { // edit 13/05 (FTP) traffic TcpSocketFactory
463         TrafficSocket =
464         Socket::CreateSocket(nodes.Get(from), TcpSocketFactory::GetTypeId());
465         app3->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
466         (256000/2800)*Case1_conf::SIM_STOP_Sec, DataRate("256000"), 1, 1, 3); //179000
467     } else if (ProtocolType == 6) { // edit 13/05 // Database Query message
468         traffic - Best effort traffic
469         TrafficSocket =
470         Socket::CreateSocket(nodes.Get(from), TcpSocketFactory::GetTypeId());
471         app3->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
472         (81920/4096)*Case1_conf::SIM_STOP_Sec, DataRate("81920"), 1, 1, 3); //50000
473     }
474     nodes.Get(from)->AddApplication(app3);
475     app3->SetStartTime(Seconds(1.));
476 }

```



```

466 //////////////////////////////////////////////////
467 void Case1::ScheduleSchedulingRateUpdate(void)
468 {
469     //either section A or section B work during running the simulation.
470     // Section A
471     queue156->SetSchedulingRateByFactor(1, queue168->GetQueueSizeBytes(1) /
472     (float)Case1_conf::SC1BYTEBUFFER);
473     queue156->SetSchedulingRateByFactor(2, queue168->GetQueueSizeBytes(2) /
474     (float)Case1_conf::SC2BYTEBUFFER);
475     queue156->SetSchedulingRateByFactor(3, queue168->GetQueueSizeBytes(3) /
476     (float)Case1_conf::SC3BYTEBUFFER);
477
478     // Section B
479     //set by linear factor
480     queue156->SetSchedulingRateByLinearFactor(1, queue168->GetQueueSizeBytes(1));
481     queue156->SetSchedulingRateByLinearFactor(2, queue168->GetQueueSizeBytes(2));
482     queue156->SetSchedulingRateByLinearFactor(3, queue168->GetQueueSizeBytes(3));
483
484 }
485
486 Case1::~Case1()
487 {
488     // dtor
489 }
490
491

```

**A-5.2: Test Scenario 2, Topology Simulation Diagram and NS3 Code:**

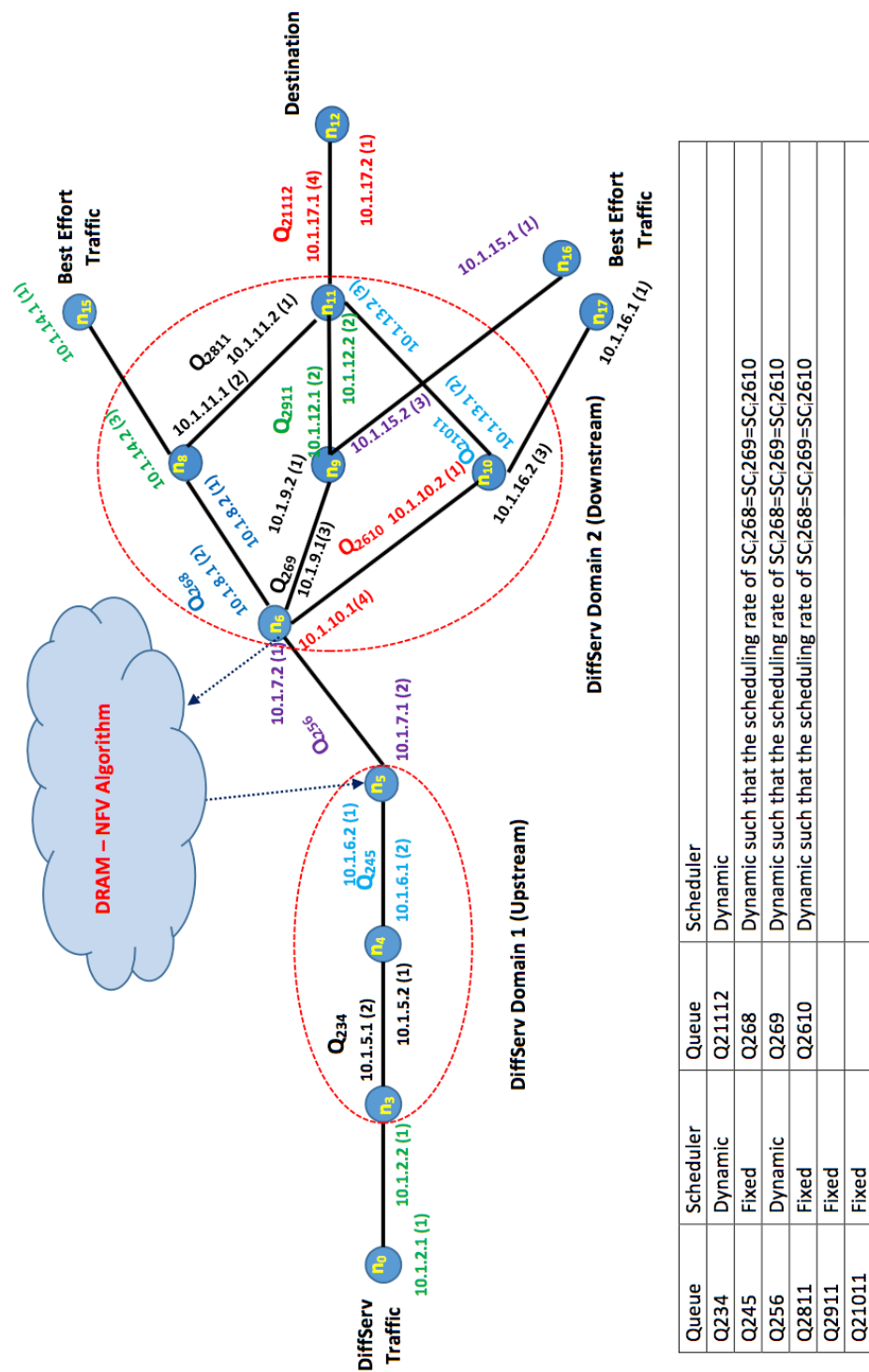


Figure 9-9, test scenario 2 simulation network topology diagram.

```

1  #ifndef CASE2_H
2  #define CASE2_H
3
4  #include "ns3/core-module.h"
5  #include "ns3/network-module.h"
6  #include "ns3/internet-module.h"
7  #include "ns3/point-to-point-module.h"
8  #include "ns3/applications-module.h"
9  #include "ns3/csma-module.h"
10 #include "RDWQueue.h"
11
12 using namespace ns3;
13
14 class Case2
15 {
16     public:
17         Case2();
18         virtual ~Case2();
19
20         NodeContainer nodes;
21
22         Ptr<RDWQueue> queue234;
23         Ptr<RDWQueue> queue245;
24         Ptr<RDWQueue> queue256;
25         Ptr<RDWQueue> queue268;
26         Ptr<RDWQueue> queue269;
27         Ptr<RDWQueue> queue2610;
28         Ptr<RDWQueue> queue2811;
29         Ptr<RDWQueue> queue2911;
30         Ptr<RDWQueue> queue21011;
31         Ptr<RDWQueue> queue21112;
32
33         void ScheduleSchedulingRateUpdate(void);
34         //void TrafficFromTo(int from, int to, int ProtocolType ,int TrafficType, int ToS);
35         void TrafficFromTo1(int from, int to, int ProtocolType ,int TrafficType, int ToS);
36         void TrafficFromTo2(int from, int to, int ProtocolType ,int TrafficType, int ToS);
37         void TrafficFromTo3(int from, int to, int ProtocolType ,int TrafficType, int ToS);
38         void AverageScheduleRate(void);
39
40         double MinimumAverageQueueDelay(double x, double y, double z);
41         double MaximumAverageQueueLength(double x, double y, double z);
42
43 };
44
45 #endif // CASE2_H
46

```



```

1  #include "Case2.h"
2  #include "TosApp.h"
3  #include "Case1_conf.h"
4
5  using namespace ns3;
6
7  static void MacRecv(const Ptr<const Packet> packet, Ptr<Ipv4> ipv4, const uint32_t &
interface)
8  {
9      Ipv4Header header;
10     packet->PeekHeader(header);
11     if (header.GetDestination().IsEqual(ipv4->GetAddress(interface, 0).GetLocal())) {
12         QosTag tosTag;
13         uint32_t band = 0;
14         if (packet->PeekPacketTag(tosTag)) {
15             band = (uint32_t)tosTag.GetTid();
16         }
17         if (band != 0 ) {
18             std::cout << "Pkt_rcv " << packet->GetUid() << " " <<
Simulator::Now() << " " << band << std::endl;
19         }
20     }
21 }
22
23 Case2::Case2()
24 {
25     Config::SetDefault("ns3::Ipv4GlobalRouting::RandomEcmpRouting",
BooleanValue(true));
26     nodes.Create(18);
27
28     InternetStackHelper stack;
29     stack.Install(nodes);
30
31     Ipv4AddressHelper address;
32     address.SetBase("10.1.1.0", "255.255.255.0");
33
34     // 0-3
35     address.NewNetwork();
36     PointToPointHelper p2p03;
37     p2p03.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
38     p2p03.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
39     address.Assign(p2p03.Install(nodes.Get(0), nodes.Get(3)));
40
41
42
43     //1-3 Not Used
44     address.NewNetwork();
45     PointToPointHelper p2p13;
46     p2p13.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
47     p2p13.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
48     address.Assign(p2p13.Install(nodes.Get(1), nodes.Get(3)));
49
50     //2-3 Not Used
51     address.NewNetwork();
52     PointToPointHelper p2p23;
53     p2p23.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
54     p2p23.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
55     address.Assign(p2p23.Install(nodes.Get(2), nodes.Get(3)));
56
57     // 3-4
58     address.NewNetwork();

```

```

59     PointToPointHelper p2p34;
60     p2p34.SetChannelAttribute("Delay",
                               TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
61     p2p34.SetDeviceAttribute("DataRate",
                               DataRateValue(DataRate(Case1_conf::UpstreamLINKCAPACITY)));
62     p2p34.SetDeviceAttribute("InterframeGap", TimeValue(MilliSeconds(0)));
63     address.Assign(p2p34.Install(nodes.Get(3), nodes.Get(4)));
64
65     //4-5
66     address.NewNetwork();
67     PointToPointHelper p2p45;
68     p2p45.SetChannelAttribute("Delay",
                               TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
69     p2p45.SetDeviceAttribute("DataRate",
                               DataRateValue(DataRate(Case1_conf::UpstreamLINKCAPACITY)));
70     p2p45.SetDeviceAttribute("InterframeGap", TimeValue(MilliSeconds(0.5)));
71     address.Assign(p2p45.Install(nodes.Get(4), nodes.Get(5)));
72
73     //5-6
74     address.NewNetwork();
75     PointToPointHelper p2p56;
76     p2p56.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
77     p2p56.SetDeviceAttribute("DataRate",
                               DataRateValue(DataRate(Case1_conf::DomainsLINKCAPACITY)));
78     p2p56.SetDeviceAttribute("InterframeGap",
                               TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP)));
79     address.Assign(p2p56.Install(nodes.Get(5), nodes.Get(6)));
80
81     //6-8
82     address.NewNetwork();
83     PointToPointHelper p2p68;
84     p2p68.SetChannelAttribute("Delay",
                               TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
85     p2p68.SetDeviceAttribute("DataRate",
                               DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
86     p2p68.SetDeviceAttribute("InterframeGap",
                               TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+2)));
87     address.Assign(p2p68.Install(nodes.Get(6), nodes.Get(8)));
88
89     //6-9
90     address.NewNetwork();
91     PointToPointHelper p2p69;
92     p2p69.SetChannelAttribute("Delay",
                               TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
93     p2p69.SetDeviceAttribute("DataRate",
                               DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
94     p2p69.SetDeviceAttribute("InterframeGap",
                               TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+2)));
95
96     address.Assign(p2p69.Install(nodes.Get(6), nodes.Get(9)));
97
98     //6-10
99     address.NewNetwork();
100    PointToPointHelper p2p610;
101    p2p610.SetChannelAttribute("Delay",
                               TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
102    p2p610.SetDeviceAttribute("DataRate",
                               DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
103    p2p610.SetDeviceAttribute("InterframeGap",
                               TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+2)));
104

```

```

105 address.Assign(p2p610.Install(nodes.Get(6), nodes.Get(10)));
106
107 //8-11
108 address.NewNetwork();
109 PointToPointHelper p2p811;
110 p2p811.SetChannelAttribute("Delay", TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
111 p2p811.SetDeviceAttribute("DataRate", DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
112 p2p811.SetDeviceAttribute("InterframeGap", TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+4)));
113 address.Assign(p2p811.Install(nodes.Get(8), nodes.Get(11)));
114
115 //9-11
116 address.NewNetwork();
117 PointToPointHelper p2p911;
118 p2p911.SetChannelAttribute("Delay", TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
119 p2p911.SetDeviceAttribute("DataRate", DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
120 p2p911.SetDeviceAttribute("InterframeGap", TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+4)));
121 address.Assign(p2p911.Install(nodes.Get(9), nodes.Get(11)));
122
123 //10-11
124 address.NewNetwork();
125 PointToPointHelper p2p1011;
126 p2p1011.SetChannelAttribute("Delay", TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
127 p2p1011.SetDeviceAttribute("DataRate", DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
128 p2p1011.SetDeviceAttribute("InterframeGap", TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+4)));
129 address.Assign(p2p1011.Install(nodes.Get(10), nodes.Get(11)));
130
131 //8-15
132 address.NewNetwork();
133 PointToPointHelper p2p815;
134 p2p815.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
135 p2p815.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
136 address.Assign(p2p815.Install(nodes.Get(15), nodes.Get(8)));
137
138 //9-16
139 address.NewNetwork();
140 PointToPointHelper p2p916;
141 p2p916.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
142 p2p916.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
143 address.Assign(p2p916.Install(nodes.Get(16), nodes.Get(9)));
144
145 //10-17
146 address.NewNetwork();
147 PointToPointHelper p2p1017;
148 p2p1017.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
149 p2p1017.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
150 address.Assign(p2p1017.Install(nodes.Get(17), nodes.Get(10)));
151
152 //11-12
153 address.NewNetwork();
154 PointToPointHelper p2p1112;
155 p2p1112.SetChannelAttribute("Delay", TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));

```

```

156 p2p1112.SetDeviceAttribute("DataRate",
DataRateValue(DataRate(Case1_conf::DestinationLINKCAPACITY)));
157 p2p1112.SetDeviceAttribute("InterframeGap",
TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+6)));
158 address.Assign(p2p1112.Install(nodes.Get(11), nodes.Get(12)));
159
160 //11-13 Not used
161 address.NewNetwork();
162 PointToPointHelper p2p1113;
163 p2p1113.SetChannelAttribute("Delay",
TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
164 p2p1113.SetDeviceAttribute("DataRate",
DataRateValue(DataRate(Case1_conf::LINKCAPACITY)));
165 address.Assign(p2p1113.Install(nodes.Get(11), nodes.Get(13)));
166 address.NewNetwork();
167
168 //11-14 Not used
169 PointToPointHelper p2p1114;
170 p2p1114.SetChannelAttribute("Delay",
TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
171 p2p1114.SetDeviceAttribute("DataRate",
DataRateValue(DataRate(Case1_conf::LINKCAPACITY)));
172 address.Assign(p2p1114.Install(nodes.Get(11), nodes.Get(14)));
173 address.NewNetwork();
174
175 // 3-4
176 // notice: queue need to be set before install!!!
177 // you can't get the queue from outside the queue, so you need to update the
parameter in the queue function.
178 // you need to set InterframeGap to decide the queue speed
179
180 queue234 = CreateObject<RDWQueue>(234);
181 queue234->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
Case1_conf::SC3BYTEBUFFER);
182 queue234->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
183 queue234->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
184 queue234->setREDTLLOW(Case1_conf::REDSC1TLLOW, Case1_conf::REDSC2TLLOW,
Case1_conf::REDSC3TLLOW);
185 queue234->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
Case1_conf::REDSC3LDROP);
186 queue234->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
Case1_conf::SC2CONGESTIONFATORALPHAL,
Case1_conf::SC3CONGESTIONFATORALPHAL);
187 queue234->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
Case1_conf::SC2CONGESTIONFATORALPHAH,
Case1_conf::SC3CONGESTIONFATORALPHAH);
188
189 queue234->setBELTA(
190 Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
Case1_conf::CONGESTIONFATBELTA3);
191
192 nodes.Get(3)->GetDevice(4)->GetObject<PointToPointNetDevice>()->SetQueue(queue234);
193
194 // 4-5
195
196 queue245 = CreateObject<RDWQueue>(245);
197 queue245->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
Case1_conf::SC3BYTEBUFFER);
198 queue245->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
199
200

```



```

201 Case1_conf::REDSCTHIGHTH);
queue245->setREDHDROP(Case1_conf::REDSCTHIGHTH, Case1_conf::REDSCTHIGHTH,
Case1_conf::REDSCTHIGHTH);
202 queue245->setREDTLLOW(Case1_conf::REDSCTHIGHTH, Case1_conf::REDSCTHIGHTH,
Case1_conf::REDSCTHIGHTH);
203 queue245->setREDLDRP(Case1_conf::REDSCTHIGHTH, Case1_conf::REDSCTHIGHTH,
Case1_conf::REDSCTHIGHTH);
204 queue245->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
205 Case1_conf::SC2CONGESTIONFATORALPHAL,
206 Case1_conf::SC3CONGESTIONFATORALPHAL);
207 queue245->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
208 Case1_conf::SC2CONGESTIONFATORALPHAH,
209 Case1_conf::SC3CONGESTIONFATORALPHAH);
210 queue245->setBELTA(
211 Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
Case1_conf::CONGESTIONFATBELTA3);
212
nodes.Get(4)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue245);
213
214 // 5-6
215 queue256 = CreateObject<RDWQueue>(256);
216 queue256->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
Case1_conf::SC3BYTEBUFFER);
217 queue256->setREDTHIGHTH(Case1_conf::REDSCTHIGHTH, Case1_conf::REDSCTHIGHTH,
Case1_conf::REDSCTHIGHTH);
218 queue256->setREDHDROP(Case1_conf::REDSCTHIGHTH, Case1_conf::REDSCTHIGHTH,
Case1_conf::REDSCTHIGHTH);
219 queue256->setREDTLLOW(Case1_conf::REDSCTHIGHTH, Case1_conf::REDSCTHIGHTH,
Case1_conf::REDSCTHIGHTH);
220 queue256->setREDLDRP(Case1_conf::REDSCTHIGHTH, Case1_conf::REDSCTHIGHTH,
Case1_conf::REDSCTHIGHTH);
221 queue256->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
222 Case1_conf::SC2CONGESTIONFATORALPHAL,
223 Case1_conf::SC3CONGESTIONFATORALPHAL);
224 queue256->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
225 Case1_conf::SC2CONGESTIONFATORALPHAH,
226 Case1_conf::SC3CONGESTIONFATORALPHAH);
227 queue256->setBELTA(
228 Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
Case1_conf::CONGESTIONFATBELTA3);
229
nodes.Get(5)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue256);
230
231 // 6-8
232
233 queue268 = CreateObject<RDWQueue>(268);
234 queue268->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
Case1_conf::SC3BYTEBUFFER);
235 queue268->setREDTHIGHTH(Case1_conf::REDSCTHIGHTH, Case1_conf::REDSCTHIGHTH,
Case1_conf::REDSCTHIGHTH);
236 queue268->setREDHDROP(Case1_conf::REDSCTHIGHTH, Case1_conf::REDSCTHIGHTH,
Case1_conf::REDSCTHIGHTH);
237 queue268->setREDTLLOW(Case1_conf::REDSCTHIGHTH, Case1_conf::REDSCTHIGHTH,
Case1_conf::REDSCTHIGHTH);
238 queue268->setREDLDRP(Case1_conf::REDSCTHIGHTH, Case1_conf::REDSCTHIGHTH,
Case1_conf::REDSCTHIGHTH);
239 queue268->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
240 Case1_conf::SC2CONGESTIONFATORALPHAL,
241 Case1_conf::SC3CONGESTIONFATORALPHAL);

```

```

242 queue268->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
243                     Case1_conf::SC2CONGESTIONFATORALPHAH,
244                     Case1_conf::SC3CONGESTIONFATORALPHAH);
245 queue268->setBELTA(
246     Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
247     Case1_conf::CONGESTIONFATBELTA3);
248
249 nodes.Get(6)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue268);
250
251 // 6-9
252 queue269 = CreateObject<RDWQueue>(269);
253 queue269->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
254                         Case1_conf::SC3BYTEBUFFER);
255 queue269->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
256                      Case1_conf::REDSC3THIGH);
257 queue269->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
258                      Case1_conf::REDSC3HDROP);
259 queue269->setREDTLLOW(Case1_conf::REDSC1TLLOW, Case1_conf::REDSC2TLLOW,
260                      Case1_conf::REDSC3TLLOW);
261 queue269->setREDLDRDP(Case1_conf::REDSC1LDRDP, Case1_conf::REDSC2LDRDP,
262                      Case1_conf::REDSC3LDRDP);
263 queue269->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
264                    Case1_conf::SC2CONGESTIONFATORALPHAL,
265                    Case1_conf::SC3CONGESTIONFATORALPHAL);
266 queue269->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
267                    Case1_conf::SC2CONGESTIONFATORALPHAH,
268                    Case1_conf::SC3CONGESTIONFATORALPHAH);
269 queue269->setBELTA(
270     Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
271     Case1_conf::CONGESTIONFATBELTA3);
272
273 nodes.Get(6)->GetDevice(3)->GetObject<PointToPointNetDevice>()->SetQueue(queue269);
274
275 // 6-10
276 queue2610 = CreateObject<RDWQueue>(2610);
277 queue2610->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
278                          Case1_conf::SC3BYTEBUFFER);
279 queue2610->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
280                       Case1_conf::REDSC3THIGH);
281 queue2610->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
282                       Case1_conf::REDSC3HDROP);
283 queue2610->setREDTLLOW(Case1_conf::REDSC1TLLOW, Case1_conf::REDSC2TLLOW,
284                       Case1_conf::REDSC3TLLOW);
285 queue2610->setREDLDRDP(Case1_conf::REDSC1LDRDP, Case1_conf::REDSC2LDRDP,
286                       Case1_conf::REDSC3LDRDP);
287 queue2610->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
288                     Case1_conf::SC2CONGESTIONFATORALPHAL,
289                     Case1_conf::SC3CONGESTIONFATORALPHAL);
290 queue2610->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
291                     Case1_conf::SC2CONGESTIONFATORALPHAH,
292                     Case1_conf::SC3CONGESTIONFATORALPHAH);
293 queue2610->setBELTA(
294     Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
295     Case1_conf::CONGESTIONFATBELTA3);
296
297 nodes.Get(6)->GetDevice(4)->GetObject<PointToPointNetDevice>()->SetQueue(queue2610);
298
299 // 8-11

```

```

284     queue2811 = CreateObject<RDWQueue>(2811);
285     queue2811->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
Case1_conf::SC3BYTEBUFFER);
286     queue2811->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
287     queue2811->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
288     queue2811->setREDTLOW(Case1_conf::REDSC1TLOW, Case1_conf::REDSC2TLOW,
Case1_conf::REDSC3TLOW);
289     queue2811->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
Case1_conf::REDSC3LDROP);
290     queue2811->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
Case1_conf::SC2CONGESTIONFATORALPHAL,
Case1_conf::SC3CONGESTIONFATORALPHAL);
291     queue2811->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
Case1_conf::SC2CONGESTIONFATORALPHAH,
Case1_conf::SC3CONGESTIONFATORALPHAH);
292     queue2811->setBELTA(
Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
Case1_conf::CONGESTIONFATBELTA3);
293
294     nodes.Get(8)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue2811);
295
296     // 9-11
297     queue2911 = CreateObject<RDWQueue>(2911);
298     queue2911->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
Case1_conf::SC3BYTEBUFFER);
299     queue2911->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
300     queue2911->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
301     queue2911->setREDTLOW(Case1_conf::REDSC1TLOW, Case1_conf::REDSC2TLOW,
Case1_conf::REDSC3TLOW);
302     queue2911->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
Case1_conf::REDSC3LDROP);
303     queue2911->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
Case1_conf::SC2CONGESTIONFATORALPHAL,
Case1_conf::SC3CONGESTIONFATORALPHAL);
304     queue2911->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
Case1_conf::SC2CONGESTIONFATORALPHAH,
Case1_conf::SC3CONGESTIONFATORALPHAH);
305     queue2911->setBELTA(
Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
Case1_conf::CONGESTIONFATBELTA3);
306
307     nodes.Get(9)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue2911);
308
309     // 10-11
310     queue21011 = CreateObject<RDWQueue>(21011);
311     queue21011->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER,
Case1_conf::SC2BYTEBUFFER, Case1_conf::SC3BYTEBUFFER);
312     queue21011->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
313     queue21011->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
314     queue21011->setREDTLOW(Case1_conf::REDSC1TLOW, Case1_conf::REDSC2TLOW,
Case1_conf::REDSC3TLOW);
315     queue21011->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
Case1_conf::REDSC3LDROP);

```



```

324 queue21011->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
325                      Case1_conf::SC2CONGESTIONFATORALPHAL,
326                      Case1_conf::SC3CONGESTIONFATORALPHAL);
327 queue21011->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
328                      Case1_conf::SC2CONGESTIONFATORALPHAH,
329                      Case1_conf::SC3CONGESTIONFATORALPHAH);
330 queue21011->setBELTA(
331     Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
332     Case1_conf::CONGESTIONFATBELTA3);
333
334 // 11-12
335 queue21112 = CreateObject<RDWQueue>(21112);
336 queue21112->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER,
337                            Case1_conf::SC2BYTEBUFFER, Case1_conf::SC3BYTEBUFFER);
338 queue21112->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
339                          Case1_conf::REDSC3THIGH);
340 queue21112->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
341                          Case1_conf::REDSC3HDROP);
342 queue21112->setREDTLLOW(Case1_conf::REDSC1TLLOW, Case1_conf::REDSC2TLLOW,
343                          Case1_conf::REDSC3TLLOW);
344 queue21112->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
345                          Case1_conf::REDSC3LDROP);
346 queue21112->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
347                      Case1_conf::SC2CONGESTIONFATORALPHAL,
348                      Case1_conf::SC3CONGESTIONFATORALPHAL);
349 queue21112->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
350                      Case1_conf::SC2CONGESTIONFATORALPHAH,
351                      Case1_conf::SC3CONGESTIONFATORALPHAH);
352 queue21112->setBELTA(
353     Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
354     Case1_conf::CONGESTIONFATBELTA3);
355
356 nodes.Get(11)->GetDevice(4)->GetObject<PointToPointNetDevice>()->SetQueue(queue21112);
357
358 for (uint32_t i = 0; i < nodes.GetN(); i++) {
359     for (uint32_t j = 1; j < nodes.Get(i)->GetNDevices(); j++) {
360         std::cout<<i<<" "<<nodes.Get(i)->GetObject<Ipv4>()->GetAddress(j,
361                                     0).GetLocal()<<"Device"<<nodes.Get(i)->GetNDevices()<<"Get device
362                                     index"<<nodes.Get(i)->GetDevice(j)<<std::endl;
363     }
364 }
365
366 Ipv4GlobalRoutingHelper::PopulateRoutingTables();
367
368 // sink
369 uint16_t sinkPort = 8080;
370
371 PacketSinkHelper udpSinkHelper("ns3::UdpSocketFactory",
372                                InetSocketAddress(Ipv4Address::GetAny(), sinkPort));
373 PacketSinkHelper tcpSinkHelper12tcp("ns3::TcpSocketFactory",
374                                     InetSocketAddress(Ipv4Address::GetAny(), sinkPort));
375 ApplicationContainer sinkApps12 = udpSinkHelper.Install(nodes.Get(12));
376 sinkApps12.Start(Seconds(0.));
377 ApplicationContainer sinkApps12tcp = tcpSinkHelper12tcp.Install(nodes.Get(12));
378 sinkApps12tcp.Start(Seconds(0.));
379
380 ApplicationContainer sinkApps13 = udpSinkHelper.Install(nodes.Get(13));

```

```

370 sinkApps13.Start(Seconds(0.));
371 ApplicationContainer sinkApps13tcp = tcpSinkHelper12tcp.Install(nodes.Get(13));
372 sinkApps13tcp.Start(Seconds(0.));
373
374 ApplicationContainer sinkApps14 = udpSinkHelper.Install(nodes.Get(14));
375 sinkApps14.Start(Seconds(0.));
376 ApplicationContainer sinkApps14tcp = tcpSinkHelper12tcp.Install(nodes.Get(14));
377 sinkApps14tcp.Start(Seconds(0.));
378
379 // SC1 traffic
380 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
381 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
382 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
383 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
384 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
385 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
386 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
387 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
388 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
389 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
390
391 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
392 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
393 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
394 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
395 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
396 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
397 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
398 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
399 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
400 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
401
402 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
403 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
404 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
405 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);
406 TrafficFromTo3(0, 12, 3, 3, 1);TrafficFromTo3(0, 12, 3, 3, 1);

```













```

536         TrafficFromTo3(0, 12, 5, 5, 3);TrafficFromTo3(0, 12, 5, 5, 3);TrafficFromTo3(0, 12, 5, 5, 3);
537
538
539         // SC4 traffic
540         TrafficFromTo3(15, 12, 6, 6, 4); TrafficFromTo3(15, 12, 6, 6, 4);TrafficFromTo3(15, 12, 6, 6, 4);
541         TrafficFromTo3(16, 12, 6, 6, 4); TrafficFromTo3(16, 12, 6, 6, 4);TrafficFromTo3(16, 12, 6, 6, 4);
542         TrafficFromTo3(17, 12, 6, 6, 4); TrafficFromTo3(17, 12, 6, 6, 4);TrafficFromTo3(17, 12, 6, 6, 4);
543
544
545
546         for (double i = 20000; i < Case1_conf::SIM_STOP_Sec * 1000; i += 1000) {
547             Simulator::Schedule(MilliSeconds(i), &Case2::ScheduleSchedulingRateUpdate, this);
548         }
549
550         for (double i = 10001; i < Case1_conf::SIM_STOP_Sec * 1000; i += 1000) { //1000
551             Simulator::Schedule(MilliSeconds(i), &Case2::AverageScheduleRate, this);
552         }
553
554
555
556
557         Config::ConnectWithoutContext("/NodeList/*/s3::Ipv4L3Protocol/Rx", MakeCallback(&MacRecv));
558     }
559
560     void Case2::TrafficFromTo1(int from, int to, int ProtocolType, int TrafficType, int ToS)
561     {
562         uint16_t sinkPort = 8080;
563         Address sinkAddress(InetSocketAddress(nodes.Get(to)->GetObject<Ipv4>()->GetAddress(1, 0).GetLocal(), sinkPort));
564         Ptr<Socket> TrafficSocket;
565         Ptr<ToSApp> app1 = CreateObject<ToSApp>(ToS, TrafficType);
566         if (ProtocolType == 3) { // edit 13/05 VoIP traffic
567             TrafficSocket = Socket::CreateSocket(nodes.Get(from), UdpSocketFactory::GetTypeId());
568             app1->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE, (80000/1600)*Case1_conf::SIM_STOP_Sec, DataRate("80000"),1,1,1);
569
570         } else if (ProtocolType == 4) { // edit 13/05 Video Conferencing traffic
571             TrafficSocket = Socket::CreateSocket(nodes.Get(from), UdpSocketFactory::GetTypeId());
572             app1->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE, (384000/11200)*Case1_conf::SIM_STOP_Sec, DataRate("384000"),1,1,1);
573             // video conferencing rate 128-384 kbits/sec,
574             // https://en.wikipedia.org/wiki/Bit_rate
575         } else if (ProtocolType == 5) { // edit 13/05 (FTP) traffic TcpSocketFactory
576             TrafficSocket = Socket::CreateSocket(nodes.Get(from), TcpSocketFactory::GetTypeId());
577             app1->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE, (256000/2800)*Case1_conf::SIM_STOP_Sec, DataRate("256000"),1,1,1);
578
579         } else if (ProtocolType == 6) { // edit 13/05 // Database Query message traffic - Best effort traffic
580             TrafficSocket =

```

```

580         Socket::CreateSocket(nodes.Get(from),UdpSocketFactory::GetTypeId());
        app1->Setup(TrafficSocket, sinkAddress,
        Case1_conf::TRAFFICPKTSIZE, (81920/4096)*Case1_conf::SIM_STOP_Sec ,
        DataRate("81920"),1,1,1);
581     }
582     nodes.Get(from)->AddApplication(app1);
583
584     app1->SetStartTime(Seconds(1.));
585 }
586 void Case2::TrafficFromTo2(int from, int to, int ProtocolType, int TrafficType,
587 int ToS)
588 {
589     uint16_t sinkPort = 8080;
590     Address
        sinkAddress(InetSocketAddress(nodes.Get(to)->GetObject<Ipv4>()->GetAddress(1,
        0).GetLocal(), sinkPort));
591     Ptr<Socket> TrafficSocket;
592     Ptr<TosApp> app2 = CreateObject<TosApp>(ToS, TrafficType);
593     if (ProtocolType == 3) { // edit 13/05 VoIP traffic
594         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
        UdpSocketFactory::GetTypeId());
595         app2->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
        (80000/1600)*Case1_conf::SIM_STOP_Sec, DataRate("80000"),1,1,2);
596     } else if (ProtocolType == 4) { // edit 13/05 Video Conferencing traffic
597         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
        UdpSocketFactory::GetTypeId());
598         app2->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
        (384000/11200)*Case1_conf::SIM_STOP_Sec, DataRate("384000"),1,1,2);
600         // video conferencing rate 128-384 kbits/sec,
        // https://en.wikipedia.org/wiki/Bit_rate
601     } else if (ProtocolType == 5) { // edit 13/05 (FTP) traffic TcpSocketFactory
602         TrafficSocket =
        Socket::CreateSocket(nodes.Get(from),TcpSocketFactory::GetTypeId());
603         app2->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
        (256000/2800)*Case1_conf::SIM_STOP_Sec, DataRate("256000"),1,1,2);
604     } else if (ProtocolType == 6) { // edit 13/05 // Database Query message
        traffic - Best effort traffic
606         TrafficSocket =
        Socket::CreateSocket(nodes.Get(from),TcpSocketFactory::GetTypeId());
607         app2->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
        (81920/4096)*Case1_conf::SIM_STOP_Sec, DataRate("81920"),1,1,2);
608     }
609     nodes.Get(from)->AddApplication(app2);
610
611     app2->SetStartTime(Seconds(1.));
612 }
613 void Case2::TrafficFromTo3(int from, int to, int ProtocolType, int TrafficType,
614 int ToS)
615 {
616     uint16_t sinkPort = 8080;
        Address
        sinkAddress(InetSocketAddress(nodes.Get(to)->GetObject<Ipv4>()->GetAddress(1,
        0).GetLocal(), sinkPort));
617     Ptr<Socket> TrafficSocket;
618     Ptr<TosApp> app3 = CreateObject<TosApp>(ToS, TrafficType);
619     if (ProtocolType == 3) { // edit 13/05 VoIP traffic
620         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
        UdpSocketFactory::GetTypeId());

```

```

621         app3->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
622                     (80000/1600)*Case1_conf::SIM_STOP_Sec, DataRate("80000"),1,1,3);
623     } else if (ProtocolType == 4) { // edit 13/05    Video Conferencing traffic
624         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
        UDPsocketFactory::GetTypeId());
625         app3->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
        (384000/11200)*Case1_conf::SIM_STOP_Sec, DataRate("384000"),1,1,3);
626         // video conferencing rate 128-384 kbits/sec,
        https://en.wikipedia.org/wiki/Bit_rate
627     } else if (ProtocolType == 5) { // edit 13/05    (FTP) traffic TcpSocketFactory
628         TrafficSocket =
        Socket::CreateSocket(nodes.Get(from),TcpSocketFactory::GetTypeId());
629         app3->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
        (256000/2800)*Case1_conf::SIM_STOP_Sec, DataRate("256000"),1,1,3);
630
631     } else if (ProtocolType == 6) { // edit 13/05    // Database Query message
        traffic - Best effort traffic
632         TrafficSocket =
        Socket::CreateSocket(nodes.Get(from),TcpSocketFactory::GetTypeId());
633         app3->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
        (81920/4096)*Case1_conf::SIM_STOP_Sec, DataRate("81920"),1,1,3);
634
635     }
636     nodes.Get(from)->AddApplication(app3);
637
638     app3->SetStartTime(Seconds(1.));
639
640
641 }
642
643 void Case2::ScheduleSchedulingRateUpdate(void)
644 {
645     double length68[4], length69[4], length610[4];
646     float AverageQueueLengthMaximum[4];
647     for (int i = 1; i <= 3; i++) {
648         length68[i] = queue268->GetAverageQueueLength1(i);
649         length69[i] = queue269->GetAverageQueueLength1(i);
650         length610[i] = queue2610->GetAverageQueueLength1(i);
651         AverageQueueLengthMaximum[i] = MaximumAverageQueueLength (length68[i],
        length69[i], length610[i]);
652         std::cout<<"length68:"<<" "<<length68[i]<<" "<<"length69:"<<"
        "<<length69[i]<<" "<<"length610:"<<" "<<length610[i]<<" "<<i<<"
        "<<"maximum average length:"<<" "<<AverageQueueLengthMaximum[i]<<std::endl;
653
654     }
655     // either section A or B work
656     // Section A
657     queue256->SetSchedulingRateByLinearFactor(1, AverageQueueLengthMaximum[1]);
658     queue256->SetSchedulingRateByLinearFactor(2, AverageQueueLengthMaximum[2]);
659     queue256->SetSchedulingRateByLinearFactor(3, AverageQueueLengthMaximum[3]);
660     // Section B
661     // queue256->SetSchedulingRateByFactor(1,
        AverageQueueLengthMaximum[1]/Case1_conf::SC1BYTEBUFFER);
662     // queue256->SetSchedulingRateByFactor(2,
        AverageQueueLengthMaximum[2]/Case1_conf::SC2BYTEBUFFER);
663     // queue256->SetSchedulingRateByFactor(3,
        AverageQueueLengthMaximum[3]/Case1_conf::SC3BYTEBUFFER);
664
665 }
666

```

```

667
668 void Case2::AverageScheduleRate(void)
669 {
670     double Minqueuedelay [4],Maxqueueuelength [4] ;
671     double length68[4], length69[4], length610[4];
672     double delay68[4], delay69[4], delay610[4];
673
674     for (int i = 1; i <= 3; i++) {
675         delay68[i] = queue268->GetAverageQueueDelay(i);
676         delay69[i] = queue269->GetAverageQueueDelay(i);
677         delay610[i] = queue2610->GetAverageQueueDelay(i);
678         Minqueuedelay [i] = MinimumAverageQueueDelay
679             (delay68[i],delay69[i],delay610[i]);
680         std::cout<<"delay68:"<<" "<<delay68[i]<<" "<<"delay69:"<<"
681             "<<delay69[i]<<" "<<"delay610:"<<" "<<delay610[i]<<" "<<i<<" "<<"minimum
682             average delay:"<<" "<<Minqueuedelay [i]<<std::endl;
683
684         length68[i] = queue268->GetAverageQueueLength2(i);
685         length69[i] = queue269->GetAverageQueueLength2(i);
686         length610[i] = queue2610->GetAverageQueueLength2(i);
687         Maxqueueuelength [i] = MaximumAverageQueueLength(length68[i], length69[i],
688             length610[i]);
689         std::cout<<"length68:"<<" "<<length68[i]<<" "<<"length69:"<<"
690             "<<length69[i]<<" "<<"length610:"<<" "<<length610[i]<<" "<<i<<"
691             "<<"maximum average length:"<<" "<<Maxqueueuelength [i]<<std::endl;
692
693         queue268->SetSchedulingRateByAverage(i, Maxqueueuelength [1], Maxqueueuelength
694             [2], Maxqueueuelength [3], Minqueuedelay[1], Minqueuedelay[2],
695             Minqueuedelay[3]);
696         queue269->SetSchedulingRateByAverage(i, Maxqueueuelength [1], Maxqueueuelength
697             [2], Maxqueueuelength [3], Minqueuedelay[1], Minqueuedelay[2],
698             Minqueuedelay[3]);
699         queue2610->SetSchedulingRateByAverage(i, Maxqueueuelength [1], Maxqueueuelength
700             [2], Maxqueueuelength [3], Minqueuedelay[1], Minqueuedelay[2],
701             Minqueuedelay[3]);
702     }
703 }
704
705 double Case2::MinimumAverageQueueDelay(double x, double y, double z)
706 {
707     double minqueuedelay;
708     if (x !=0 && y !=0 && z!=0)
709     {
710         minqueuedelay = x;
711         if (y< minqueuedelay)
712         {
713             minqueuedelay = y;
714         }
715         if (z < minqueuedelay)
716         {
717             minqueuedelay = z;
718         }
719     }
720     else if (x ==0 && y !=0 && z !=0)
721     {
722         minqueuedelay = y;
723         if (z < minqueuedelay)
724         {
725             minqueuedelay = z;
726         }
727     }
728 }

```



```

716     }
717
718     else if (x !=0 && y ==0 && z !=0)
719     {
720         minqueuedelay = x;
721         if (z < minqueuedelay)
722         {
723             minqueuedelay = z;
724         }
725     }
726
727     else if (x !=0 && y !=0 && z ==0)
728     {
729         minqueuedelay = x;
730         if (y < minqueuedelay)
731         {
732             minqueuedelay = y;
733         }
734     }
735
736     else if (x ==0 && y ==0 && z !=0)
737     {
738         minqueuedelay = z;
739     }
740
741     else if (x ==0 && y !=0 && z ==0)
742     {
743         minqueuedelay = y;
744     }
745
746     else if (x !=0 && y ==0 && z ==0)
747     {
748         minqueuedelay = x;
749     }
750     else if (x ==0 && y ==0 && z ==0)
751     {
752         minqueuedelay = 0.1;
753     }
754     return minqueuedelay;
755 }
756 double Case2::MaximumAverageQueueLength(double x, double y, double z)
757 {
758     double maxqueuelebgth = x;
759     if (y>maxqueuelebgth)
760     {
761         maxqueuelebgth = y;
762     }
763     if (z > maxqueuelebgth)
764     {
765         maxqueuelebgth = z;
766     }
767     return maxqueuelebgth;
768 }
769
770 Case2::~~Case2()
771 {
772     // dtor
773 }
774

```

### A-5.3: Test Scenario 3, Topology Simulation Diagram and NS3 Code:

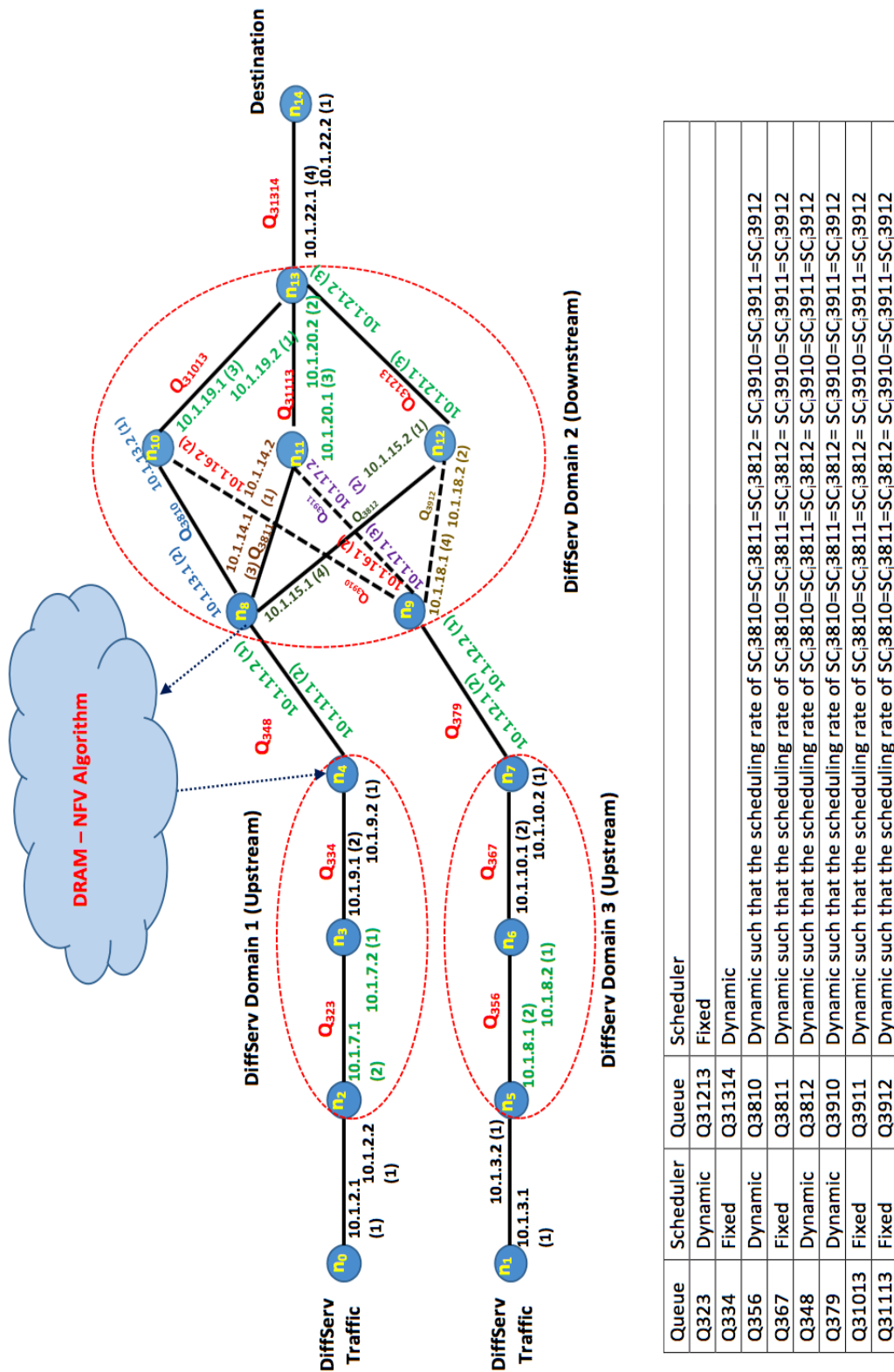


Figure 9-10, test scenario 3 simulation network topology diagram.



```

1  #ifndef CASE3_H
2  #define CASE3_H
3
4  #include "ns3/core-module.h"
5  #include "ns3/network-module.h"
6  #include "ns3/internet-module.h"
7  #include "ns3/point-to-point-module.h"
8  #include "ns3/applications-module.h"
9  #include "ns3/csma-module.h"
10 #include "RDWQueue.h"
11
12 #include "ns3/Case1_conf.h"
13
14
15 using namespace ns3;
16
17 class Case3
18 {
19     public:
20         Case3();
21         virtual ~Case3();
22
23         NodeContainer nodes;
24
25         Ptr<RDWQueue> queue323;
26         Ptr<RDWQueue> queue334;
27         Ptr<RDWQueue> queue356;
28         Ptr<RDWQueue> queue367;
29         Ptr<RDWQueue> queue348;
30         Ptr<RDWQueue> queue379;
31         Ptr<RDWQueue> queue3810;
32         Ptr<RDWQueue> queue3811;
33         Ptr<RDWQueue> queue3812;
34         Ptr<RDWQueue> queue3910;
35         Ptr<RDWQueue> queue3911;
36         Ptr<RDWQueue> queue3912;
37         Ptr<RDWQueue> queue31013;
38         Ptr<RDWQueue> queue31113;
39         Ptr<RDWQueue> queue31213;
40         Ptr<RDWQueue> queue31314;
41
42         void ScheduleSchedulingRateUpdate(void);
43         //void TrafficFromTo(int from, int to, int ProtocolType ,int TrafficType, int ToS);
44         void TrafficFromTo1(int from, int to, int ProtocolType ,int TrafficType, int ToS);
45         void TrafficFromTo2(int from, int to, int ProtocolType ,int TrafficType, int ToS);
46         void TrafficFromTo3(int from, int to, int ProtocolType ,int TrafficType, int ToS);
47         void AverageScheduleRate(void);
48         double MinimumAverageQueueDelay(double x, double y, double z);
49         double MaximumAverageQueueLength(double x, double y, double z);
50
51
52 };
53
54 #endif // CASE3_H
55

```

```

1
2 #include "Case3.h"
3 #include "TosApp.h"
4 #include "Case1_conf.h"
5
6
7 using namespace ns3;
8
9 // static void MacRecv(Ptr<const Packet> packet)
10 static void MacRecv(const Ptr<const Packet> packet, Ptr<Ipv4> ipv4, const uint32_t &
interface)
11 {
12     Ipv4Header header;
13     packet->PeekHeader(header);
14     if (header.GetDestination().IsEqual(ipv4->GetAddress(interface, 0).GetLocal())) {
15         QosTag tosTag;
16         uint32_t band = 0;
17         if (packet->PeekPacketTag(tosTag)) {
18             band = (uint32_t)tosTag.GetTid();
19         }
20         if (band != 0 ) {
21             std::cout << "Pkt_recv " << packet->GetUid() << " " <<
Simulator::Now() << " " << band << std::endl;
22         }
23     }
24 }
25
26 Case3::Case3()
27 {
28     Config::SetDefault("ns3::Ipv4GlobalRouting::RandomEcmpRouting",
BooleanValue(true));
29     nodes.Create(18);
30
31     InternetStackHelper stack;
32     stack.Install(nodes);
33
34     Ipv4AddressHelper address;
35     address.SetBase("10.1.1.0", "255.255.255.0");
36     //////////////////////////////////Sceniro 3 //////////////////////////////////
37     //0-2
38     address.NewNetwork();
39     PointToPointHelper p2p02;
40     p2p02.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
41     p2p02.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
42     address.Assign(p2p02.Install(nodes.Get(0), nodes.Get(2)));
43
44     //1-5
45     address.NewNetwork();
46     PointToPointHelper p2p15;
47     p2p15.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
48     p2p15.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
49     address.Assign(p2p15.Install(nodes.Get(1), nodes.Get(5)));
50
51     //15-10
52     address.NewNetwork();
53     PointToPointHelper p2p1510;
54     p2p1510.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
55     p2p1510.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
56     address.Assign(p2p1510.Install(nodes.Get(15), nodes.Get(10)));
57
58     //16-11

```

```

59     address.NewNetwork();
60     PointToPointHelper p2p1511;
61     p2p1511.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
62     p2p1511.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
63     address.Assign(p2p1511.Install(nodes.Get(16), nodes.Get(11)));
64
65     //17-12
66     address.NewNetwork();
67     PointToPointHelper p2p1512;
68     p2p1512.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
69     p2p1512.SetDeviceAttribute("DataRate", DataRateValue(DataRate(1000000000)));
70     address.Assign(p2p1512.Install(nodes.Get(17), nodes.Get(12)));
71
72     //2-3
73     address.NewNetwork();
74     PointToPointHelper p2p23;
75     p2p23.SetChannelAttribute("Delay", TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
76     p2p23.SetDeviceAttribute("DataRate", DataRateValue(DataRate(Case1_conf::UpstreamLINKCAPACITY)));
77     p2p23.SetDeviceAttribute("InterframeGap", TimeValue(MilliSeconds(0)));
78     address.Assign(p2p23.Install(nodes.Get(2), nodes.Get(3)));
79
80     queue323 = CreateObject<RDWQueue>(323);
81     queue323->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER, Case1_conf::SC3BYTEBUFFER);
82     queue323->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH, Case1_conf::REDSC3THIGH);
83     queue323->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP, Case1_conf::REDSC3HDROP);
84     queue323->setREDTLLOW(Case1_conf::REDSC1TLLOW, Case1_conf::REDSC2TLLOW, Case1_conf::REDSC3TLLOW);
85     queue323->setREDLDRDP(Case1_conf::REDSC1LDRDP, Case1_conf::REDSC2LDRDP, Case1_conf::REDSC3LDRDP);
86     queue323->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL, Case1_conf::SC2CONGESTIONFATORALPHAL, Case1_conf::SC3CONGESTIONFATORALPHAL);
87     queue323->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH, Case1_conf::SC2CONGESTIONFATORALPHAH, Case1_conf::SC3CONGESTIONFATORALPHAH);
88     queue323->setBELTA(Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2, Case1_conf::CONGESTIONFATBELTA3);
89
90     nodes.Get(2)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue323);
91
92
93
94
95
96
97
98     //5-6
99     address.NewNetwork();
100    PointToPointHelper p2p56;
101    p2p56.SetChannelAttribute("Delay", TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
102    p2p56.SetDeviceAttribute("DataRate", DataRateValue(DataRate(Case1_conf::UpstreamLINKCAPACITY)));
103    p2p56.SetDeviceAttribute("InterframeGap", TimeValue(MilliSeconds(0)));
104    address.Assign(p2p56.Install(nodes.Get(5), nodes.Get(6)));
105
106    queue356 = CreateObject<RDWQueue>(356);
107    queue356->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER, Case1_conf::SC3BYTEBUFFER);

```

```

108     Case1_conf::SC3BYTEBUFFER);
queue356->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
109 queue356->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
110 queue356->setREDTLLOW(Case1_conf::REDSC1TLLOW, Case1_conf::REDSC2TLLOW,
Case1_conf::REDSC3TLLOW);
111 queue356->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
Case1_conf::REDSC3LDROP);
112 queue356->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
113                     Case1_conf::SC2CONGESTIONFATORALPHAL,
114                     Case1_conf::SC3CONGESTIONFATORALPHAL);
115 queue356->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
116                     Case1_conf::SC2CONGESTIONFATORALPHAH,
117                     Case1_conf::SC3CONGESTIONFATORALPHAH);
118 queue356->setBELTA(Case1_conf::CONGESTIONFATBELTA1,
Case1_conf::CONGESTIONFATBELTA2, Case1_conf::CONGESTIONFATBELTA3);
119
nodes.Get(5)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue356);
120
121 //3-4
122 address.NewNetwork();
123 PointToPointHelper p2p34;
124 p2p34.SetChannelAttribute("Delay",
TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
125 p2p34.SetDeviceAttribute("DataRate",
DataRateValue(DataRate(Case1_conf::UpstreamLINKCAPACITY)));
126 p2p34.SetDeviceAttribute("InterframeGap", TimeValue(MilliSeconds(0.5)));
127 address.Assign(p2p34.Install(nodes.Get(3), nodes.Get(4)));
128
129 queue334 = CreateObject<RDWQueue>(334);
130 queue334->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
Case1_conf::SC3BYTEBUFFER);
131 queue334->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
132 queue334->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
133 queue334->setREDTLLOW(Case1_conf::REDSC1TLLOW, Case1_conf::REDSC2TLLOW,
Case1_conf::REDSC3TLLOW);
134 queue334->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
Case1_conf::REDSC3LDROP);
135 queue334->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
136                     Case1_conf::SC2CONGESTIONFATORALPHAL,
137                     Case1_conf::SC3CONGESTIONFATORALPHAL);
138 queue334->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
139                     Case1_conf::SC2CONGESTIONFATORALPHAH,
140                     Case1_conf::SC3CONGESTIONFATORALPHAH);
141 queue334->setBELTA(
142     Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
Case1_conf::CONGESTIONFATBELTA3);
143
nodes.Get(3)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue334);
144
145 //6-7
146 address.NewNetwork();
147 PointToPointHelper p2p67;
148 p2p67.SetChannelAttribute("Delay",
TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
149 p2p67.SetDeviceAttribute("DataRate",

```

```

150     DataRateValue(DataRate(Case1_conf::UpstreamLINKCAPACITY)));
151     p2p67.SetDeviceAttribute("InterframeGap", TimeValue(MilliSeconds(0.5)));
152     address.Assign(p2p67.Install(nodes.Get(6), nodes.Get(7)));
153
154     queue367 = CreateObject<RDWQueue>(367);
155     queue367->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
156     Case1_conf::SC3BYTEBUFFER);
157     queue367->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
158     Case1_conf::REDSC3THIGH);
159     queue367->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
160     Case1_conf::REDSC3HDROP);
161     queue367->setREDTLOW(Case1_conf::REDSC1TLOW, Case1_conf::REDSC2TLOW,
162     Case1_conf::REDSC3TLOW);
163     queue367->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
164     Case1_conf::REDSC3LDROP);
165     queue367->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
166     Case1_conf::SC2CONGESTIONFATORALPHAL,
167     Case1_conf::SC3CONGESTIONFATORALPHAL);
168     queue367->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
169     Case1_conf::SC2CONGESTIONFATORALPHAH,
170     Case1_conf::SC3CONGESTIONFATORALPHAH);
171     queue367->setBELTA(
172     Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
173     Case1_conf::CONGESTIONFATBELTA3);
174
175     nodes.Get(6)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue367);
176
177 //4-8
178 address.NewNetwork();
179 PointToPointHelper p2p48;
180 p2p48.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
181 p2p48.SetDeviceAttribute("DataRate",
182     DataRateValue(DataRate(Case1_conf::DomainsLINKCAPACITY)));
183 p2p48.SetDeviceAttribute("InterframeGap", TimeValue(MilliSeconds(1)));
184 address.Assign(p2p48.Install(nodes.Get(4), nodes.Get(8)));
185
186     queue348 = CreateObject<RDWQueue>(348);
187     queue348->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
188     Case1_conf::SC3BYTEBUFFER);
189     queue348->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
190     Case1_conf::REDSC3THIGH);
191     queue348->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
192     Case1_conf::REDSC3HDROP);
193     queue348->setREDTLOW(Case1_conf::REDSC1TLOW, Case1_conf::REDSC2TLOW,
194     Case1_conf::REDSC3TLOW);
195     queue348->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
196     Case1_conf::REDSC3LDROP);
197     queue348->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
198     Case1_conf::SC2CONGESTIONFATORALPHAL,
199     Case1_conf::SC3CONGESTIONFATORALPHAL);
200     queue348->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
201     Case1_conf::SC2CONGESTIONFATORALPHAH,
202     Case1_conf::SC3CONGESTIONFATORALPHAH);
203     queue348->setBELTA(
204     Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
205     Case1_conf::CONGESTIONFATBELTA3);
206
207     nodes.Get(4)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue348);
208
209

```



```

193 //7-9
194 address.NewNetwork();
195 PointToPointHelper p2p79;
196 p2p79.SetChannelAttribute("Delay", TimeValue(MilliSeconds(0.1)));
197 p2p79.SetDeviceAttribute("DataRate",
DataRateValue(DataRate(Case1_conf::DomainsLINKCAPACITY)));
198 p2p79.SetDeviceAttribute("InterframeGap", TimeValue(MilliSeconds(1)));
199 address.Assign(p2p79.Install(nodes.Get(7), nodes.Get(9)));
200
201 queue379 = CreateObject<RDWQueue>(379);
202 queue379->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
Case1_conf::SC3BYTEBUFFER);
203 queue379->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
204 queue379->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
205 queue379->setREDTLLOW(Case1_conf::REDSC1TLLOW, Case1_conf::REDSC2TLLOW,
Case1_conf::REDSC3TLLOW);
206 queue379->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
Case1_conf::REDSC3LDROP);
207 queue379->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
Case1_conf::SC2CONGESTIONFATORALPHAL,
Case1_conf::SC3CONGESTIONFATORALPHAL);
208
209 queue379->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
Case1_conf::SC2CONGESTIONFATORALPHAH,
Case1_conf::SC3CONGESTIONFATORALPHAH);
210
211 queue379->setBELTA(
Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
Case1_conf::CONGESTIONFATBELTA3);
212
213
214 nodes.Get(7)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue379);
215
216 //8-10
217 address.NewNetwork();
218 PointToPointHelper p2p3810;
219 p2p3810.SetChannelAttribute("Delay",
TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
220 p2p3810.SetDeviceAttribute("DataRate",
DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
221 p2p3810.SetDeviceAttribute("InterframeGap",
TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+2)));
222 address.Assign(p2p3810.Install(nodes.Get(8), nodes.Get(10)));
223
224 queue3810 = CreateObject<RDWQueue>(3810);
225 queue3810->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
Case1_conf::SC3BYTEBUFFER);
226 queue3810->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
227 queue3810->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
228 queue3810->setREDTLLOW(Case1_conf::REDSC1TLLOW, Case1_conf::REDSC2TLLOW,
Case1_conf::REDSC3TLLOW);
229 queue3810->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
Case1_conf::REDSC3LDROP);
230 queue3810->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
Case1_conf::SC2CONGESTIONFATORALPHAL,
Case1_conf::SC3CONGESTIONFATORALPHAL);
231
232 queue3810->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
Case1_conf::SC2CONGESTIONFATORALPHAH,
Case1_conf::SC3CONGESTIONFATORALPHAH);
233
234
235
236

```



```

237     queue3810->setBELTA(
238         Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
239         Case1_conf::CONGESTIONFATBELTA3);
240
241     nodes.Get(8)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue3810);
242
243     //8-11
244     address.NewNetwork();
245     PointToPointHelper p2p3811;
246     p2p3811.SetChannelAttribute("Delay",
247         TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
248     p2p3811.SetDeviceAttribute("DataRate",
249         DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
250     p2p3811.SetDeviceAttribute("InterframeGap",
251         TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+2)));
252     address.Assign(p2p3811.Install(nodes.Get(8), nodes.Get(11)));
253
254     queue3811 = CreateObject<RDWQueue>(3811);
255     queue3811->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
256         Case1_conf::SC3BYTEBUFFER);
257     queue3811->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
258         Case1_conf::REDSC3THIGH);
259     queue3811->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
260         Case1_conf::REDSC3HDROP);
261     queue3811->setREDTLOW(Case1_conf::REDSC1TLOW, Case1_conf::REDSC2TLOW,
262         Case1_conf::REDSC3TLOW);
263     queue3811->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
264         Case1_conf::REDSC3LDROP);
265     queue3811->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
266         Case1_conf::SC2CONGESTIONFATORALPHAL,
267         Case1_conf::SC3CONGESTIONFATORALPHAL);
268     queue3811->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
269         Case1_conf::SC2CONGESTIONFATORALPHAH,
270         Case1_conf::SC3CONGESTIONFATORALPHAH);
271     queue3811->setBELTA(
272         Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
273         Case1_conf::CONGESTIONFATBELTA3);
274
275     nodes.Get(8)->GetDevice(3)->GetObject<PointToPointNetDevice>()->SetQueue(queue3811);
276
277     //8-12
278     address.NewNetwork();
279     PointToPointHelper p2p3812;
280     p2p3812.SetChannelAttribute("Delay",
281         TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
282     p2p3812.SetDeviceAttribute("DataRate",
283         DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
284     p2p3812.SetDeviceAttribute("InterframeGap",
285         TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+2)));
286     address.Assign(p2p3812.Install(nodes.Get(8), nodes.Get(12)));
287
288     queue3812 = CreateObject<RDWQueue>(3812);
289     queue3812->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
290         Case1_conf::SC3BYTEBUFFER);
291     queue3812->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
292         Case1_conf::REDSC3THIGH);
293     queue3812->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
294         Case1_conf::REDSC3HDROP);
295     queue3812->setREDTLOW(Case1_conf::REDSC1TLOW, Case1_conf::REDSC2TLOW,
296         Case1_conf::REDSC3TLOW);

```

```

278     Case1_conf::REDSC3TLOW);
queue3812->setREDLDRP(Case1_conf::REDSC1LDRP, Case1_conf::REDSC2LDRP,
Case1_conf::REDSC3LDRP);
279 queue3812->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
280                     Case1_conf::SC2CONGESTIONFATORALPHAL,
281                     Case1_conf::SC3CONGESTIONFATORALPHAL);
282 queue3812->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
283                     Case1_conf::SC2CONGESTIONFATORALPHAH,
284                     Case1_conf::SC3CONGESTIONFATORALPHAH);
285 queue3812->setBELTA(
286     Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
Case1_conf::CONGESTIONFATBELTA3);
287
nodes.Get(8)->GetDevice(4)->GetObject<PointToPointNetDevice>()->SetQueue(queue3812);

288
289 //9-10
290 address.NewNetwork();
291 PointToPointHelper p2p3910;
292 p2p3910.SetChannelAttribute("Delay",
TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
293 p2p3910.SetDeviceAttribute("DataRate",
DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
294 p2p3910.SetDeviceAttribute("InterframeGap",
TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+2)));
295 address.Assign(p2p3910.Install(nodes.Get(9), nodes.Get(10)));
296
queue3910 = CreateObject<RDWQueue>(3910);
297 queue3910->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER,
Case1_conf::SC3BYTEBUFFER);
298 queue3910->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
299 queue3910->setREDHDRP(Case1_conf::REDSC1HDRP, Case1_conf::REDSC2HDRP,
Case1_conf::REDSC3HDRP);
300 queue3910->setREDTLW(Case1_conf::REDSC1TLW, Case1_conf::REDSC2TLW,
Case1_conf::REDSC3TLW);
301 queue3910->setREDLDRP(Case1_conf::REDSC1LDRP, Case1_conf::REDSC2LDRP,
Case1_conf::REDSC3LDRP);
302 queue3910->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
Case1_conf::SC2CONGESTIONFATORALPHAL,
Case1_conf::SC3CONGESTIONFATORALPHAL);
303 queue3910->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
Case1_conf::SC2CONGESTIONFATORALPHAH,
Case1_conf::SC3CONGESTIONFATORALPHAH);
304 queue3910->setBELTA(
305     Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
Case1_conf::CONGESTIONFATBELTA3);
306
307
308
309
310
311 nodes.Get(9)->GetDevice(2)->GetObject<PointToPointNetDevice>()->SetQueue(queue3910);

312
313 //9-11
314 address.NewNetwork();
315 PointToPointHelper p2p3911;
316 p2p3911.SetChannelAttribute("Delay",
TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
317 p2p3911.SetDeviceAttribute("DataRate",
DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
318 p2p3911.SetDeviceAttribute("InterframeGap",
TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+2)));
319 address.Assign(p2p3911.Install(nodes.Get(9), nodes.Get(11)));

```

```

320
321 queue3911 = CreateObject<RDWQueue>(3911);
322 queue3911->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER, 2
Case1_conf::SC3BYTEBUFFER);
323 queue3911->setREDTHIGH(Case1_conf::REDS1THIGH, Case1_conf::REDS2THIGH, 2
Case1_conf::REDS3THIGH);
324 queue3911->setREDHDROP(Case1_conf::REDS1HDROP, Case1_conf::REDS2HDROP, 2
Case1_conf::REDS3HDROP);
325 queue3911->setREDTLOW(Case1_conf::REDS1TLOW, Case1_conf::REDS2TLOW, 2
Case1_conf::REDS3TLOW);
326 queue3911->setREDLDROP(Case1_conf::REDS1LDROP, Case1_conf::REDS2LDROP, 2
Case1_conf::REDS3LDROP);
327 queue3911->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
328 Case1_conf::SC2CONGESTIONFATORALPHAL,
329 Case1_conf::SC3CONGESTIONFATORALPHAL);
330 queue3911->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
331 Case1_conf::SC2CONGESTIONFATORALPHAH,
332 Case1_conf::SC3CONGESTIONFATORALPHAH);
333 queue3911->setBELTA(
334 Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2, 2
Case1_conf::CONGESTIONFATBELTA3);
335
nodes.Get(9)->GetDevice(3)->GetObject<PointToPointNetDevice>()->SetQueue(queue3911);
336
337 //9-12
338 address.NewNetwork();
339 PointToPointHelper p2p3912;
340 p2p3912.SetChannelAttribute("Delay", 2
TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
341 p2p3912.SetDeviceAttribute("DataRate", 2
DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
342 p2p3912.SetDeviceAttribute("InterframeGap", 2
TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+2)));
343 address.Assign(p2p3912.Install(nodes.Get(9), nodes.Get(12)));
344
345 queue3912 = CreateObject<RDWQueue>(3912);
346 queue3912->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER, Case1_conf::SC2BYTEBUFFER, 2
Case1_conf::SC3BYTEBUFFER);
347 queue3912->setREDTHIGH(Case1_conf::REDS1THIGH, Case1_conf::REDS2THIGH, 2
Case1_conf::REDS3THIGH);
348 queue3912->setREDHDROP(Case1_conf::REDS1HDROP, Case1_conf::REDS2HDROP, 2
Case1_conf::REDS3HDROP);
349 queue3912->setREDTLOW(Case1_conf::REDS1TLOW, Case1_conf::REDS2TLOW, 2
Case1_conf::REDS3TLOW);
350 queue3912->setREDLDROP(Case1_conf::REDS1LDROP, Case1_conf::REDS2LDROP, 2
Case1_conf::REDS3LDROP);
351 queue3912->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
352 Case1_conf::SC2CONGESTIONFATORALPHAL,
353 Case1_conf::SC3CONGESTIONFATORALPHAL);
354 queue3912->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
355 Case1_conf::SC2CONGESTIONFATORALPHAH,
356 Case1_conf::SC3CONGESTIONFATORALPHAH);
357 queue3912->setBELTA(
358 Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2, 2
Case1_conf::CONGESTIONFATBELTA3);
359
nodes.Get(9)->GetDevice(4)->GetObject<PointToPointNetDevice>()->SetQueue(queue3912);
360
361 //10-13

```

```

362 address.NewNetwork();
363 PointToPointHelper p2p1013;
364 p2p1013.SetChannelAttribute("Delay",
TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
365 p2p1013.SetDeviceAttribute("DataRate",
DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
366
367 p2p1013.SetDeviceAttribute("InterframeGap",
TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+4)));
368 address.Assign(p2p1013.Install(nodes.Get(10), nodes.Get(13)));
369
370 queue31013 = CreateObject<RDWQueue>(31013);
371 queue31013->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER,
Case1_conf::SC2BYTEBUFFER, Case1_conf::SC3BYTEBUFFER);
372 queue31013->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
373 queue31013->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
374 queue31013->setREDTLLOW(Case1_conf::REDSC1TLLOW, Case1_conf::REDSC2TLLOW,
Case1_conf::REDSC3TLLOW);
375 queue31013->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
Case1_conf::REDSC3LDROP);
376 queue31013->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
Case1_conf::SC2CONGESTIONFATORALPHAL,
377 Case1_conf::SC3CONGESTIONFATORALPHAL);
378 queue31013->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
Case1_conf::SC2CONGESTIONFATORALPHAH,
379 Case1_conf::SC3CONGESTIONFATORALPHAH);
380 queue31013->setBELTA(
381 Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
382 Case1_conf::CONGESTIONFATBELTA3);
383
384 nodes.Get(10)->GetDevice(4)->GetObject<PointToPointNetDevice>()->SetQueue(queue31013);
385
386 //11-13
387 address.NewNetwork();
388 PointToPointHelper p2p1113;
389 p2p1113.SetChannelAttribute("Delay",
TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
390 p2p1113.SetDeviceAttribute("DataRate",
DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
391
392 p2p1113.SetDeviceAttribute("InterframeGap",
TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+4)));
393 address.Assign(p2p1113.Install(nodes.Get(11), nodes.Get(13)));
394
395 queue31113 = CreateObject<RDWQueue>(31113);
396 queue31113->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER,
Case1_conf::SC2BYTEBUFFER, Case1_conf::SC3BYTEBUFFER);
397 queue31113->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
398 queue31113->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
399 queue31113->setREDTLLOW(Case1_conf::REDSC1TLLOW, Case1_conf::REDSC2TLLOW,
Case1_conf::REDSC3TLLOW);
400 queue31113->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
Case1_conf::REDSC3LDROP);
401 queue31113->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
Case1_conf::SC2CONGESTIONFATORALPHAL,
402 Case1_conf::SC3CONGESTIONFATORALPHAL);
403

```



```

404     queue31113->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
405                          Case1_conf::SC2CONGESTIONFATORALPHAH,
406                          Case1_conf::SC3CONGESTIONFATORALPHAH);
407     queue31113->setBELTA(
408         Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
409         Case1_conf::CONGESTIONFATBELTA3);
410
411     nodes.Get(10)->GetDevice(4)->GetObject<PointToPointNetDevice>()->SetQueue(queue31113);
412
413 //12-13
414     address.NewNetwork();
415     PointToPointHelper p2p1213;
416     p2p1213.SetChannelAttribute("Delay",
417                               TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
418     p2p1213.SetDeviceAttribute("DataRate",
419                               DataRateValue(DataRate(Case1_conf::DownstreamLINKCAPACITY)));
420     p2p1213.SetDeviceAttribute("InterframeGap",
421                               TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+4)));
422     address.Assign(p2p1213.Install(nodes.Get(12), nodes.Get(13)));
423
424     queue31213 = CreateObject<RDWQueue>(31213);
425     queue31213->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER,
426                              Case1_conf::SC2BYTEBUFFER, Case1_conf::SC3BYTEBUFFER);
427     queue31213->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
428                            Case1_conf::REDSC3THIGH);
429     queue31213->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
430                            Case1_conf::REDSC3HDROP);
431     queue31213->setREDTLOW(Case1_conf::REDSC1TLOW, Case1_conf::REDSC2TLOW,
432                            Case1_conf::REDSC3TLOW);
433     queue31213->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
434                            Case1_conf::REDSC3LDROP);
435     queue31213->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
436                          Case1_conf::SC2CONGESTIONFATORALPHAL,
437                          Case1_conf::SC3CONGESTIONFATORALPHAL);
438     queue31213->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
439                          Case1_conf::SC2CONGESTIONFATORALPHAH,
440                          Case1_conf::SC3CONGESTIONFATORALPHAH);
441     queue31213->setBELTA(
442         Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
443         Case1_conf::CONGESTIONFATBELTA3);
444
445     nodes.Get(12)->GetDevice(4)->GetObject<PointToPointNetDevice>()->SetQueue(queue31213);
446
447 //13-14
448     address.NewNetwork();
449     PointToPointHelper p2p1314;
450     p2p1314.SetChannelAttribute("Delay",
451                               TimeValue(MilliSeconds(Case1_conf::LINKDELAY)));
452     p2p1314.SetDeviceAttribute("DataRate",
453                               DataRateValue(DataRate(Case1_conf::DestinationLINKCAPACITY)));
454     p2p1314.SetDeviceAttribute("InterframeGap",
455                               TimeValue(MilliSeconds(Case1_conf::INTERFRAMEGAP+6)));
456     address.Assign(p2p1314.Install(nodes.Get(13), nodes.Get(14)));
457
458     queue31314 = CreateObject<RDWQueue>(31314);
459     queue31314->setBYTEBUFFER(Case1_conf::SC1BYTEBUFFER,
460                              Case1_conf::SC2BYTEBUFFER, Case1_conf::SC3BYTEBUFFER);

```

```

447 queue31314->setREDTHIGH(Case1_conf::REDSC1THIGH, Case1_conf::REDSC2THIGH,
Case1_conf::REDSC3THIGH);
448 queue31314->setREDHDROP(Case1_conf::REDSC1HDROP, Case1_conf::REDSC2HDROP,
Case1_conf::REDSC3HDROP);
449 queue31314->setREDTLLOW(Case1_conf::REDSC1TLLOW, Case1_conf::REDSC2TLLOW,
Case1_conf::REDSC3TLLOW);
450 queue31314->setREDLDROP(Case1_conf::REDSC1LDROP, Case1_conf::REDSC2LDROP,
Case1_conf::REDSC3LDROP);
451 queue31314->setALPHAL(Case1_conf::SC1CONGESTIONFATORALPHAL,
Case1_conf::SC2CONGESTIONFATORALPHAL,
452 Case1_conf::SC3CONGESTIONFATORALPHAL);
453 queue31314->setALPHAH(Case1_conf::SC1CONGESTIONFATORALPHAH,
Case1_conf::SC2CONGESTIONFATORALPHAH,
454 Case1_conf::SC3CONGESTIONFATORALPHAH);
455 queue31314->setBELTA(
Case1_conf::CONGESTIONFATBELTA1, Case1_conf::CONGESTIONFATBELTA2,
456 Case1_conf::CONGESTIONFATBELTA3);
457
458 nodes.Get(13)->GetDevice(4)->GetObject<PointToPointNetDevice>()->SetQueue(queue31314);
459
460
461
462
463 for (uint32_t i = 0; i < nodes.GetN(); i++) {
464     for (uint32_t j = 1; j < nodes.Get(i)->GetNDevices(); j++) {
465         std::cout<<i<<" "<<nodes.Get(i)->GetObject<Ipv4>()->GetAddress(j,
0).GetLocal()-<<"Device"<<nodes.Get(i)->GetNDevices()-<<"Get device
index"<<nodes.Get(i)->GetDevice(j)<<std::endl;
466     }
467 }
468
469 Ipv4GlobalRoutingHelper::PopulateRoutingTables();
470
471 // sink
472 uint16_t sinkPort = 8080;
473
474 PacketSinkHelper udpSinkHelper("ns3::UdpSocketFactory",
InetSocketAddress(Ipv4Address::GetAny(), sinkPort));
475 PacketSinkHelper tcpSinkHelper14tcp("ns3::TcpSocketFactory",
InetSocketAddress(Ipv4Address::GetAny(), sinkPort));
476 ApplicationContainer sinkApps14 = udpSinkHelper.Install(nodes.Get(14));
477 sinkApps14.Start(Seconds(0.));
478 ApplicationContainer sinkApps14tcp = tcpSinkHelper14tcp.Install(nodes.Get(14));
479 sinkApps14tcp.Start(Seconds(0.));
480
481 // Traffic 0
482 TrafficFromTo3(0, 14, 3, 3, 1);TrafficFromTo3(0, 14, 3, 3,
1);TrafficFromTo3(0, 14, 3, 3, 1);
483 TrafficFromTo3(0, 14, 3, 3, 1);TrafficFromTo3(0, 14, 3, 3,
1);TrafficFromTo3(0, 14, 3, 3, 1);
484 TrafficFromTo3(0, 14, 3, 3, 1);TrafficFromTo3(0, 14, 3, 3,
1);TrafficFromTo3(0, 14, 3, 3, 1);
485 TrafficFromTo3(0, 14, 3, 3, 1);TrafficFromTo3(0, 14, 3, 3,
1);TrafficFromTo3(0, 14, 3, 3, 1);
486 TrafficFromTo3(0, 14, 3, 3, 1);TrafficFromTo3(0, 14, 3, 3,
1);TrafficFromTo3(0, 14, 3, 3, 1);
487 TrafficFromTo3(0, 14, 3, 3, 1);TrafficFromTo3(0, 14, 3, 3,
1);TrafficFromTo3(0, 14, 3, 3, 1);
488 TrafficFromTo3(0, 14, 3, 3, 1);TrafficFromTo3(0, 14, 3, 3,
1);TrafficFromTo3(0, 14, 3, 3, 1);
489 TrafficFromTo3(0, 14, 3, 3, 1);TrafficFromTo3(0, 14, 3, 3,
1);TrafficFromTo3(0, 14, 3, 3, 1);

```











[illegible]











```

747     3);TrafficFromTo3(1, 14, 5, 5, 3);
TrafficFromTo3(1, 14, 5, 5, 3);TrafficFromTo3(1, 14, 5, 5,
3);TrafficFromTo3(1, 14, 5, 5, 3);
748
749     TrafficFromTo3(1, 14, 5, 5, 3);TrafficFromTo3(1, 14, 5, 5,
3);TrafficFromTo3(1, 14, 5, 5, 3);
750     TrafficFromTo3(1, 14, 5, 5, 3);TrafficFromTo3(1, 14, 5, 5,
3);TrafficFromTo3(1, 14, 5, 5, 3);
751     TrafficFromTo3(1, 14, 5, 5, 3);TrafficFromTo3(1, 14, 5, 5,
3);TrafficFromTo3(1, 14, 5, 5, 3);
752     TrafficFromTo3(1, 14, 5, 5, 3);TrafficFromTo3(1, 14, 5, 5,
3);TrafficFromTo3(1, 14, 5, 5, 3);
753     TrafficFromTo3(1, 14, 5, 5, 3);TrafficFromTo3(1, 14, 5, 5,
3);TrafficFromTo3(1, 14, 5, 5, 3);
754     TrafficFromTo3(1, 14, 5, 5, 3);TrafficFromTo3(1, 14, 5, 5,
3);TrafficFromTo3(1, 14, 5, 5, 3);
755     TrafficFromTo3(1, 14, 5, 5, 3);TrafficFromTo3(1, 14, 5, 5,
3);TrafficFromTo3(1, 14, 5, 5, 3);
756     TrafficFromTo3(1, 14, 5, 5, 3);TrafficFromTo3(1, 14, 5, 5,
3);TrafficFromTo3(1, 14, 5, 5, 3);
757     TrafficFromTo3(1, 14, 5, 5, 3);TrafficFromTo3(1, 14, 5, 5,
3);TrafficFromTo3(1, 14, 5, 5, 3);
758     TrafficFromTo3(1, 14, 5, 5, 3);TrafficFromTo3(1, 14, 5, 5,
3);TrafficFromTo3(1, 14, 5, 5, 3);
759
760
761
762     for (double i = 20000; i < Case1_conf::SIM_STOP_Sec * 1000; i += 1000) {
763         Simulator::Schedule(MilliSeconds(i), &Case3::ScheduleSchedulingRateUpdate,
this);
764     }
765     for (double i = 10001; i < Case1_conf::SIM_STOP_Sec * 1000; i += 1000) {
766         Simulator::Schedule(MilliSeconds(i), &Case3::AverageScheduleRate, this);
767     }
768
769
770
771
772
773     Config::ConnectWithoutContext("/NodeList/*/ $ns3::Ipv4L3Protocol/Rx",
MakeCallback(&MacRecv));
774 }
775
776 void Case3::TrafficFromTo1(int from, int to, int ProtocolType, int TrafficType,
int ToS)
777 {
778     uint16_t sinkPort = 8080;
779     Address
sinkAddress(InetSocketAddress(nodes.Get(to)->GetObject<Ipv4>()->GetAddress(1,
0).GetLocal(), sinkPort));
780     Ptr<Socket> TrafficSocket;
781     Ptr<ToSApp> app1 = CreateObject<ToSApp>(ToS, TrafficType);
782     if (ProtocolType == 3) { // edit 13/05 VoIP traffic
783         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
UdpSocketFactory::GetTypeId());
784         app1->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
(80000/1600)*Case1_conf::SIM_STOP_Sec, DataRate("80000"),1,1,1);
785     } else if (ProtocolType == 4) { // edit 13/05 Video Conferencing traffic
786         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
UdpSocketFactory::GetTypeId());
787

```



```

788         app1->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
789         (384000/11200)*Case1_conf::SIM_STOP_Sec, DataRate("384000"),1,1,1);
790         // video conferencing rate 128-384 kbits/sec,
791         https://en.wikipedia.org/wiki/Bit_rate
792     } else if (ProtocolType == 5) { // edit 13/05    (FTP) traffic TcpSocketFactory
793         TrafficSocket =
794         Socket::CreateSocket(nodes.Get(from),TcpSocketFactory::GetTypeId());
795         app1->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
796         (256000/2800)*Case1_conf::SIM_STOP_Sec, DataRate("256000"),1,1,1);
797
798     } else if (ProtocolType == 6) { // edit 13/05    // Database Query message
799     traffic - Best effort traffic
800         TrafficSocket =
801         Socket::CreateSocket(nodes.Get(from),UdpSocketFactory::GetTypeId());
802         app1->Setup(TrafficSocket, sinkAddress,
803         Case1_conf::TRAFFICPKTSIZE, (81920/4096)*Case1_conf::SIM_STOP_Sec ,
804         DataRate("81920"),1,1,1);
805     }
806     nodes.Get(from)->AddApplication(app1);
807
808     app1->SetStartTime(Seconds(1.));
809 }
810 void Case3::TrafficFromTo2(int from, int to, int ProtocolType, int TrafficType,
811 int ToS)
812 {
813     uint16_t sinkPort = 8080;
814     Address
815     sinkAddress(InetSocketAddress(nodes.Get(to)->GetObject<Ipv4>()->GetAddress(1,
816     0).GetLocal(), sinkPort));
817     Ptr<Socket> TrafficSocket;
818     Ptr<ToSApp> app2 = CreateObject<ToSApp>(ToS, TrafficType);
819     if (ProtocolType == 3) { // edit 13/05    VoIP traffic
820         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
821         UdpSocketFactory::GetTypeId());
822         app2->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
823         (80000/1600)*Case1_conf::SIM_STOP_Sec, DataRate("80000"),1,1,2);
824
825     } else if (ProtocolType == 4) { // edit 13/05    Video Conferencing traffic
826         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
827         UdpSocketFactory::GetTypeId());
828         app2->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
829         (384000/11200)*Case1_conf::SIM_STOP_Sec, DataRate("384000"),1,1,2);
830         // video conferencing rate 128-384 kbits/sec,
831         https://en.wikipedia.org/wiki/Bit_rate
832     } else if (ProtocolType == 5) { // edit 13/05    (FTP) traffic TcpSocketFactory
833         TrafficSocket =
834         Socket::CreateSocket(nodes.Get(from),TcpSocketFactory::GetTypeId());
835         app2->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
836         (256000/2800)*Case1_conf::SIM_STOP_Sec, DataRate("256000"),1,1,2);
837
838     } else if (ProtocolType == 6) { // edit 13/05    // Database Query message
839     traffic - Best effort traffic
840         TrafficSocket =
841         Socket::CreateSocket(nodes.Get(from),TcpSocketFactory::GetTypeId());
842         app2->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
843         (81920/4096)*Case1_conf::SIM_STOP_Sec, DataRate("81920"),1,1,2);
844     }
845     nodes.Get(from)->AddApplication(app2);
846
847     app2->SetStartTime(Seconds(1.));

```

```

828 }
829 void Case3::TrafficFromTo3(int from, int to, int ProtocolType, int TrafficType,
830 int ToS)
831 {
832     uint16_t sinkPort = 8080;
833     Address
834     sinkAddress(InetSocketAddress(nodes.Get(to)->GetObject<Ipv4>()->GetAddress(1,
835 0).GetLocal(), sinkPort));
836     Ptr<Socket> TrafficSocket;
837     Ptr<TosApp> app3 = CreateObject<TosApp>(ToS, TrafficType);
838     if (ProtocolType == 3) { // edit 13/05 VoIP traffic
839         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
840         UdpSocketFactory::GetTypeId());
841         app3->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
842         (80000/1600)*Case1_conf::SIM_STOP_Sec, DataRate("80000"),1,1,3);
843     } else if (ProtocolType == 4) { // edit 13/05 Video Conferencing traffic
844         TrafficSocket = Socket::CreateSocket(nodes.Get(from),
845         UdpSocketFactory::GetTypeId());
846         app3->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
847         (384000/11200)*Case1_conf::SIM_STOP_Sec, DataRate("384000"),1,1,3);
848         // video conferencing rate 128-384 kbits/sec,
849         // https://en.wikipedia.org/wiki/Bit_rate
850     } else if (ProtocolType == 5) { // edit 13/05 (FTP) traffic TcpSocketFactory
851         TrafficSocket =
852         Socket::CreateSocket(nodes.Get(from),TcpSocketFactory::GetTypeId());
853         app3->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
854         (256000/2800)*Case1_conf::SIM_STOP_Sec, DataRate("256000"),1,1,3);
855     } else if (ProtocolType == 6) { // edit 13/05 // Database Query message
856         traffic - Best effort traffic
857         TrafficSocket =
858         Socket::CreateSocket(nodes.Get(from),TcpSocketFactory::GetTypeId());
859         app3->Setup(TrafficSocket, sinkAddress, Case1_conf::TRAFFICPKTSIZE,
860         (81920/4096)*Case1_conf::SIM_STOP_Sec, DataRate("81920"),1,1,3);
861     }
862     nodes.Get(from)->AddApplication(app3);
863     app3->SetStartTime(Seconds(1.));
864 }
865
866 void Case3::ScheduleSchedulingRateUpdate(void)
867 {
868     double Length810[4], Length811[4], Length812[4];
869     double Length910[4], Length911[4], Length912[4];
870     double MaxQueueLength1 [4], MaxQueueLength2 [4], MaxQueueLength [4];
871     for (int i = 1; i <= 3; i++) {
872         Length810[i] = queue3810->GetAverageQueueLength1(i);
873         Length811[i] = queue3811->GetAverageQueueLength1(i);
874         Length812[i] = queue3812->GetAverageQueueLength1(i);
875         Length910[i] = queue3910->GetAverageQueueLength1(i);
876         Length911[i] = queue3911->GetAverageQueueLength1(i);
877         Length912[i] = queue3912->GetAverageQueueLength1(i);
878     }
879     MaxQueueLength1 [i] = MaximumAverageQueueLength(Length810[i], Length811[i],
880 Length812[i]);
881     MaxQueueLength2 [i] = MaximumAverageQueueLength(Length910[i], Length911[i],
882 Length912[i]);

```

```

874 MaxQueueLength [i] = MaximumAverageQueueLength(0, MaxQueueLength1 [i],
MaxQueueLength2 [i]);
875 }
876 // either section A or B work
877 //Section A
878 queue348->SetSchedulingRateByFactor(1, MaxQueueLength[1] /
(float)Case1_conf::SC1BYTEBUFFER);
879 queue379->SetSchedulingRateByFactor(1, MaxQueueLength[1] /
(float)Case1_conf::SC1BYTEBUFFER);
880
881 queue348->SetSchedulingRateByFactor(2, MaxQueueLength[2] /
(float)Case1_conf::SC2BYTEBUFFER);
882 queue379->SetSchedulingRateByFactor(2, MaxQueueLength[2] /
(float)Case1_conf::SC2BYTEBUFFER);
883
884 queue348->SetSchedulingRateByFactor(3, MaxQueueLength[3] /
(float)Case1_conf::SC3BYTEBUFFER);
885 queue379->SetSchedulingRateByFactor(3, MaxQueueLength[3] /
(float)Case1_conf::SC3BYTEBUFFER);
886
887
888 //Section B
889
890 queue348->SetSchedulingRateByLinearFactor(1, MaxQueueLength[1]);
891 queue379->SetSchedulingRateByLinearFactor(1, MaxQueueLength[1]);
892
893 queue348->SetSchedulingRateByLinearFactor(2, MaxQueueLength[2]);
894 queue379->SetSchedulingRateByLinearFactor(2, MaxQueueLength[2]);
895
896 queue348->SetSchedulingRateByLinearFactor(3, MaxQueueLength[3]);
897 queue379->SetSchedulingRateByLinearFactor(3, MaxQueueLength[3]);
898
899 }
900
901
902 void Case3::AverageScheduleRate(void)
903 {
904     double length810[4], length811[4], length812[4];
905     double length910[4], length911[4], length912[4];
906     double delay810[4], delay811[4], delay812[4];
907     double delay910[4], delay911[4], delay912[4];
908     double Minqueuedelay1 [4], Minqueuedelay2 [4], Minqueuedelay [4];
909     double MaxqueueLength1 [4], MaxqueueLength2 [4], MaxqueueLength [4];
910     for (int i = 1; i <= 3; i++) {
911         delay810[i] = queue3810->GetAverageQueueDelay(i);
912         delay811[i] = queue3811->GetAverageQueueDelay(i);
913         delay812[i] = queue3812->GetAverageQueueDelay(i);
914         delay910[i] = queue3910->GetAverageQueueDelay(i);
915         delay911[i] = queue3911->GetAverageQueueDelay(i);
916         delay912[i] = queue3912->GetAverageQueueDelay(i);
917         Minqueuedelay1 [i] = MinimumAverageQueueDelay
(delay810[i],delay811[i],delay812[i]);
918         Minqueuedelay2 [i] = MinimumAverageQueueDelay
(delay910[i],delay911[i],delay912[i]);
919         Minqueuedelay [i] = MinimumAverageQueueDelay (0,Minqueuedelay1
[i],Minqueuedelay2 [i]);
920         std::cout<<Simulator::Now() <<" "<<"delay810:"<<" "<<delay810[i]<<"
"<<"delay811:"<<" "<<delay811[i]<<" "<<"delay812:"<<" "<<delay812[i]<<"
"<<i<<" "<<"minimum average delay1:"<<" "<<Minqueuedelay1 [i]<<std::endl;
921         std::cout<<Simulator::Now() <<" "<<"delay910:"<<" "<<delay910[i]<<"
"<<"delay911:"<<" "<<delay911[i]<<" "<<"delay912:"<<" "<<delay912[i]<<"

```



```

922     "<<i<<" "<<"minimum average delay2:"<<" "<<Minqueuedelay2 [i]<<std::endl;
std::cout<<Simulator::Now() <<" "<<"Minqueuesdelay:"<<" "<<Minqueuedelay
[i]<<std::endl;
923
924
925     length810[i] = queue3810->GetAverageQueueLength2(i);
926     length811[i] = queue3811->GetAverageQueueLength2(i);
927     length812[i] = queue3812->GetAverageQueueLength2(i);
928     length910[i] = queue3910->GetAverageQueueLength2(i);
929     length911[i] = queue3911->GetAverageQueueLength2(i);
930     length912[i] = queue3912->GetAverageQueueLength2(i);
931     Maxqueuelength1 [i] = MaximumAverageQueueLength(length810[i], length811[i],
length812[i]);
932     Maxqueuelength2 [i] = MaximumAverageQueueLength(length910[i], length911[i],
length912[i]);
933     Maxqueuelength [i] = MaximumAverageQueueLength(0, Maxqueuelength1 [i],
Maxqueuelength2 [i]);
934     std::cout<<Simulator::Now()<<"length810:"<<" "<<length810[i]<<"
"<<"length811:"<<" "<<length811[i]<<" "<<"length812:"<<" "<<length812[i]<<"
"<<i<<" "<<"maximum average length:"<<" "<<Maxqueuelength1 [i]<<std::endl;
935     std::cout<<Simulator::Now()<<"length910:"<<" "<<length910[i]<<"
"<<"length911:"<<" "<<length911[i]<<" "<<"length912:"<<" "<<length912[i]<<"
"<<i<<" "<<"maximum average length:"<<" "<<Maxqueuelength2 [i]<<std::endl;
936     std::cout<<Simulator::Now()<<"Maximumqueuelengths:"<<" "<<Maxqueuelength
[i]<<std::endl;
937
938     queue3810->SetSchedulingRateByAverage(i, Maxqueuelength [1], Maxqueuelength
[2], Maxqueuelength [3], Minqueuedelay[1], Minqueuedelay[2], Minqueuedelay[3]);
939     queue3811->SetSchedulingRateByAverage(i, Maxqueuelength [1], Maxqueuelength
[2], Maxqueuelength [3], Minqueuedelay[1], Minqueuedelay[2], Minqueuedelay[3]);
940     queue3812->SetSchedulingRateByAverage(i, Maxqueuelength [1], Maxqueuelength
[2], Maxqueuelength [3], Minqueuedelay[1], Minqueuedelay[2], Minqueuedelay[3]);
941     queue3910->SetSchedulingRateByAverage(i, Maxqueuelength [1], Maxqueuelength
[2], Maxqueuelength [3], Minqueuedelay[1], Minqueuedelay[2], Minqueuedelay[3]);
942     queue3911->SetSchedulingRateByAverage(i, Maxqueuelength [1], Maxqueuelength
[2], Maxqueuelength [3], Minqueuedelay[1], Minqueuedelay[2], Minqueuedelay[3]);
943     queue3912->SetSchedulingRateByAverage(i, Maxqueuelength [1], Maxqueuelength
[2], Maxqueuelength [3], Minqueuedelay[1], Minqueuedelay[2], Minqueuedelay[3]);
944
945
946
947     }
948
949 }
950 double Case3::MinimumAverageQueueDelay(double x, double y, double z)
951 {
952     double minqueuedelay;
953     if (x !=0 && y !=0 && z!=0)
954     {
955         minqueuedelay = x;
956         if (y< minqueuedelay)
957         {
958             minqueuedelay = y;
959         }
960         if (z < minqueuedelay)
961         {
962             minqueuedelay = z;
963         }
964     }
965     else if (x ==0 && y !=0 && z !=0)
966     {
967         minqueuedelay = y;

```

```

967         if (z < minqueuedelay)
968         {
969             minqueuedelay = z;
970         }
971     }
972
973     else if (x !=0 && y ==0 && z !=0)
974     {
975         minqueuedelay = x;
976         if (z < minqueuedelay)
977         {
978             minqueuedelay = z;
979         }
980     }
981
982     else if (x !=0 && y !=0 && z ==0)
983     {
984         minqueuedelay = x;
985         if (y < minqueuedelay)
986         {
987             minqueuedelay = y;
988         }
989     }
990
991     else if (x ==0 && y ==0 && z !=0)
992     {
993         minqueuedelay = z;
994     }
995
996     else if (x ==0 && y !=0 && z ==0)
997     {
998         minqueuedelay = y;
999     }
1000
1001     else if (x !=0 && y ==0 && z ==0)
1002     {
1003         minqueuedelay = x;
1004     }
1005     else if (x ==0 && y ==0 && z ==0)
1006     {
1007         minqueuedelay = 0.1;
1008     }
1009     return minqueuedelay;
1010 }
1011 double Case3::MaximumAverageQueueLength(double x, double y, double z)
1012 {
1013     double maxqueuelebgth = x;
1014     if (y>maxqueuelebgth)
1015     {
1016         maxqueuelebgth = y;
1017     }
1018     if (z > maxqueuelebgth)
1019     {
1020         maxqueuelebgth = z;
1021     }
1022     return maxqueuelebgth;
1023 }
1024 Case3::~~Case3()
1025 {
1026     // dtor
1027 }

```

## A-6: Simulation Parameters Definition Code

```
1  #ifndef CASE1_CONF_H
2  #define CASE1_CONF_H
3
4  #include "ns3/core-module.h"
5
6
7  using namespace ns3;
8
9  class Case1_conf
10 {
11 public:
12
13     static double  CONSTANTRADVAR ;
14     static double  EXPONRADVARMEAN ;
15     static double  EXPONRADVARBOUND ;
16     static double  PARETORADVARMEAN ;
17     static double  PARETORADVARSAHPE ;
18
19     // update for on-off application
20     static double  ONTIMEEXPONMEAN ;
21     static double  ONTIMEEXPONBOUND ;
22     static double  OFFTIMEEXPONMEAN ;
23     static double  OFFTIMEEXPONBOUND ;
24
25     //////////////////////////////////////
26     static const double  EXPONPKTSIZEMEAN ;
27     static const double  EXPONPKTSIZEBOUND ;
28
29     static const int  REDSC1THIGH;
30     static const int  REDSC2THIGH;
31     static const int  REDSC3THIGH;
32
33     static const double  REDSC1HDRDP;
34     static const double  REDSC2HDRDP;
35     static const double  REDSC3HDRDP;
36
37
38     static const int  REDSC1TLOW;
39     static const int  REDSC2TLOW;
40     static const int  REDSC3TLOW;
41
42     static const double  REDSC1LDRDP;
43     static const double  REDSC2LDRDP;
44     static const double  REDSC3LDRDP;
45
46
47     static int  SC1PKTBUFFER;
48     static int  SC2PKTBUFFER;
49     static int  SC3PKTBUFFER;
50
51     static int  SC1BYTEBUFFER;
52     static int  SC2BYTEBUFFER;
53     static int  SC3BYTEBUFFER;
54
55     static float  SC1CONGESTIONFATORALPHAL;
56     static float  SC2CONGESTIONFATORALPHAL;
57     static float  SC3CONGESTIONFATORALPHAL;
58
59     static float  SC1CONGESTIONFATORALPHAH;
60     static float  SC2CONGESTIONFATORALPHAH;
61     static float  SC3CONGESTIONFATORALPHAH;
```

```

62
63     static float CONGESTIONFATBELTA1;
64     static float CONGESTIONFATBELTA2;
65     static float CONGESTIONFATBELTA3;
66
67     static float SC1BETAMAX;
68     static float SC2BETAMAX;
69     static float SC3BETAMAX;
70
71     static float SC1INITWEIGHT;
72     static float SC2INITWEIGHT;
73     static float SC3INITWEIGHT;
74
75     static uint64_t LINKCAPACITY;
76     static uint64_t LINKDELAY;
77     static float INTERFRAMEGAP;
78
79     static uint64_t DomainsLINKCAPACITY;
80     static uint64_t DestinationLINKCAPACITY;
81     static uint64_t UpstreamLINKCAPACITY;
82     static uint64_t DownstreamLINKCAPACITY;
83
84     static uint64_t TRAFFICDATARATE;
85     static uint32_t TRAFFICPKTSIZE;
86     static uint64_t TRAFFICPKTNUM;
87
88     static double SIM_STOP_Sec;
89
90     static double ROWFORAVGQLENGTH;
91
92
93
94 };
95
96
97
98
99 #endif // CASE1_CONF_H
100

```

```

1  #include "Case1_conf.h"
2  using namespace ns3;
3
4
5
6  double Case1_conf::CONSTANTRADVAR =0.1;
7  double Case1_conf::EXPONRADVARMEAN = 3.14;
8  double Case1_conf::EXPONRADVARBOUND = 0.0;
9  double Case1_conf::PARETORADVARMEAN = 3.0;
10 double Case1_conf::PARETORADVARSAHPE = 2.0;
11
12 // update for on-off application
13 double Case1_conf::ONTIMEEXPONMEAN = 3;
14 double Case1_conf::ONTIMEEXPONBOUND = 0;
15 double Case1_conf::OFFTIMEEXPONMEAN = 3;
16 double Case1_conf::OFFTIMEEXPONBOUND = 0;
17
18 const double Case1_conf::EXPONPKTSIZEMEAN = 0.5;
19 const double Case1_conf::EXPONPKTSIZEBOUND = 0.0;
20
21
22 const int Case1_conf::REDSC1THIGH=12800; //0.5* buffer size
23 const int Case1_conf::REDSC2THIGH=89600; //0.5* buffer size
24 const int Case1_conf::REDSC3THIGH=22400; //0.5* buffer size
25
26
27 const double Case1_conf::REDSC1HDR0P=0.10;
28 const double Case1_conf::REDSC2HDR0P=0.10;
29 const double Case1_conf::REDSC3HDR0P=0.10;
30
31
32
33 const int Case1_conf::REDSC1TLOW=12000;
34 const int Case1_conf::REDSC2TLOW=78400;
35 const int Case1_conf::REDSC3TLOW=18200;
36
37
38 const double Case1_conf::REDSC1LDR0P=0.10;
39 const double Case1_conf::REDSC2LDR0P=0.10;
40 const double Case1_conf::REDSC3LDR0P=0.10;
41
42
43 int Case1_conf::SC1PKTBUFFER=2048;
44 int Case1_conf::SC2PKTBUFFER=2048;
45 int Case1_conf::SC3PKTBUFFER=2048;
46
47
48 int Case1_conf::SC1BYTEBUFFER=25600; //128*200
49 int Case1_conf::SC2BYTEBUFFER=179200; // 128*1400
50 int Case1_conf::SC3BYTEBUFFER=44800; //128*350
51
52
53 float Case1_conf::SC1CONGESTIONFATORALPHA=0.9;
54 float Case1_conf::SC2CONGESTIONFATORALPHA=0.9;
55 float Case1_conf::SC3CONGESTIONFATORALPHA=0.9;
56
57 float Case1_conf::SC1CONGESTIONFATORALPHA=0.46875;
58 float Case1_conf::SC2CONGESTIONFATORALPHA=0.4375;
59 float Case1_conf::SC3CONGESTIONFATORALPHA=0.40625;
60
61

```

```

62 float Case1_conf::CONGESTIONFATBELTA1=0.3;
63 float Case1_conf::CONGESTIONFATBELTA2=0.6;
64 float Case1_conf::CONGESTIONFATBELTA3=0.9;
65
66 float Case1_conf::SC1BETAMAX=0.9;
67 float Case1_conf::SC2BETAMAX=0.9;
68 float Case1_conf::SC3BETAMAX=0.9;
69
70 float Case1_conf::SC1INITWEIGHT=1;
71 float Case1_conf::SC2INITWEIGHT=2;
72 float Case1_conf::SC3INITWEIGHT=4;
73
74 uint64_t Case1_conf::LINKCAPACITY=2000000;
75 uint64_t Case1_conf::LINKDELAY=1;
76 float Case1_conf::INTERFRAMEGAP=2;
77
78 uint64_t Case1_conf::UpstreamLINKCAPACITY=2000000;
79 uint64_t Case1_conf::DownstreamLINKCAPACITY=2000000;
80 uint64_t Case1_conf::DomainsLINKCAPACITY=4000000;
81 uint64_t Case1_conf::DestinationLINKCAPACITY=4000000;
82
83
84 uint64_t Case1_conf::TRAFFICDATARATE=1000000000;
85 uint32_t Case1_conf::TRAFFICPKTSIZE=800;
86 uint64_t Case1_conf::TRAFFICPKTNUM=10000000;
87
88 double Case1_conf::SIM_STOP_Sec=200;
89
90 double Case1_conf::ROWFORAVGQLENGTH=0.1;
91

```



## A-7: End to End Average Delay Analysis Code

```
1  #include <iostream>
2  #include <fstream>
3  #include <string>
4  #include <stdint.h>
5  #include <sstream>
6  #include <cmath>
7  #include <map>
8  #include <iterator>
9  #include <vector>
10 using namespace std;
11
12
13 int main()
14 {
15     //Use g++ -std=c++14 -Wall -Wextra -pedantic-errors -O3 -o ETE ETE.cpp
16     //O3 - optimise
17     //std=c++14 - the current standard for C++
18     //pedantic-errors - the International Standard, not your home grown GNU dialect
19     // to run the program ./ETE
20
21
22     std::map<int,double_t> Pkt_arrivalmap; // identify the map that contain the arrival information of packets.
23     std::map<int,double_t> SC1Pkt_arrivalmap;
24     std::map<int,double_t> SC2Pkt_arrivalmap;
25     std::map<int,double_t> SC3Pkt_arrivalmap;
26     std::map<int,double_t> SC4Pkt_arrivalmap;
27
28     std::map<int,double_t> Pkt_creatingmap; // identify the map that contain the creating information of packets.
29     std::map<int,double_t> SC1Pkt_creatingmap;
30     std::map<int,double_t> SC2Pkt_creatingmap;
31     std::map<int,double_t> SC3Pkt_creatingmap;
32     std::map<int,double_t> SC4Pkt_creatingmap;
33
34     std::vector<double_t> TotalTrafficETED;
35     std::vector<double_t> SC1TrafficETED;
36     std::vector<double_t> SC2TrafficETED;
37     std::vector<double_t> SC3TrafficETED;
38     std::vector<double_t> SC4TrafficETED;
39
40     double_t ETEDforeachclass(int i);
41
42     int pos,pos2,pos3;
43     double_t TTETED=0;
44     double_t SC1TETED=0;
45     double_t SC2TETED=0;
46     double_t SC3TETED=0;
47     double_t SC4TETED=0;
48
49     string line,search,word1="ns",word2="Pkt_create", word3="Pkt_rcv"; // create an object from string use to read each line in file.
50     ifstream inf; // create an object from class of input file stream for reading only
51
52     //// to store columns values of 123.txt file (receiving) into variables, then store variables in container arrival map:
53     // search each line of file 123.txt on Pkt_create and print this line (okay)
54
55     inf.open ("123.txt"); // open file "123.txt" for reading
56     if (inf.is_open()) // condition to check if it is possible to open file or not. (if it exist), then open it and do following process.
```

```

57 {
58
59     while (getline(inf,line)) // loop continue until reaching the end of file.
60     {
61
62         if (line.find("Pkt_rcv") != string::npos) // search on "Pkt_rcv" each
63         {
64             line
65             pos=line.find(word1); // take the length of "ns" in string line.
66             pos3=line.find(word3); // take the length of "Pkt_rcv" in string line.
67             line.replace(pos,word1.length()," "); // to remove "ns"
68             line.replace(pos3,word3.length()," "); // to remove "Pkt_rcv"
69             istringstream iss (line);
70             istringstream iss1 (line);
71             double_t column1;
72             double_t column3;
73             double_t column2;
74             iss >> column1 >> column2;
75             //cout << column1 <<" "<< column2 <<" "<< endl;
76             Pkt_arrivalmap[column1]=column2;
77             //cout << column1 <<" "<< column2 <<" "<< endl;
78             iss1 >> column1 >> column2 >> column3;
79             //cout << column1 <<" "<< column2 <<" "<< column3 <<" "<< endl;
80             if (column3==1)
81             {
82                 SC1Pkt_arrivalmap[column1]=column2;
83             }
84             else if (column3==2)
85             {
86                 SC2Pkt_arrivalmap[column1]=column2;
87             }
88             else if (column3==3)
89             {
90                 SC3Pkt_arrivalmap[column1]=column2;
91             }
92             else
93             {
94                 SC4Pkt_arrivalmap[column1]=column2;
95             }
96         }
97     }
98 }
99
100 }
101
102
103 inf.close();
104 }
105
106 else cout << "Unable to open file inf \n";
107
108 // to store columns values of 123.txt file (creating) into variables, then store
109 // variables in container creatingmap:
110 // search each line of file 123.txt on Pkt_create and print this line (okay)
111
112 inf.open ("123.txt"); // open file "123.txt" for reading
113 if (inf.is_open()) // condition to check if it is possible to open file or not.
114 {
115     (if it exist), then open it and do following prcess.
116 }

```

```

115 while (getline(inf,line)) // loop continue until reaching the end of file.
116 {
117
118     if (line.find("Pkt_create") != string::npos) // search on "Pkt_create"
119     {
120         pos=line.find(word1); // take the length of "ns" in string line.
121         pos2=line.find(word2); // take the length of "Pkt_create" in string line.
122         line.replace(pos,word1.length()," "); // to remove "ns"
123         line.replace(pos2,word2.length()," "); // to remove "Pkt_create"
124         istringstream iss (line);
125         istringstream iss1 (line);
126         double_t column1;
127         double_t column3;
128         double_t column2;
129         iss >> column1 >> column2;
130         //cout << column1 << " " << column2 << " " << " " << endl;
131         Pkt_creatingmap[column1]=column2;
132         iss1 >> column1 >> column2 >> column3;
133         //cout << column1 << " " << column2 << " " << column3 << " " << endl;
134         if (column3==1)
135         {
136             SC1Pkt_creatingmap[column1]=column2;
137         }
138         else if (column3==2)
139         {
140             SC2Pkt_creatingmap[column1]=column2;
141         }
142         else if (column3==3)
143         {
144             SC3Pkt_creatingmap[column1]=column2;
145         }
146
147         else
148
149             SC4Pkt_creatingmap[column1]=column2;
150
151     }
152 }
153
154
155 inf.close();
156 }
157 else cout << "Unable to open file inf \n";
158
159
160 //////////////////////////////////////////////////// For total service classes traffic
161 auto Pkt_arrivalmap_it = Pkt_arrivalmap.begin();
162 auto Pkt_arrivalmap_end = Pkt_arrivalmap.end();
163 const int lowest_key=Pkt_arrivalmap_it->first;
164 auto Pkt_creatingmap_it = Pkt_creatingmap.lower_bound(lowest_key);
165 auto Pkt_creatingmap_end = Pkt_creatingmap.end();
166
167 while (Pkt_arrivalmap_it != Pkt_arrivalmap_end && Pkt_creatingmap_it !=
168 Pkt_creatingmap_end)
169 {
170     if (Pkt_arrivalmap_it ->first < Pkt_creatingmap_it ->first)
171         ++ Pkt_arrivalmap_it;
172     else
173         if (Pkt_arrivalmap_it ->first == Pkt_creatingmap_it ->first)
174         {

```

```

174         TotalTrafficETED.push_back(Pkt_arrivalmap_it ->second - Pkt_creatingmap_it  2
        ->second);
175         Pkt_arrivalmap_it=Pkt_arrivalmap.erase(Pkt_arrivalmap_it);
176         Pkt_creatingmap_it = Pkt_creatingmap.erase(Pkt_creatingmap_it);
177     }
178     else ++Pkt_creatingmap_it;
179
180 }
181
182
183 for (std::vector<double_t>::iterator it = TotalTrafficETED.begin(); it !=  2
TotalTrafficETED.end(); it++)
184 {
185     TTETED+=(*it/TotalTrafficETED.size())/1000000000;
186 }
187     cout << "Total Traffic ETED" << " " << TTETED << " " <<"Sec."<< endl;
188
189     //////////////////////////////////// For SC1 traffic
190
191     auto SC1Pkt_arrivalmap_it = SC1Pkt_arrivalmap.begin();
192     auto SC1Pkt_arrivalmap_end = SC1Pkt_arrivalmap.end();
193     const int SC1lowest_key=SC1Pkt_arrivalmap_it->first;
194     auto SC1Pkt_creatingmap_it = SC1Pkt_creatingmap.lower_bound(SC1lowest_key);
195     auto SC1Pkt_creatingmap_end = SC1Pkt_creatingmap.end();
196
197     while (SC1Pkt_arrivalmap_it != SC1Pkt_arrivalmap_end && SC1Pkt_creatingmap_it !=  2
SC1Pkt_creatingmap_end)
198     {
199         if (SC1Pkt_arrivalmap_it ->first < SC1Pkt_creatingmap_it ->first)
200             ++ SC1Pkt_arrivalmap_it;
201         else
202             if (SC1Pkt_arrivalmap_it ->first == SC1Pkt_creatingmap_it ->first)
203             {
204                 SC1TrafficETED.push_back(SC1Pkt_arrivalmap_it ->second -  2
SC1Pkt_creatingmap_it ->second);
205                 SC1Pkt_arrivalmap_it=SC1Pkt_arrivalmap.erase(SC1Pkt_arrivalmap_it);
206                 SC1Pkt_creatingmap_it = SC1Pkt_creatingmap.erase(SC1Pkt_creatingmap_it);
207             }
208             else ++SC1Pkt_creatingmap_it;
209     }
210 }
211
212 for (std::vector<double_t>::iterator it = SC1TrafficETED.begin(); it !=  2
SC1TrafficETED.end(); it++)
213 {
214     SC1TETED+=(*it/SC1TrafficETED.size())/1000000000;
215 }
216     cout << "SC1 Traffic ETED" << " " << SC1TETED << " " <<"Sec."<< endl;
217
218
219
220     //////////////////////////////////// For SC2 traffic
221
222     auto SC2Pkt_arrivalmap_it = SC2Pkt_arrivalmap.begin();
223     auto SC2Pkt_arrivalmap_end = SC2Pkt_arrivalmap.end();
224     const int SC2lowest_key=SC2Pkt_arrivalmap_it->first;
225     auto SC2Pkt_creatingmap_it = SC2Pkt_creatingmap.lower_bound(SC2lowest_key);
226     auto SC2Pkt_creatingmap_end = SC2Pkt_creatingmap.end();
227
228     while (SC2Pkt_arrivalmap_it != SC2Pkt_arrivalmap_end && SC2Pkt_creatingmap_it !=  2
SC2Pkt_creatingmap_end)

```

```

229 {
230     if (SC2Pkt_arrivalmap_it ->first < SC2Pkt_creatingmap_it ->first)
231         ++ SC2Pkt_arrivalmap_it;
232     else
233         if (SC2Pkt_arrivalmap_it ->first == SC2Pkt_creatingmap_it ->first)
234         {
235             SC2TrafficETED.push_back(SC2Pkt_arrivalmap_it ->second -
236                                     SC2Pkt_creatingmap_it ->second);
237             SC2Pkt_arrivalmap_it=SC2Pkt_arrivalmap.erase(SC2Pkt_arrivalmap_it);
238             SC2Pkt_creatingmap_it = SC2Pkt_creatingmap.erase(SC2Pkt_creatingmap_it);
239         }
240         else ++SC2Pkt_creatingmap_it;
241     }
242
243     for (std::vector<double_t>::iterator it = SC2TrafficETED.begin(); it !=
244          SC2TrafficETED.end(); it++)
245     {
246         SC2TETED+=(*it/SC2TrafficETED.size())/1000000000;
247     }
248     cout << "SC2 Traffic ETED" << " " << SC2TETED << " " << "Sec." << endl;
249
250     //////////////////////////////////////////////////// For SC3 traffic
251     auto SC3Pkt_arrivalmap_it = SC3Pkt_arrivalmap.begin();
252     auto SC3Pkt_arrivalmap_end = SC3Pkt_arrivalmap.end();
253     const int SC3lowest_key=SC3Pkt_arrivalmap_it->first;
254     auto SC3Pkt_creatingmap_it = SC3Pkt_creatingmap.lower_bound(SC3lowest_key);
255     auto SC3Pkt_creatingmap_end = SC3Pkt_creatingmap.end();
256
257     while (SC3Pkt_arrivalmap_it != SC3Pkt_arrivalmap_end && SC3Pkt_creatingmap_it !=
258            SC3Pkt_creatingmap_end)
259     {
260         if (SC3Pkt_arrivalmap_it ->first < SC3Pkt_creatingmap_it ->first)
261             ++ SC3Pkt_arrivalmap_it;
262         else
263             if (SC3Pkt_arrivalmap_it ->first == SC3Pkt_creatingmap_it ->first)
264             {
265                 SC3TrafficETED.push_back(SC3Pkt_arrivalmap_it ->second -
266                                         SC3Pkt_creatingmap_it ->second);
267                 SC3Pkt_arrivalmap_it=SC3Pkt_arrivalmap.erase(SC3Pkt_arrivalmap_it);
268                 SC3Pkt_creatingmap_it = SC3Pkt_creatingmap.erase(SC3Pkt_creatingmap_it);
269             }
270             else ++SC3Pkt_creatingmap_it;
271     }
272
273     for (std::vector<double_t>::iterator it = SC3TrafficETED.begin(); it !=
274          SC3TrafficETED.end(); it++)
275     {
276         SC3TETED+=(*it/SC3TrafficETED.size())/1000000000;
277     }
278     cout << "SC3 Traffic ETED" << " " << SC3TETED << " " << "Sec." << endl;
279
280     //////////////////////////////////////////////////// For SC4 traffic
281     auto SC4Pkt_arrivalmap_it = SC4Pkt_arrivalmap.begin();
282     auto SC4Pkt_arrivalmap_end = SC4Pkt_arrivalmap.end();
283     const int SC4lowest_key=SC4Pkt_arrivalmap_it->first;
284     auto SC4Pkt_creatingmap_it = SC4Pkt_creatingmap.lower_bound(SC4lowest_key);
285     auto SC4Pkt_creatingmap_end = SC4Pkt_creatingmap.end();

```



```

285
286 while (SC4Pkt_arrivalmap_it != SC4Pkt_arrivalmap_end && SC4Pkt_creatingmap_it != SC4Pkt_creatingmap_end)
287 {
288     if (SC4Pkt_arrivalmap_it ->first < SC4Pkt_creatingmap_it ->first)
289         ++ SC4Pkt_arrivalmap_it;
290     else
291         if (SC4Pkt_arrivalmap_it ->first == SC4Pkt_creatingmap_it ->first)
292         {
293             SC4TrafficETED.push_back(SC4Pkt_arrivalmap_it ->second - SC4Pkt_creatingmap_it ->second);
294             SC4Pkt_arrivalmap_it=SC4Pkt_arrivalmap.erase(SC4Pkt_arrivalmap_it);
295             SC4Pkt_creatingmap_it = SC4Pkt_creatingmap.erase(SC4Pkt_creatingmap_it);
296         }
297         else ++SC4Pkt_creatingmap_it;
298     }
299 }
300
301 for (std::vector<double_t>::iterator it = SC4TrafficETED.begin(); it != SC4TrafficETED.end(); it++)
302 {
303     SC4TETED+=(*it/SC4TrafficETED.size())/1000000000;
304 }
305     cout << "SC4 Traffic ETED" << " " << SC4TETED << " " << "Sec." << endl;
306
307
308     cout << "=====" << endl;
309     cout << "Total number of arrived packets" << " " << TotalTrafficETED.size() << " " << "Packets" << endl;
310     cout << "Total number of SC1 arrived packets" << " " << SC1TrafficETED.size() << " " << "Packets" << endl;
311     cout << "Total number of SC2 arrived packets" << " " << SC2TrafficETED.size() << " " << "Packets" << endl;
312     cout << "Total number of SC3 arrived packets" << " " << SC3TrafficETED.size() << " " << "Packets" << endl;
313     cout << "Total number of SC4 arrived packets" << " " << SC4TrafficETED.size() << " " << "Packets" << endl;
314
315
316
317
318
319     return 0;
320
321
322
323 }
324
325

```



## A-8: Output file Analysis Code

```
//run the simulation
NS_LOG="RDWQueue" ./waf --run scratch/Casel-sim > 123.txt 2>&1
=====
//display the result
less 123.txt
=====
//number of packets from each source
for ((i=1;i<5;i++)); do echo "SC"$i "${cat 123.txt | grep create | awk -v b=$i '{if($4==b){print $0}}' | wc -l}"; done
or
less 123.txt | grep "pkt size" | awk '{print $1}' | sort | uniq -c .
or
less 123.txt | grep "pkt size" | awk '{print $6}' | sort | uniq -c
=====
//total number of packets
less 123.txt | grep "pkt size" | wc -l
=====
//schedule rate
To get the average queue scheduling rate within an update interval
less 123.txt | grep "Queue Scheduling Rate Report 'Queue ID'" | awk '{print $1" "$6" "$7" "$8"
"$9" "$10" "$11" "$12" "$13}' | less

or

to display the scheduling rate for service class queues and current usage, the first three values
represent the scheduling rates and the last three values represent the current usage.
less 123.txt | grep "CalculateSchedulingRate 'Queue ID'" | less
=====
//congestion level alpha (DWFQ algorithm)
less 123.txt | grep "ByFactor" | less

To display the congestion level in SC1
less 123.txt | grep "ByFactor case" | grep "'Queue ID' 1" | less

less 123.txt | grep "ByFactor case 1 'Queue ID' 1" | less
The first number represent the case, the second represent the queue ID that reduce its rate and the
third number represent the service class.

To display the congestion level in SC2
less 123.txt | grep "ByFactor case" | grep "'Queue ID' 2" | less
The first number represent the case, the second represent the queue ID that reduce its rate and the
third number represent the service class.

To display the congestion level in SC3
less 123.txt | grep "ByFactor case" | grep "'Queue ID' 3" | less

less 123.txt | grep "ByFactor case 1 'Queue ID' 3" | less
The first number represent the case, the second represent the queue ID that reduce its rate and the
third number represent the service class.

or

To display SC1 congestion level only (in case of using DWFQ algorithm):
less 123.txt | grep "ByFactor case 1 'Queue ID' 1" | less
less 123.txt | grep "ByFactor case 2 'Queue ID' 1" | less
less 123.txt | grep "ByFactor case 3 'Queue ID' 1" | less

To display SC2 congestion and cases of beta:
less 123.txt | grep "ByFactor case 1 'Queue ID' 2" | less
less 123.txt | grep "ByFactor case 2 'Queue ID' 2" | less
less 123.txt | grep "ByFactor case 3 'Queue ID' 2" | less

To display SC3 congestion and cases of beta:
less 123.txt | grep "ByFactor case 1 'Queue ID' 3" | less
less 123.txt | grep "ByFactor case 2 'Queue ID' 3" | less
less 123.txt | grep "ByFactor case 3 'Queue ID' 3" | less
=====
for any scenairo, the congestion level and beta values (in case of using DRAM-NFV)
less 123.txt | grep "ByLinearFactor case 3 'Queue ID' 1" | less
less 123.txt | grep "ByLinearFactor case 3 'Queue ID' 2" | less
less 123.txt | grep "ByLinearFactor case 3 'Queue ID' 3" | less

less 123.txt | grep "ByLinearFactor case 2 'Queue ID' 1" | less
```

```

less 123.txt | grep "ByLinearFactor case 2 'Queue ID' 2" | less
less 123.txt | grep "ByLinearFactor case 2 'Queue ID' 3" | less

less 123.txt | grep "ByLinearFactor case 1 'Queue ID' 1" | less
less 123.txt | grep "ByLinearFactor case 1 'Queue ID' 2" | less
less 123.txt | grep "ByLinearFactor case 1 'Queue ID' 3" | less
=====

//Display scheduling rates, GCD and bucket values for a queue

To display the scheduling rate only at specific queue
less 123.txt | grep "NEW SCHEDULE RATE VALUE 'Queue ID'" | less
or
less 123.txt | grep "CalculateSchedulingRate 'Queue ID'" | less
The first three values represent the scheduling rates

To display the GCD and buckets values at specific queue
less 123.txt | grep "NEW SCHEDULE RATE BUCKETS 'Queue ID'" | less

=====
// Display normalised values only for a queue

To display the service classes normalised values for specific queue
less 123.txt | grep "NEW SCHEDULE RATE NORMALIZED VALUE 'Queue ID'" | grep -v 'nan' | less
=====
Delay of all service classes at a queue

To get the average queue delay in MilliSecond within an update interval k
less 123.txt | grep "Average Queue Delay Report 'Queue ID'" | awk '{print $1" "$6" "$7" "$8" "$9"
"$10" "$11" "$12" "$13}' | less

To get the average queue delay in Second within an update interval k
less 123.txt | grep "Average Queue Delay Report 'Queue ID'" | awk '{print $1" "$6" "$7" "$8" "$9"
"$10" "$11/1000" "$12/1000" "$13/1000}' | less

or

To calculate the average service class queue delay in sec (sclavgdelay) // within an update time
interval k
less 123.txt | grep "QueueReport 'Queue ID'" | awk '{print $1" "$8/1000" "$9/1000" "$10/1000}' |
grep -v 'nan' | less

=====
To get the average queue length, instant queue length and previous average queue length for a specific
service class queue within an update interval k

The first value represent the instant, the second represent the previous average and the third
represent the average queue length

less 123.txt | grep "SC1 Average Queue Length 'Queue ID'" | awk '{print $1" "$6" "$10" "$11"
"$14" "$15" "$17" "$19" "$20" "$22" "$23}' | less

less 123.txt | grep "SC2 Average Queue Length 'Queue ID'" | awk '{print $1" "$6" "$10" "$11"
"$14" "$15" "$17" "$19" "$20" "$22" "$23}' | less

less 123.txt | grep "SC3 Average Queue Length 'Queue ID'" | awk '{print $1" "$6" "$10" "$11"
"$14" "$15" "$17" "$19" "$20" "$22" "$23}' | less

To get the average queue length within an update interval k For all classes in a specific queue
less 123.txt | grep "Average Queue Length Report 'Queue ID'" | awk '{print $1" "$6" "$7" "$8"
"$9" "$10" "$11" "$12" "$13}' | less
=====
Number of dropped packet of SC1 at a queue
// Lower
less 123.txt | grep "'Queue ID' SC1 Lthreshold Drop" | less | tail -1 | awk '{print $1" "$6" "$7"
"$8" "$9" "$10}' | less
// Higher
less 123.txt | grep "'Queue ID' SC1 Hthreshold Drop" | less | tail -1 | awk '{print $1" "$6" "$7"
"$8" "$9" "$10}' | less
// overload
less 123.txt | grep "'Queue ID' SC1 Buffer Drop" | less | tail -1 | awk '{print $1" "$6" "$7"
"$8" "$9" "$10}' | less

```

```

Number of dropped packet of SC2 at a queue
// Lower
less 123.txt | grep "'Queue ID' SC2 Lthreshold Drop" | less | tail -1 | awk '{print $1" "$6" "$7"
"$8" "$9" "$10}' | less
// Higher
less 123.txt | grep "'Queue ID' SC2 Hthreshold Drop" | less | tail -1 | awk '{print $1" "$6" "$7"
"$8" "$9" "$10}' | less
// overload
less 123.txt | grep "'Queue ID' SC2 Buffer Drop" | less | tail -1 | awk '{print $1" "$6" "$7"
"$8" "$9" "$10}' | less

Number of dropped packet of SC3 at a queue
// Lower
less 123.txt | grep "'Queue ID' SC3 Lthreshold Drop" | less | tail -1 | awk '{print $1" "$6" "$7"
"$8" "$9" "$10}' | less
// Higher
less 123.txt | grep "'Queue ID' SC3 Hthreshold Drop" | less | tail -1 | awk '{print $1" "$6" "$7"
"$8" "$9" "$10}' | less
// overload
less 123.txt | grep "'Queue ID' SC3 Buffer Drop" | less | tail -1 | awk '{print $1" "$6" "$7"
"$8" "$9" "$10}' | less
=====
// Total number of dropped packets for each drop case for whole simulation
less 123.txt | grep -i drop | awk '{print $7" "$8" "$9}' | sort | uniq -c
or
for ((i=1;i<4;i++)); do echo SC$i; less 123.txt | grep -i drop | grep SC$i | awk '{print $8" "$9}' |
sort | uniq -c; done

// Total number of dropped packets for each service class queue for whole simulation
less 123.txt | grep -i drop | awk '{print $7}' | sort | uniq -c
=====
// total number of dropped packet at a specific queue and for each case
less 123.txt | grep "'Queue ID' " | grep -i drop | awk '{print $7" "$8" "$9}' | sort | uniq -c

// total number of dropped packets at a specific queue for each service class
less 123.txt | grep "'Queue ID' " | grep -i drop | awk '{print $7}' | sort | uniq -c
=====
//Utilization
For a specific queue:
less 123.txt | grep "LinkUtilization in second for output port: 'Queue ID'" | awk '{print
$1/1000000000000" "$2" "$3" "$4" "$5" "$6" "$7" "$8" "$9" "$10" "$11" "$12" "$13" "$14*100" "$15"
"$16*100" "$17" "$18*100" "$19" "$20*100}' | less
=====

// For scenarios 2 and 3 only:

// To get the average queue delay of a sub-queue at queues (268,269 and 2610) or
(3810,3811,3812,3910,3911 and 3912)
// for SC1
less 123.txt | grep "SC1GetAverageQueueDelay" | less // in msec.
or
less 123.txt | grep "SC1UpdateAverageQueueDelay" | less // in msec.
// for SC2
less 123.txt | grep "SC2GetAverageQueueDelay" | less
or
less 123.txt | grep "SC2UpdateAverageQueueDelay" | less
// for SC3
less 123.txt | grep "SC3GetAverageQueueDelay" | less
or
less 123.txt | grep "SC3UpdateAverageQueueDelay" | less

=====
// To get the average queue length of sub queue at queues 268,269 and 2610 or
(3810,3811,3812,3910,3911 and 3912)
// for SC1
less 123.txt | grep "SC1GetAverageQueueLength" | less
or
less 123.txt | grep "SC1UpdateAverageQueueLength" | less
// for SC2
less 123.txt | grep "SC2GetAverageQueueLength" | less
or
less 123.txt | grep "SC2UpdateAverageQueueLength" | less
// for SC3
less 123.txt | grep "SC3GetAverageQueueLength" | less

```

```

or
less 123.txt | grep "SC3UpdateAverageQueueLength" | less
=====
// To get the equivalent Minimum average sub queue delay for all classes in queue 268 or 269 or 2610
or 3810 or 3811 or 3812 or 3910 or 3911 or 3912.
for each mentioned queue:
less 123.txt | grep "Average Delay Values 'X'" | less //in msec.
X is either 268 or 269 or 2610 or 3810 or 3811 or 3812 or 3910 or 3911 or 3912.
=====
// To get the equivalent Maximum average sub queue length for all classes in queue 268 or 269 or 2610
or 3810 or 3811 or 3812 or 3910 or 3911 or 3912.
for each mentioned queue:
less 123.txt | grep "Average Length Values 'X'" | less
X is either 268 or 269 or 2610 or 3810 or 3811 or 3812 or 3910 or 3911 or 3912.
=====
// To get the average service class queue delay at a specific queue (268 or 269 or 2610 or 3810 or
3811 or 3812 or 3910 or 3911 or 3912) and the equivalent Minimum average delay value.
less 123.txt | grep "delay'X':" | less // in msec.
X is either 268 or 269 or 2610 or 3810 or 3811 or 3812 or 3910 or 3911 or 3912.
=====
// To get the average service class queue length at a specific queue (268 or 269 or 2610 or 3810 or
3811 or 3812 or 3910 or 3911 or 3912) and the equivalent Maximum average length value.
less 123.txt | grep "length'X':" | less
=====
// To get the equivalent average of average scheduling rates for service class queues at queues
(268,269 and 2610) or (3810,3811,3812,3910,3911 and 3912)
less 123.txt | grep "CalculateSchedulingRateByAverage" | less
=====
// To get the equivalent Minimum average delay queue for service class queues at queues 268 or 269 or
2610 or (3810,3811,3812,3910,3911 and 3912)
less 123.txt | grep "Average Delay Values " | less // in msec.
=====
// To get the equivalent Maximum average length queue for service class queues at queues 268 or 269 or
2610 or (3810,3811,3812,3910,3911 and 3912)
less 123.txt | grep "Average Length Values " | less
=====

```

**A-9: Conferences and Training Courses.**

No.	preliminary study and Training courses	Date
1	Introduction to Endnote X7	9 <sup>th</sup> of December 2013
2	Qualitative research tools: Nvivo. Day 1 introduction to Nvivo.	11 <sup>th</sup> of February 2014
3	Doing a literature review.	19 <sup>th</sup> of February 2014
4	Postgraduate students: Enterprise futures conference.	29 <sup>th</sup> of May 2014
5	Attending NTAD MSC module (Network, technology, design and architecture).	1st semester 2013/2014
6	Attending MSc Module NPS (Network programming and simulation).	2nd semester 2013/2014
7	Attending PGR Conference 2014-Salford University.	22 <sup>nd</sup> -23 <sup>rd</sup> of January 2014
8	Submitted a poster and abstract in the College Dean's Annual Research Showcase Event.	18 <sup>th</sup> of June 2014
9	Attending symposium (PGNET2014, Liverpool).	23 <sup>rd</sup> -24 <sup>th</sup> of June 2014
10	LEAP Higher - Academic Writing in English as a Second Language.	24 <sup>th</sup> of Nov, 1 <sup>st</sup> of Dec, 8 <sup>th</sup> of Dec. and 15 <sup>th</sup> of Dec. 2014
11	How to get Published with IEEE.	17 <sup>th</sup> of Feb 2015.
12	IP EXPO 2015 – Manchester.	20 <sup>th</sup> and 21 <sup>st</sup> of May 2015.
13	Submitted a poster in SPARC/Dean's Annual Research Showcase – Salford University.	26 <sup>th</sup> to 28 <sup>th</sup> of May 2015.
14	Attended GTS workshops (Inclusive teaching & classroom management) Assessment and Feedback, Salford University.	13 <sup>th</sup> of January 2017
15	Submitting a paper to IFIP Networking 2017 conference sponsored by IEEE Computer Society, Stockholm – Sweden.	12 <sup>th</sup> -16 <sup>th</sup> June 2017
16	Participating in the University of Salford conference (SPARC 2017)	27 <sup>th</sup> -29 <sup>th</sup> June 2017